

HZM6.8Z4MWA

Silicon Planar Zener Diode for Surge Absorb

REJ03G0369-0100

Rev.1.00

Oct 01, 2004

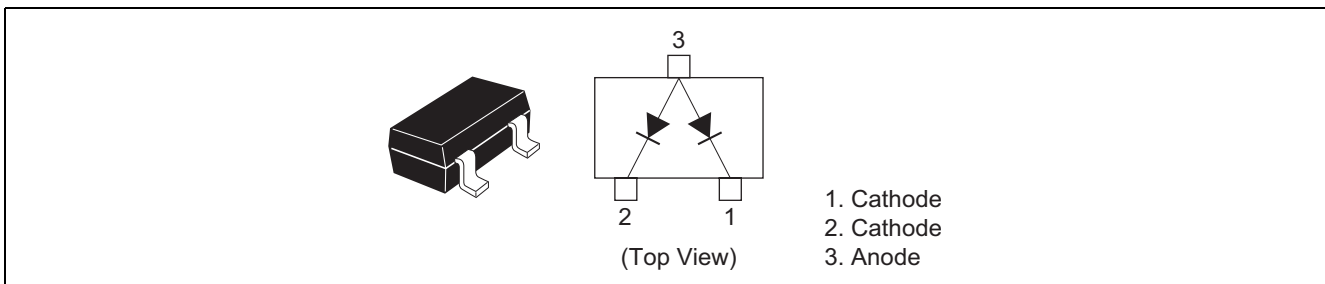
Features

- HZM6.8Z4MWA has two devices in a monolithic, and can absorb surge.
- Low capacitance ($C = 4.0 \text{ pF Typ} / 4.5 \text{ pF max}$) and can protect ESD of signal line.
- MPAK Package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HZM6.8Z4MWA	N2	MPAK

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Power dissipation	Pd *	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: two device total, See Fig.2.

Electrical Characteristics *1

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Zener voltage	Vz	6.47	—	7.00	V	Iz = 5 mA, 40 ms pulse
Reverse current	IR	—	—	2	μA	VR = 3.5 V
Capacitance	C	—	4.0	4.5	pF	VR = 0 V, f = 1 MHz
Dynamic resistance	rd	—	—	30	Ω	Iz = 5 mA
ESD-Capability *2	—	8	—	—	kV	C = 150 pF, R = 330 Ω, Both forward and reverse direction 10 pulse

Notes: 1. Per one device.

2. Failure criterion ; IR > 2 μA at VR = 3.5 V.

Main Characteristic

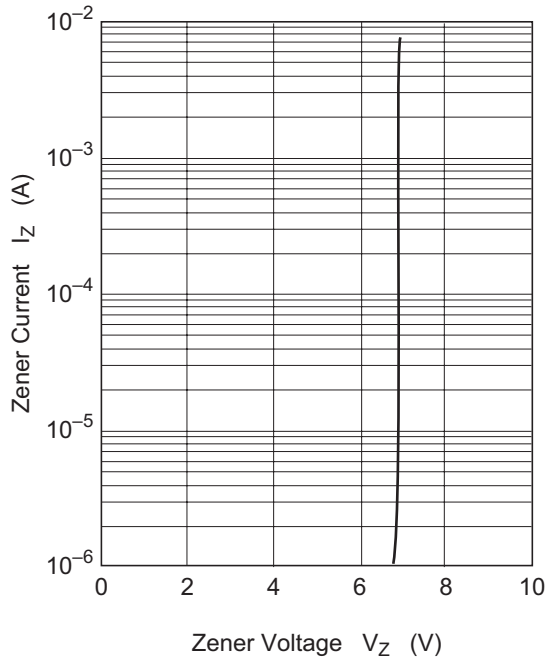


Fig.1 Zener current vs. Zener voltage

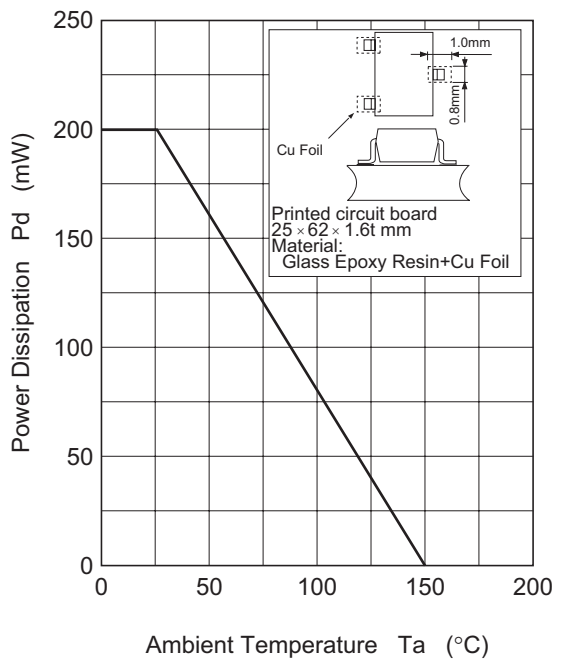


Fig.2 Power Dissipation vs. Ambient Temperature

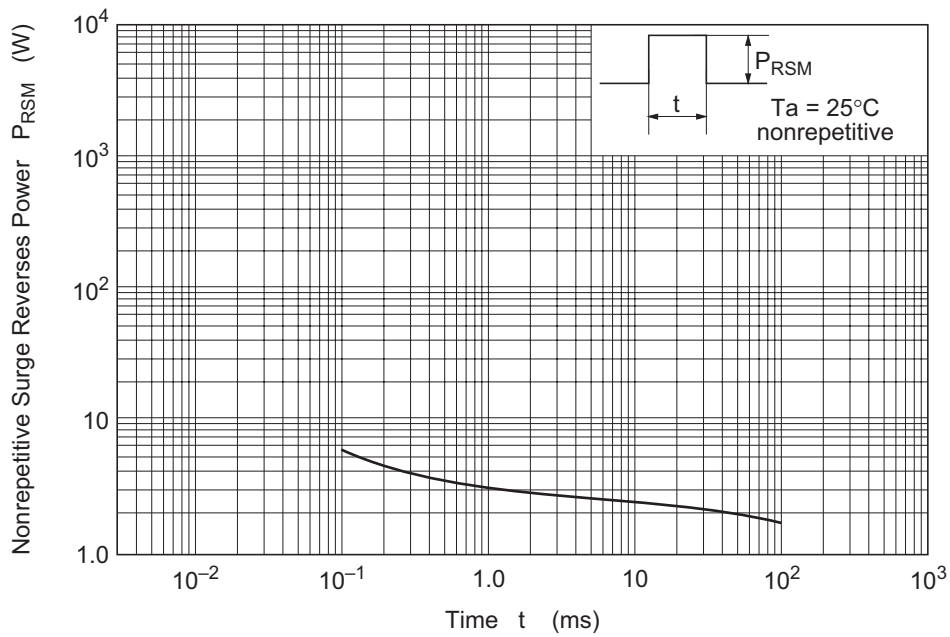
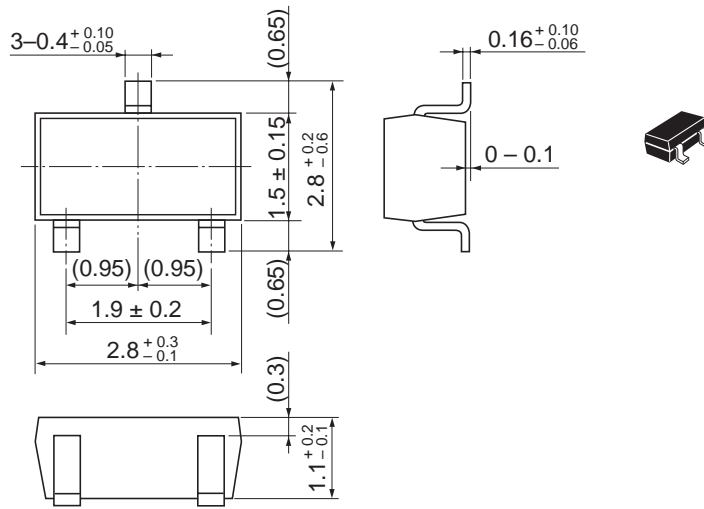


Fig.3 Surge Reverse Power Ratings

Package Dimensions

As of January, 2003
Unit: mm



Package Code	MPAK
JEDEC	—
JEITA	Conforms
Mass (reference value)	0.011 g

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