TOSHIBA Diode Silicon Epitaxial Planar Type

# **JDV2S19S**

#### VCO for the UHF band

• High capacitance ratio:  $C_{1V}/C_{4V} = 1.8$  (typ.)

Low series resistance: r<sub>S</sub> = 0.35 Ω (typ.)

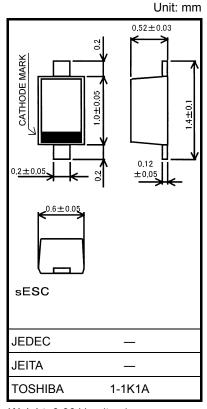
• This device is suitable for use in a small-size tuner.

## Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Reverse voltage	$V_{R}$	10	٧
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	-55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Weight: 0.0011 g (typ.)

#### **Electrical Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse voltage	$V_{R}$	$I_R = 1 \mu A$	10	_	_	V
Reverse current	I <sub>R</sub>	V <sub>R</sub> = 10 V	_	_	3	nA
Capacitance	C <sub>1V</sub>	V <sub>R</sub> = 1 V, f = 1 MHz	3.46	_	3.87	pF
	C <sub>4V</sub>	V <sub>R</sub> = 4 V, f = 1 MHz	1.83	_	2.13	
Capacitance ratio	C <sub>1V</sub> /C <sub>4V</sub>	_	1.74	_	1.91	_
Series resistance	r <sub>S</sub>	V <sub>R</sub> = 1 V, f = 470 MHz	_	0.35	0.47	Ω

Note: Signal level when capacitance is measured:  $V_{sig} = 100 \text{ mVrms}$ 

#### Marking



### **RESTRICTIONS ON PRODUCT USE**

20070701-EN GENERAL

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