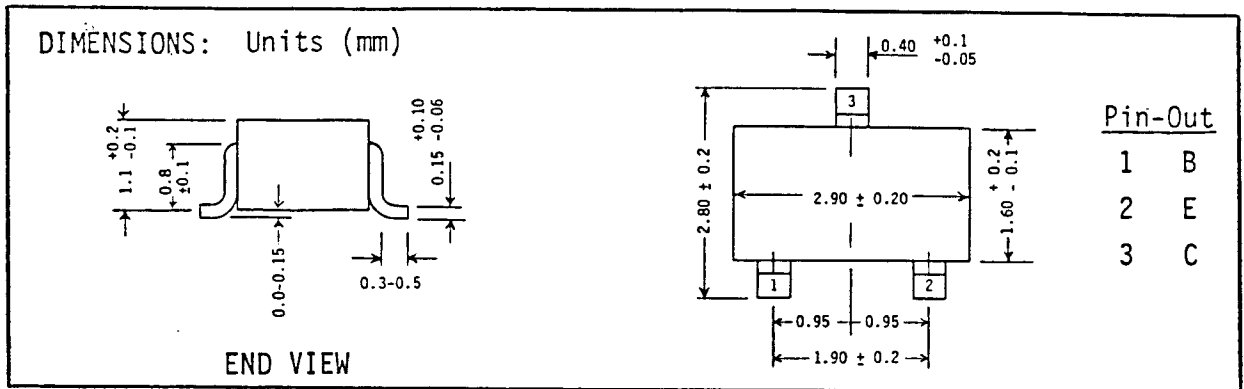


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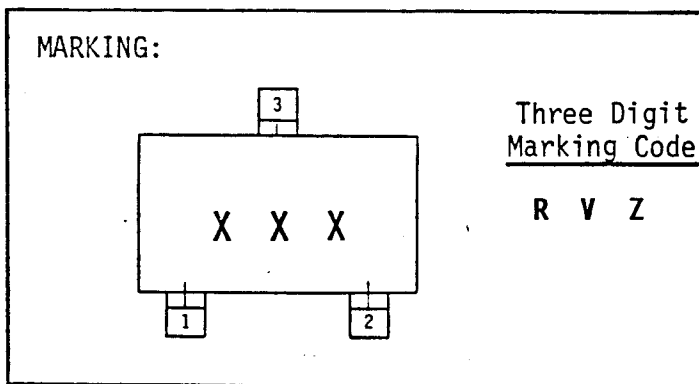
SUBJECT
SOT-23 TRANSISTOR, PNP, SILICON

ABSOLUTE MAXIMUM RATINGS: (Ta = 25°C)

Collector-Base Voltage	V _{CB0}	25 V
Collector-Emitter Voltage	V _{CE0}	25 V
Emitter-Base Voltage	V _{EB0}	4 V
Collector Current	I _C	200 mA
Power Dissipation-Free Air	P _D	200 mW
Power Dissipation-Ceramic Substrate	P _D	350 mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-55 to 150 °C
Solder Temperature (10 seconds)		260 °C



THE JAPANESE STYLE SC-59 PACKAGE



PACKAGING:

_____	BULK, 500 per BAG
_____	MAGAZINES OF 50 EACH
_____	8mm T&R, T- 146 3K/REEL
_____	8mm T&R, T- 147 3K/REEL
_____	8mm T&R, T- 246 10K/REEL
_____	8mm T&R, T- 247 10K/REEL

REMARKS: PROCESS: A-38 Thermal Resistance R_{θJA} 625 °C/Watt
FREE AIR, T_A = 25°C

ROHM ELECTRONICS
3034 Owen Drive, Antioch, TN 37013
TEL: (615) 641-2020 FAX: (615) 641-2022

APPROVAL <i>AK</i> 10/12/87	CHECK ←	DESIGN
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MASTER



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SUBJECT TRANSISTOR, PNP ,SILICON SOT-23	DATE January 14, 1987

ELECTRICAL CHARACTERISTICS: (Ta = 25°C Unless Otherwise Specified)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
BV _{CB0}	I _C = 10 μ A	25			V
BV _{CE0}	I _C = 1.0 mA	25			V
BV _{EB0}	I _E = 10 μ A	4			V
I _{CB0}	V _{CB} = 25 V		0.7	100	nA
I _{EB0}	V _{EB} = 4 V		0.7	100	nA
h _{FE}	I _C = 2.0 mA, V _{CE} = 1.0 V	120		360	
h _{FE}	I _C = 50 mA, V _{CE} = 1.0 V	60			
V _{CE(SAT)}	I _C = 50 mA, I _B = 5.0 mA		0.15	0.40	V
V _{BE(SAT)}	I _C = 50 mA, I _B = 5.0 mA		0.85	0.95	V
f _T	I _C = 10 mA, V _{CE} = 20V, f = 100MHz	250	400		MHz
h _{fe}	I _C = 2.0 mA, V _{CE} = 10V, f = 1.0KHz	120		480	
C _{ob}	V _{CB} = 5 V, I _E = 0, f = 100KHz			4.5	pF
C _{ib}	V _{EB} = 0.5 V, I _C = 0, f = 100KHz			10	pF
NF	I _C = 100 μ A, V _{CE} = 5 V, R _s = 1 K Ω Bandwidth = 10 Hz to 15.7KHz			4.0	dB

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