# NEC'S GENERAL PURPOSE 5 V AGC AMPLIFIER

## UPC3221GV

#### **FEATURES**

- ON-CHIP LOW DISTORTION AMPLIFIER: IIP3 = +2.5 dBm at minimum gain
- WIDE AGC DYNAMIC RANGE: GCR = 50 dB TYP
- ON-CHIP VIDEO AMPLIFIER: VOUT = 1.0 VP-P at single-ended output
- SUPPLY VOLTAGE: Vcc = 5 V
- PACKAGED IN 8 PIN SSOP SUITABLE FOR SURFACE MOUNTING
- LOW NOISE FIGURE: 4.2 dB TYP

#### DESCRIPTION

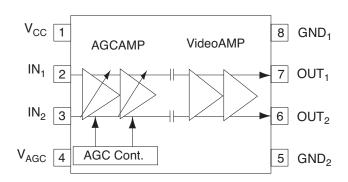
Nec's UPC3221GV is a Silicon Monolithic IC designed for use as an AGC Amplifier for digital CATV, cable modem and IP telephony systems. This IC consists of a two stage gain control amplifier and a fixed gain video amplifier. The device provides a differential input and differential output for noise performance, which eliminates shielding requirements.

The package is 8-pin SSOP (Shrink Small Outline Package) suitable for surface mount.

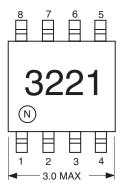
This IC is manufactured using NEC's 10 GHz fT NESAT<sup>™</sup>II AL silicon bipolar process. This process uses silicon nitride passivation film. This material can protect chip surface from external pollution and prevent corrosion/migration. Thus, this IC has excellent performance, uniformity and reliability.

NEC's stringent quality assurance and test procedures ensure the highest reliability and performance.

#### INTERNAL BLOCK DIAGRAM AND PIN CONFIGURATION



#### PACKAGE OUTLINE S08



All dimensions are typical unless specified otherwise.

#### **APPLICATIONS**

- Digital CATV
- · Cable modem receivers
- IP Telephony receivers

#### **ELECTRICAL CHARACTERISTICS**

 $(TA = 25^{\circ}C, Vcc = 5 V, Zs = 1K\Omega, ZL = 1K\Omega, fin = 45 MHz, single-ended output)$ , unless otherwise noted

PART NUMBER					UPC3221GV		
PACKAGE OUTLINE				S08			
SYMBOLS	SYMBOLS PARAMETERS AND CONDITIONS UNITS				MAX		
DC Characte	ristics						
I <sub>CC</sub>	Circuit Current <sup>1</sup> (no input signal)	mA	26	33	41		
I <sub>AGC</sub> (H)	AGC Pin Current <sup>1</sup> , No input Signal, V <sub>AGC</sub> = 3.5 V	V	-	16	50		
V <sub>AGC</sub> (H)	AGC Voltage High Level <sup>1</sup> , at Maximum gain	V	3.0	-	3.5		
V <sub>AGC</sub> (L)	V <sub>AGC</sub> (L) AGC Voltage Low Level <sup>1</sup> , at Minimum gain V			-	0.5		
RF Characte	ristics						
G <sub>MAX</sub>	Maximum Gain <sup>1</sup> , VAGC = 3.0 V, P <sub>in</sub> = -60 dBm	dB	57	60	63		
G <sub>MID</sub> 1	Middle Gain 1 <sup>1</sup> , VAGC = 2.2 V, P <sub>in</sub> = -60 dBm	dB	47.5	50.5	53.5		

## California Eastern Laboratories

#### ELECTRICAL CHARACTERISTICS, cont.

 $(T_A = 25^{\circ}C, V_{CC} = 5 \text{ V}, f_{IN} = 45 \text{ MHz}, Z_S = 50\Omega, Z_L = 250\Omega, single-ended output), unless otherwise noted$ 

PART NUMBER PACKAGE OUTLINE					UPC3221GV S08		
RF Characte	ristics						
G <sub>MID</sub> 2	Middle Gain 2 <sup>1</sup> , VAGC = 1.2 V, PIN = -30 dBm	dB	18	21	24		
G <sub>MIN</sub>	Minimum Gain <sup>1</sup> , VAGC = 0.5 V, PIN = -30 dBm	dB	6	10	14		
G <sub>CRin</sub>	Gain Control Range Input <sup>1</sup> , V <sub>AGC</sub> = 0.5 to 3.0 V	dB	43	50	-		
G <sub>CRout</sub>	Gain Control Range Output <sup>1</sup> , V <sub>out</sub> = 1.0 V <sub>p-p</sub>	dB	36	40	-		
G <sub>slope</sub>	Gain Control Slope <sup>1</sup> , Gain (at V <sub>AGC</sub> = 2.2 V) - Gain (at V <sub>AGC</sub> = 1.2 V)	dB	26.5	29.5	32.5		
V <sub>oclip</sub>	Maximum Output Voltage <sup>1</sup> , V <sub>AGC</sub> = 3.0 V at maximum gain	V <sub>P-P</sub>	2.0	2.8	_		
NF	Noise Figure <sup>3</sup> , V <sub>AGC</sub> = 3.0 V at maximum gain	dB	-	4.2	5.7		
IM <sub>3</sub> 1	Third Order Intermodulation Distortion <sup>1</sup> , $f_{IN1} = 44$ MHz, $f_{IN2} = 45$ MHz, PIN = -30 dBm/tone, Vout = 0.7 V <sub>p-p</sub> /tone at single ended output, Z <sub>L</sub> = 250 $\Omega$	dBc	43	47	_		
IM <sub>3</sub> 2	Third Order Intermodulation Distortion <sup>1</sup> 2, fl <sub>N1</sub> = 44 MHz, f <sub>IN2</sub> = 45 MHz, V <sub>AGC</sub> = 3.0 V at maximum gain, Vout = 0.7 V <sub>p-p</sub> /tone at single ended output, Z <sub>L</sub> = 250 $\Omega$	dBc	50	56	_		
ΔG	Gain <sup>1,2</sup> , $V_{AGC}$ = 3.0 V, $P_{in}$ = -60 dBm, $Z_{L}$ = 250 $\Omega$ , $\Delta G$ = G at $P_{out}$ 1-G at $P_{out}$ 2	dB	-0.5	0	+0.5		

**STANDARD CHARACTERISTICS** (TA = 25°C, Vcc = 5 V, Zs = 50Ω), unless otherwise noted

	PART NUMBER	UPC3221GV			
	PACKAGE OUTLINE		S08		
SYMBOLS	PARAMETERS	UNITS	REFERENCE VALUE		
NF2	GAIN Recuction <sup>3</sup> = -10 dB	dB	6.0		
NF3	GAIN Recuction <sup>3</sup> = -20 dB	dB	9.5		
V <sub>out</sub>	$P_{in}^{1} = -56 \text{ to } -16 \text{ dBm}$	V <sub>p-p</sub>	1.0		
Z <sub>in</sub>	Input Impedance <sup>4</sup> = 0.5 V, f = 45 MHz	Ω	0.9k - j1.4k		
Z <sub>out</sub>	Output Impedance <sup>4</sup> = 0.5 V, f = 45 MHz	Ω	9.0+j1.9		
IIP <sub>3</sub>	3rd Order Input Intercept Point <sup>1</sup> = V <sub>AGC</sub> = 0.5 V at minmum gain, f <sub>1</sub> = 44 MHz, f <sub>2</sub> = 45 MHz, Z <sub>L</sub> = 250 $\Omega$ at single ended output	dBm	+2.5		

Note:

- 1. By measurement Circuit 1
- 2. By measurement Circuit 2
- 3. By measurement Circuit 3
- 4. By measurement Circuit 4

#### ABSOLUTE MAXIMUM RATINGS<sup>1,2</sup>

(TA = 25°C,  $V_{CC}$  = 5 V,  $Z_S$  = 50  $\Omega$ , unless otherwise specified)

SYMBOLS	PARAMETERS	UNITS	RATINGS
Vcc	Voltage Current	V	6.0
V <sub>AGC</sub> (H) AGC Voltage		V	0 to $V_{CC}$
PD	P <sub>D</sub> Power Dissipation <sup>2</sup>		250
Та	Operating Ambient Temp. <sup>1</sup>	°C	-40 to +85
TSTG Operating Ambient Temp. <sup>1</sup>		°C	-55 to +150

#### Notes:

1. Operation in excess of  $% \left( {{{\rm{AN}}}} \right)$  and one of these parameters may result in permanent damage.

2. Mounted on a 50 x 50 x 1.6 mm epoxy glass PWB, with copper patterning on both sides,  $T_{\rm A}$  = 85°C

#### RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER	UNITS	MIN	TYP	MAX
Vcc	Supply Voltage	V	4.5	5.0	5.5
Та	Operating Ambient Temp. <sup>1</sup>	°C	-40	+25	+85
VAGC	Gain Control Voltage Range	V	0	-	3.5
fвw	fвw Video Input Signal Range		10	45	100

Note:

1. Vcc = 4.5 to 5.5 V

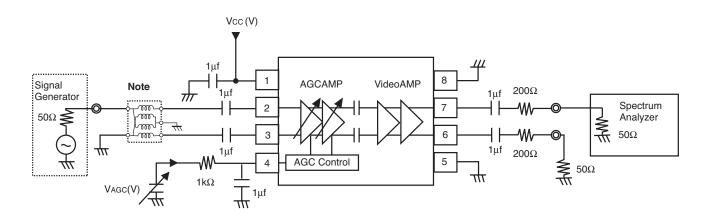
## **PIN EXPLANATIONS**

Pin No.	Name	Applied Voltage (v)	Pin Voltage (v) <sup>1</sup>	Description	Internal Equivalent Circuit
1	Vcc	4.5 to 5.5		Power supply pin. This pin should be externally equipped with bypass capaci- tor to minimize ground impedance.	
2	INPUT1		1.29	Signal input pins of AGC amplifier.	
3	INPUT2		1.29		
4	VAGC	0 to Vcc		Gain control pin. This pin's bias govern the AGC output level. Minimuim Gain at VAGC = $0.5$ V Maximum Gain at VAGC = $3$ to $3.5$ V Recommended to use by dividing AGC voltage with external resistor (ex. $1k\Omega$ )	AGC Amp S
5	GND 2	0		Ground pin. This pin should be con- nected to system ground with minimum inductance. Ground pattern on the board should be formed as wide as possible.	
6	OUTPUT2		2.28	Signal output pins of video amplifier	
7	OUTPUT1		2.28		
8	GND 1	0		Ground pin. This pin should be con- nected to system ground with minimum inductance. Ground pattern on the board should be formed as wide as pos- sible. All ground pins must be connect- ed together with wide ground pattern to decrease impedance difference.	

Note:

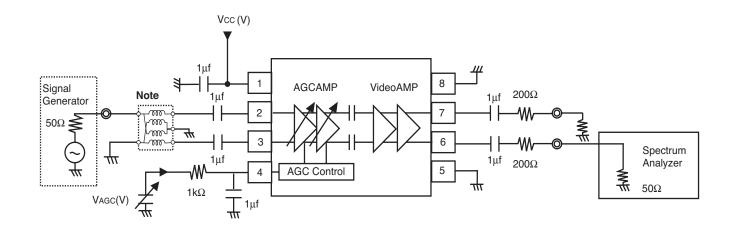
1. PIN is measured at Vcc = 5 V

## **MEASUREMENT CIRCUIT 1**



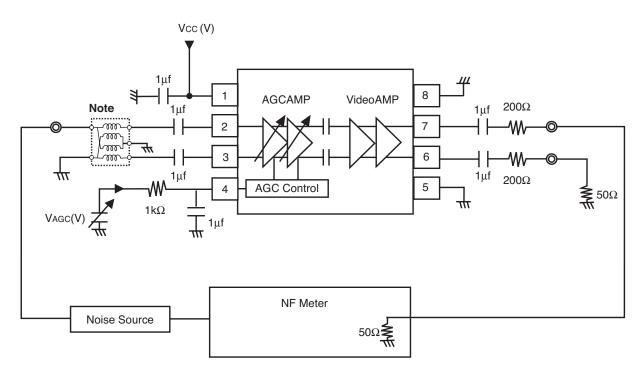
Note: Balun Transformer : TOKO 617DB-1010 B4F (Double balanced type)

## **MEASUREMENT CIRCUIT 2**



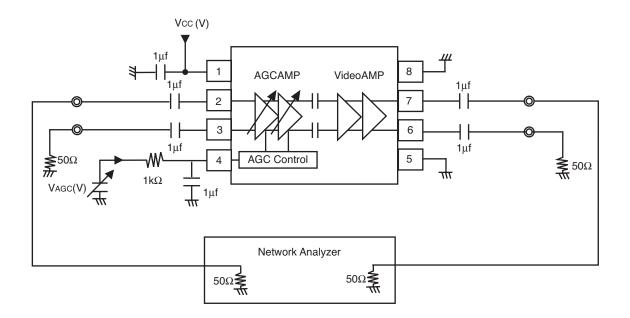
Note: Balun Transformer : TOKO 617DB-1010 B4F (Double balanced type)

## **MEASUREMENT CIRCUIT 3**

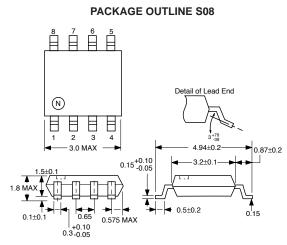


Note: Balun Transformer : TOKO 617DB-1010 B4F (Double balanced type)

### **MEASUREMENT CIRCUIT 4**



#### OUTLINE DIMENSIONS (Units in mm)



All dimensions are typical unless specified otherwise.

#### **ORDERING INFORMATION**

PART NUMBER	QUANTITY	
UPC3221GV-E1	1 kp/reel	

Note:

Embossed tape 8 mm wide. Pin 1 indicates pull-out direction of tape.

Life Support Applications

These NEC products are not intended for use in life support devices, appliances, or systems where the malfunction of these products can reasonably be expected to result in personal injury. The customers of CEL using or selling these products for use in such applications do so at their own risk and agree to fully indemnify CEL for all damages resulting from such improper use or sale.

California Eastern Laboratories, Your source for NEC RF, Microwave, Optoelectronic, and Fiber Optic Semiconductor Devices. 4590 Patrick Henry Drive • Santa Clara, CA 95054-1817 • (408) 988-3500 • FAX (408) 988-0279 • www.cel.com DATA SUBJECT TO CHANGE WITHOUT NOTICE