TOSHIBA Transistor Silicon PNP Triple Diffused Type

# 2SA1923

#### **High Voltage Switching Applications**

Unit: mm

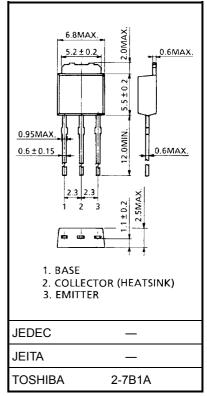
• High voltage:  $V_{CEO} = -400 \text{ V}$ 

• Low saturation voltage:  $V_{CE (sat)} = -1 V (max)$ 

 $(I_C = -100 \text{ mA}, I_B = -10 \text{ mA})$ 

#### **Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		$V_{CBO}$	-400	V	
Collector-emitter voltage		V <sub>CEO</sub>	-400	V	
Emitter-base voltage		V <sub>EBO</sub>	-7	٧	
Collector current	DC	IC	-0.5	Α	
	Pulse	I <sub>CP</sub>	-1		
Base current		Ι <sub>Β</sub>	-0.25	Α	
Collector power dissipation	Ta = 25°C	D.	1	W	
	Tc = 25°C	PC	10		
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	−55 to 150	°C	

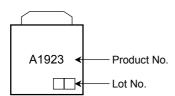


Weight: 0.36 g (typ.)

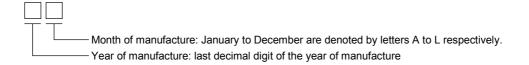
## Electrical Characteristics (Ta = 25°C)

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off c	urrent	I <sub>CBO</sub>	V <sub>CB</sub> = -400 V, I <sub>E</sub> = 0	_	_	-10	μΑ
Emitter cut-off cur	rent	I <sub>EBO</sub>	V <sub>EB</sub> = -7 V, I <sub>C</sub> = 0	-	_	-1	μΑ
Collector-emitter breakdown voltage		V (BR) CEO	I <sub>C</sub> = -10 mA, I <sub>B</sub> = 0	-400	_	_	V
DC current gain		h <sub>FE (1)</sub>	V <sub>CE</sub> = -5 V, I <sub>C</sub> = -20 mA	140	_	450	
		h <sub>FE (2)</sub>	V <sub>CE</sub> = -5 V, I <sub>C</sub> = -100 mA	140	_	400	
Collector-emitter	saturation voltage	V <sub>CE (sat)</sub>	I <sub>C</sub> = -100 mA, I <sub>B</sub> = -10 mA		-0.4	-1.0	V
Base-emitter saturation voltage		V <sub>BE (sat)</sub>	$I_C = -100 \text{ mA}, I_B = -10 \text{ mA}$	1	-0.76	-0.9	V
Transition frequer	псу	f <sub>T</sub>	V <sub>CE</sub> = -5 V, I <sub>C</sub> = -50 mA	1	35	_	MHz
Collector output capacitance		C <sub>ob</sub>	V <sub>CB</sub> = -10 V, I <sub>E</sub> = 0, f = 1 MHz	1	18	_	pF
Switching time Sto	Turn-on time	t <sub>on</sub>	20 μs INPUT HB1 OUTPUT  1B2 HB2 NCC = -200 V	_	0.2	_	
	Storage time	t <sub>stg</sub>		_	2.3	_	μs
	Fall time	t <sub>f</sub>	$I_{B1}$ = -10 mA, $I_{B2}$ = 20 mA, DUTY CYCLE ≤ 1%	_	0.2	_	

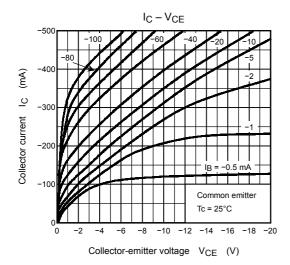
### Marking

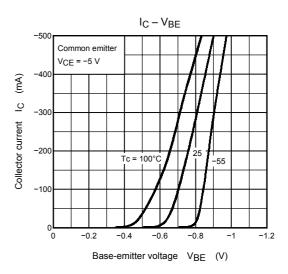


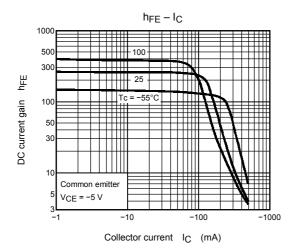
## **Explanation of Lot No.**

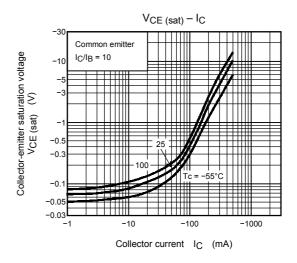


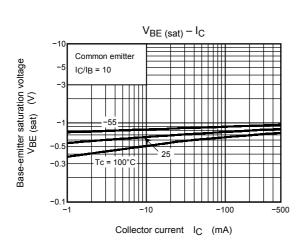
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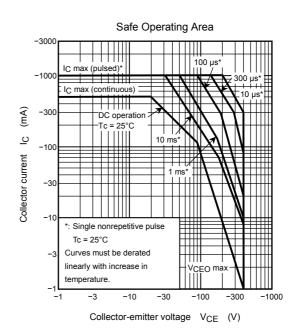












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