

#### GENERAL PURPOSE AMPLIFIER

# Typical Applications

- Broadband, Low Noise Gain Blocks
- IF or RF Buffer Amplifiers
- Driver Stage for Power Amplifiers
- Final PA for Low Power Applications
- Broadband Test Equipment

# **Product Description**

The RF2337 is a general purpose, low-cost RF amplifier IC. The device is manufactured on an advanced Gallium Arsenide Heterojunction Bipolar Transistor (HBT) process, and has been designed for use as an easily-cascadable  $50\Omega$  gain block. Applications include IF and RF amplification in wireless voice and data communication products operating in frequency bands up to  $6000 \, \text{MHz}$ . The device is self-contained with  $50\Omega$  input and output impedances and requires only two external DC biasing elements to operate as specified. The RF2337 is available in a very small industry-standard SOT23 5-lead surface mount package, enabling compact designs which conserve board space.

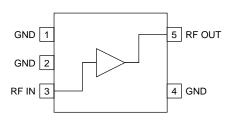
Optimum Technology Matching® Applied

☐ Si BJT

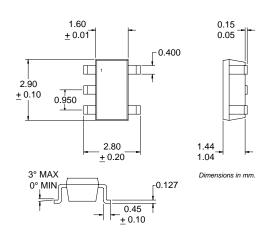
**▼** GaAs HBT

☐ GaAs MESFET

☐ Si Bi-CMOS ☐ SiGe HBT ☐ Si CMOS



Functional Block Diagram



Package Style: SOT 5 Lead

#### **Features**

- DC to 6000MHz Operation
- Internally matched Input and Output
- 15dB Small Signal Gain
- +25dBm Output IP3
- +12dBm Output Power
- Single Positive Power Supply

#### Ordering Information

RF2337 General Purpose Amplifier
RF2337 PCBA Fully Assembled Evaluation Board

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# **RF2337**

### **Absolute Maximum Ratings**

Parameter	Rating	Unit
Supply Current	75	mA
Input RF Power	+15	dBm
Operating Ambient Temperature	-40 to +85	°C
Storage Temperature	-60 to +150	°C



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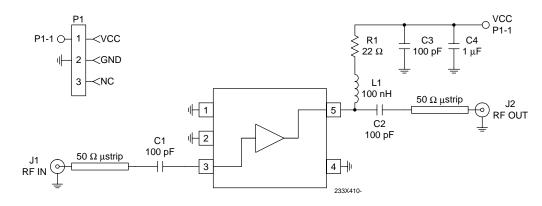
Parameter	Specification		Unit	Condition		
Parameter	Min.	Тур.	Max.	Unit	Condition	
Overall					T=25°C, I <sub>CC</sub> =40mA	
Frequency Range		DC to 6000		MHz		
3dB Bandwidth		2		GHz		
Gain		16		dB	Freq=100MHz	
		15.4		dB	Freq=1000MHz	
		14.4		dB	Freq=2000MHz	
		12.5		dB	Freq=3000MHz	
		11.5			Freq=4000MHz	
		10.6			Freq=5000MHz	
		10			Freq=6000MHz	
Noise Figure		4.5		dB	Freq=2000MHz	
Input VSWR		2.0:1			In a 50Ω system, DC to 3000MHz	
Output VSWR		2.0:1			In a 50Ω system, DC to 3000MHz	
Output IP <sub>3</sub>		+25		dBm	Freq=1000MHz±50kHz, P <sub>TONE</sub> =-10dBm	
Output P <sub>1dB</sub>		+11.8		dBm	Freq=2000MHz	
Reverse Isolation		17.5		dB	Freq=2000MHz	
Power Supply					With $22\Omega$ bias resistor	
Device Operating Voltage		3.6		V	At pin 5 with I <sub>CC</sub> =40 mA	
Operating Current		40		mA		

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Pin	Function	Description	Interface Schematic	
1	GND	Ground connection. Keep traces physically short and connect immediately to ground plane for best performance.		
2	GND	Same as pin 1.		
3	RF IN	RF input pin. This pin is NOT internally DC blocked. A DC blocking capacitor, suitable for the frequency of operation, should be used in most applications. DC coupling of the input is not allowed, because this will override the internal feedback loop and cause temperature instability.		
4	GND	Same as pin 1.		
5	RF OUT	RF output and bias pin. Biasing is accomplished with an external series resistor and choke inductor to $V_{CC}$ . The resistor is selected to set the DC current into this pin to a desired level. The resistor value is determined by the following equation: $R = \frac{(V_{SUPPLY} - V_{DEVICE})}{I_{CC}}$ Care should also be taken in the resistor selection to <b>ensure that the</b>	RF IN O	
		current into the part never exceeds 75mA over the planned operating temperature. This means that a resistor between the supply and this pin is always required, even if a supply near 3.6V is available, to provide DC feedback to prevent thermal runaway. Because DC is present on this pin, a DC blocking capacitor, suitable for the frequency of operation, should be used in most applications. The supply side of the bias network should also be well bypassed.	<u> </u>	

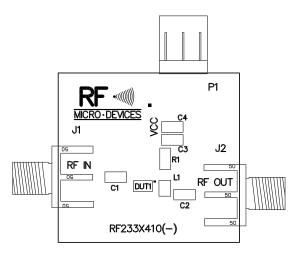
# **Evaluation Board Schematic**

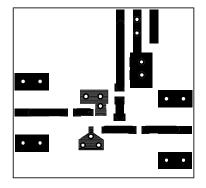
(Download Bill of Materials from www.rfmd.com.)



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# Evaluation Board Layout Board Size 1" x 1"





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