



MMBT3904

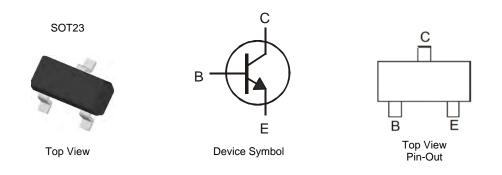
40V NPN SMALL SIGNAL SURFACE MOUNT TRANSISTOR

Features

- Epitaxial Planar Die Construction
- Complementary PNP Type Available (MMBT3906)
- Ideal for Medium Power Amplification and Switching
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound,
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Weight: 0.008 grams (approximate)



Ordering Information (Notes 4 & 5)

Product	Grade	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
MMBT3904-7-F	Commercial	K1N / C1N	7	8	3,000
MMBT3904Q-7-F	Automotive	K1N	7	8	3,000

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

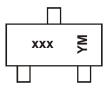
2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.

3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com

5. Products with Q-suffix are automotive grade. Automotive products are electrical and thermal the same as the commercial, except where specified.

Marking Information



Product Type Marking Code: xxx = K1N or C1N YM = Date Code Marking Y = Year (ex: X = 2010)M = Month (ex: 9 = September)

Date Code Key

Notes:

Year	2010		2011	2012		2013	2014		2015	2016		2017
Code	Х		Y	Z		А	В		С	D		Е
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings $@T_A = 25^{\circ}C$ unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	60	V
Collector-Emitter Voltage	V _{CEO}	40	V
Emitter-Base Voltage	V _{EBO}	6.0	V
Collector Current - Continuous	Ι _C	200	mA

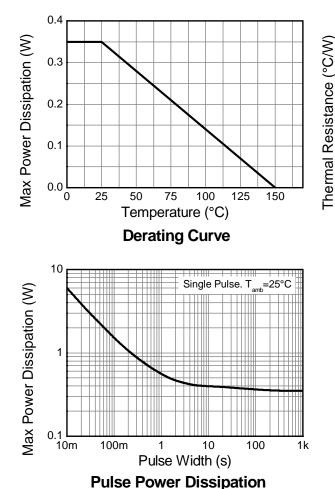
Thermal Characteristics @T_A = 25°C unless otherwise specified

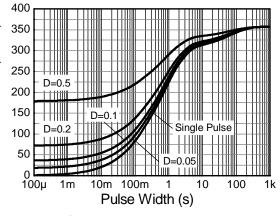
Characteristic		Symbol	Value	Unit	
Collector Power Dissipation	(Note 6)	D	D- 310		
	(Note 7)	- P _D	350	mW	
Thermal Resistance, Junction to Ambient	(Note 6)	Р	403	°C/W	
	(Note 7)	R _{0JA}	357	-0/10	
Thermal Resistance, Junction to Leads	(Note 8)	R _{θJL}	350	°C/W	
Operating and Storage Temperature Range	T _J ,T _{STG}	-55 to +150	°C		

Notes:

For the device mounted on minimum recommended pad layout FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
 For the device mounted on 15mm x 15mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

8. Thermal resistance from junction to solder-point (at the end of the collector lead).









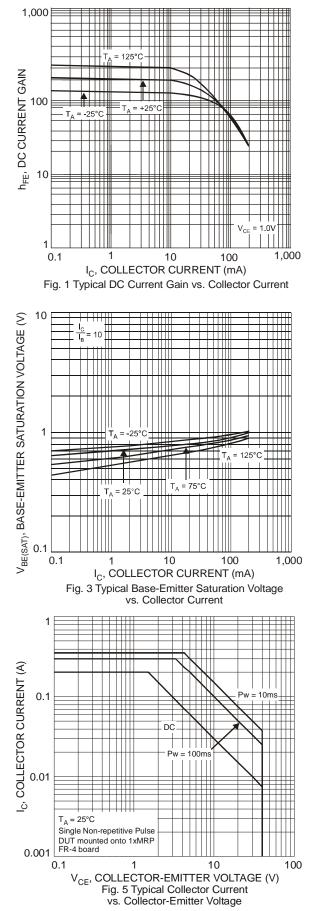
Electrical Characteristics @T_A = 25°C unless otherwise specified

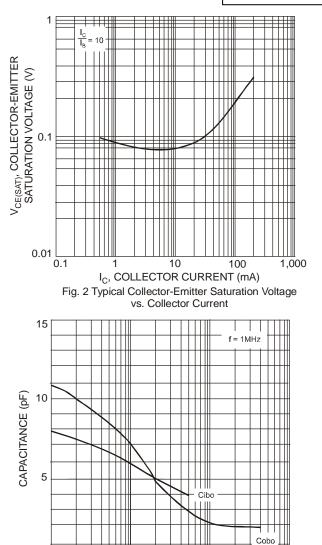
Characteristic	Symbol	Min	Max	Unit	Test Condition	
OFF CHARACTERISTICS			•			
Collector-Base Breakdown Voltage	BV _{CBO}	60	—	V	$I_{C} = 10 \mu A, I_{E} = 0$	
Collector-Emitter Breakdown Voltage (Note 9)	BV _{CEO}	40	_	V	$I_{\rm C} = 1.0 {\rm mA}, I_{\rm B} = 0$	
Emitter-Base Breakdown Voltage	BV _{EBO}	6.0	_	V	$I_{\rm E} = 10 \mu A, I_{\rm C} = 0$	
Collector Cutoff Current	ICEX	_	50	nA	$V_{CE} = 30V, V_{EB(OFF)} = 3.0V$	
Base Cutoff Current	I _{BL}	_	50	nA	$V_{CE} = 30V, V_{EB(OFF)} = 3.0V$	
ON CHARACTERISTICS (Note 9)					· · ·	
DC Current Gain	h _{FE}	40 70 100 60	 300 	_	$\begin{split} I_{C} &= 100 \mu A, \ V_{CE} = \ 1.0 V \\ I_{C} &= 1.0 m A, \ V_{CE} = \ 1.0 V \\ I_{C} &= 10 m A, \ V_{CE} = \ 1.0 V \\ I_{C} &= 50 m A, \ V_{CE} = \ 1.0 V \end{split}$	
		30			$I_{C} = 100 \text{mA}, V_{CE} = 1.0 \text{V}$	
Collector-Emitter Saturation Voltage	V _{CE(sat)}		0.20 0.30	V	$I_{C} = 10mA, I_{B} = 1.0mA$ $I_{C} = 50mA, I_{B} = 5.0mA$	
Base-Emitter Saturation Voltage	V _{BE(sat)}	0.65	0.85 0.95	V	$I_C = 10mA$, $I_B = 1.0mA$ $I_C = 50mA$, $I_B = 5.0mA$	
SMALL SIGNAL CHARACTERISTICS				•		
Output Capacitance	C _{obo}		4.0	pF	$V_{CB} = 5.0V, f = 1.0MHz, I_E = 0$	
Input Capacitance	Cibo	_	8.0	pF	$V_{EB} = 0.5V, f = 1.0MHz, I_{C} = 0$	
Input Impedance	h _{ie}	1.0	10	kΩ		
Voltage Feedback Ratio	h _{re}	0.5	8.0	x 10 ⁻⁴	V _{CE} = 10V, I _C = 1.0mA, f = 1.0kHz	
Small Signal Current Gain	h _{fe}	100	400	—		
Output Admittance	h _{oe}	1.0	40	μS		
Current Gain-Bandwidth Product	fT	300	_	MHz	$V_{CE} = 20V, I_C = 10mA,$ f = 100MHz	
Noise Figure	NF	_	5.0	dB	$V_{CE} = 5.0V, I_{C} = 100 \mu A,$ $R_{S} = 1.0 k \Omega, f = 1.0 k Hz$	
SWITCHING CHARACTERISTICS			-		-	
Delay Time	t _d	_	35	ns	$V_{CC} = 3.0V, I_C = 10mA,$	
Rise Time	tr	_	35	ns	$V_{BE(off)} = -0.5V, I_{B1} = 1.0mA$	
Storage Time	ts	_	200	ns	$V_{CC} = 3.0V, I_{C} = 10mA,$	
Fall Time	t _f		50	ns	$I_{B1} = I_{B2} = 1.0 \text{mA}$	

Notes: 9. Short duration pulse test used to minimize self-heating effect.



MMBT3904





 1
 10
 100

 V_R, REVERSE VOLTAGE (V)
 100

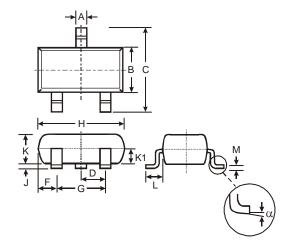
 Fig. 4 Typical Capacitance Characteristics

0

0.1

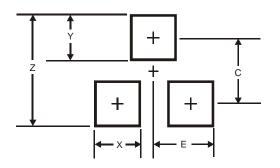


Package Outline Dimensions



SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Η	2.80	3.00	2.90			
J	0.013	0.10	0.05			
κ	0.903	1.10	1.00			
K1	-	-	0.400			
L	0.45	0.61	0.55			
Μ	0.085	0.18	0.11			
α	0°	8°	-			
All Dimensions in mm						

Suggested Pad Layout



Dimensions	Value (in mm)		
Z	2.9		
Х	0.8		
Y	0.9		
С	2.0		
E	1.35		



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