## Silicon P-Channel MOS FET

# HITACHI

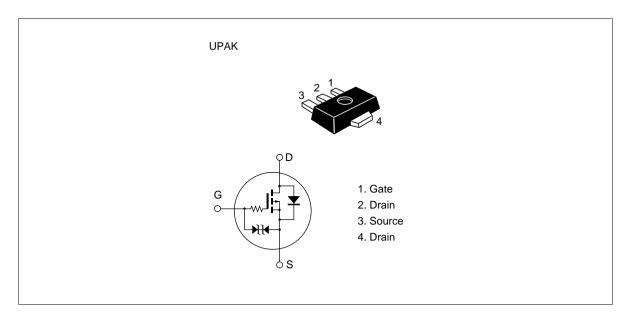
#### Application

High speed power switching

#### Features

- Low on-resistance
- High speed switching
- Low drive current
- 2.5 V gate drive device can be driven from 3 V source

#### Outline





### **Absolute Maximum Ratings** (Ta = $25^{\circ}$ C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	-20	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	-2	А
Drain peak current	I D(pulse) * 1	-4	А
Body to drain diode reverse drain current	l <sub>DR</sub>	-2	А
Channel dissipation	Pch*2	1	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

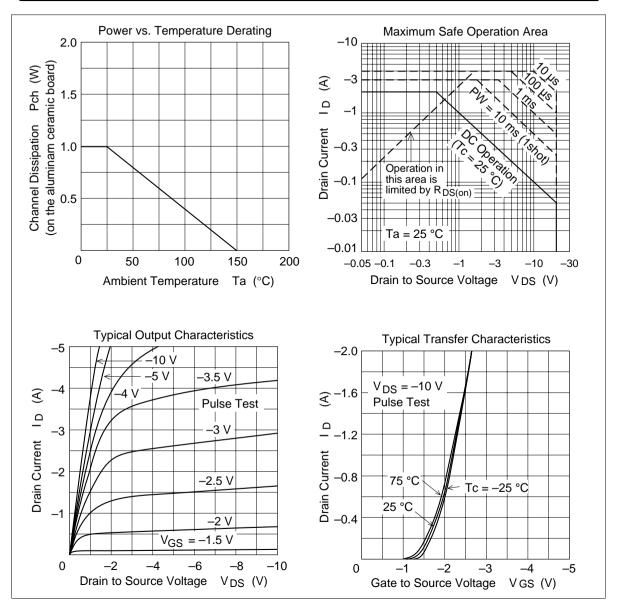
Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

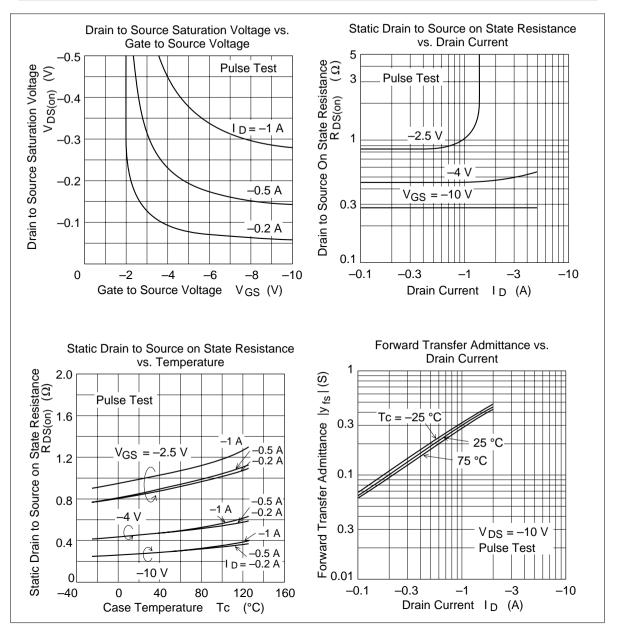
2. Value on the alumina ceramic board (12.5×20×0.7 mm)

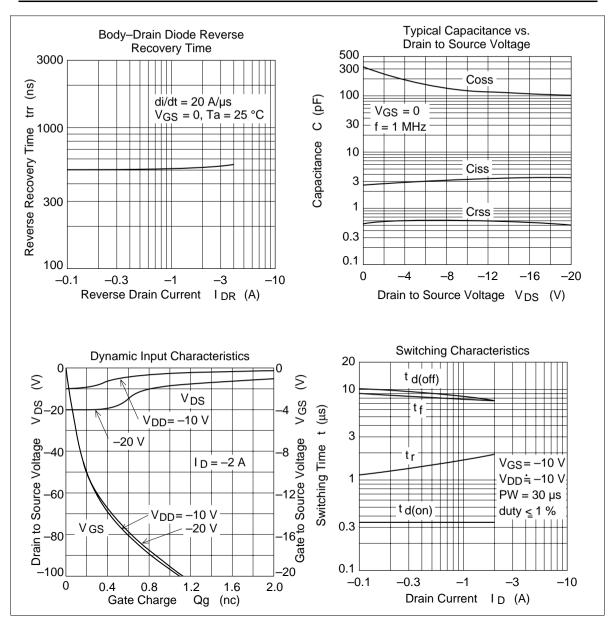
3. Marking is "RY".

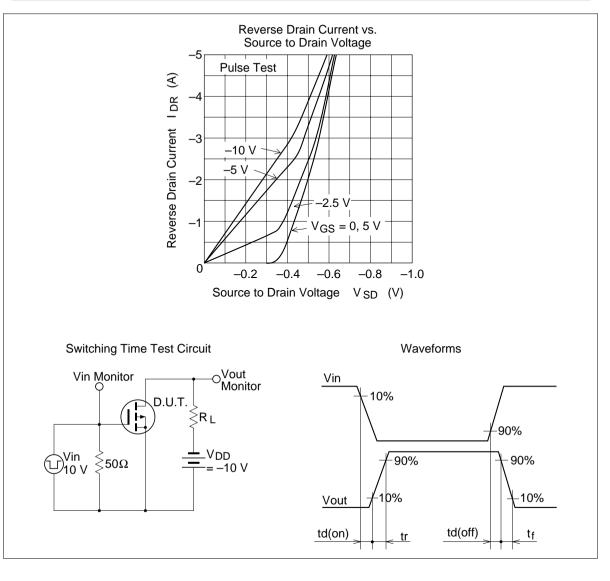
## **Electrical Characteristics** (Ta = $25^{\circ}$ C)

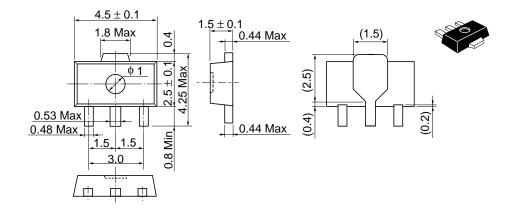
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	-20		_	V	$I_{\rm D} = -10$ mA, $V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	—	—	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>			±10	μA	$V_{GS} = \pm 16 \text{ V}, \text{ V}_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	—		-10	μΑ	$V_{\rm DS} = -16 \ V, \ V_{\rm GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-0.5		-1.5	V	$I_{\rm D} = -1 \text{ mA}, V_{\rm DS} = -10 \text{ V}$
Static drain to source on state	$R_{DS(on)}$	—	0.28	0.4	Ω	$I_{\rm D} = -1$ A, $V_{\rm GS} = -10$ V
resistance		_	0.85	1.5	Ω	$I_{\rm D} = -0.4$ A, $V_{\rm GS} = -2.5$ V
Forward transfer admittance	y <sub>fs</sub>	0.15	0.3		S	$I_{\rm D} = -1$ A, $V_{\rm DS} = -10$ V
Input capacitance	Ciss		3.2		pF	$V_{DS} = -10 V, V_{GS} = 0,$
Output capacitance	Coss	_	130		pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	0.6		pF	
Turn-on delay time	t <sub>d(on)</sub>		350		ns	$I_{\rm D} = -1$ A, $V_{\rm GS} = -10$ V,
Rise time	t,	_	1650		ns	$R_{L} = 10 \Omega$
Turn-off delay time	t <sub>d(off)</sub>	_	7280		ns	
Fall time	t <sub>f</sub>		6950		ns	
Body to drain diode forward voltage	$V_{DF}$	_	-1.0	—	V	$I_{\rm F} = -2$ A, $V_{\rm GS} = 0$
Body to drain diode reverse recovery time	t <sub>rr</sub>	_	530	_	ns	$I_{F} = -2 \text{ A}, V_{GS} = 0,$ $di_{F}/dt = 20 \text{ A}/\mu \text{s}$











Hitachi Code	UPAK
JEDEC	
EIAJ	Conforms
Weight (reference value)	0.050 g

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