2SJ453



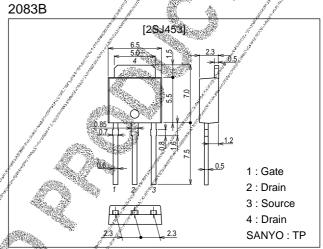
# **Ultrahigh-Speed Switching Applications**

#### **Features**

- · Low ON resistance.
- · Ultrahigh-speed switching.
- · Low voltage drive.
- · On-chip FRD.

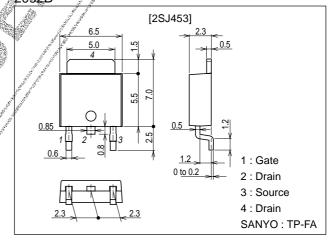
## **Package Dimensions**

unit:mm



unit:mm

2092B



- Any and all SANYO products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your SANYO representative nearest you before using any SANYO products described or contained herein in such applications.
- SANYO assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges,or other parameters) listed in products specifications of any and all SANYO products described or contained herein.

# **Specifications**

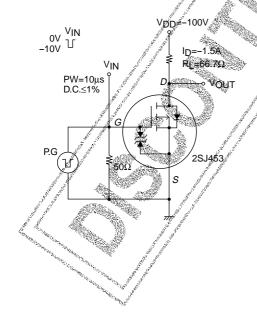
## **Absolute Maximum Ratings** at $Ta = 25^{\circ}C$

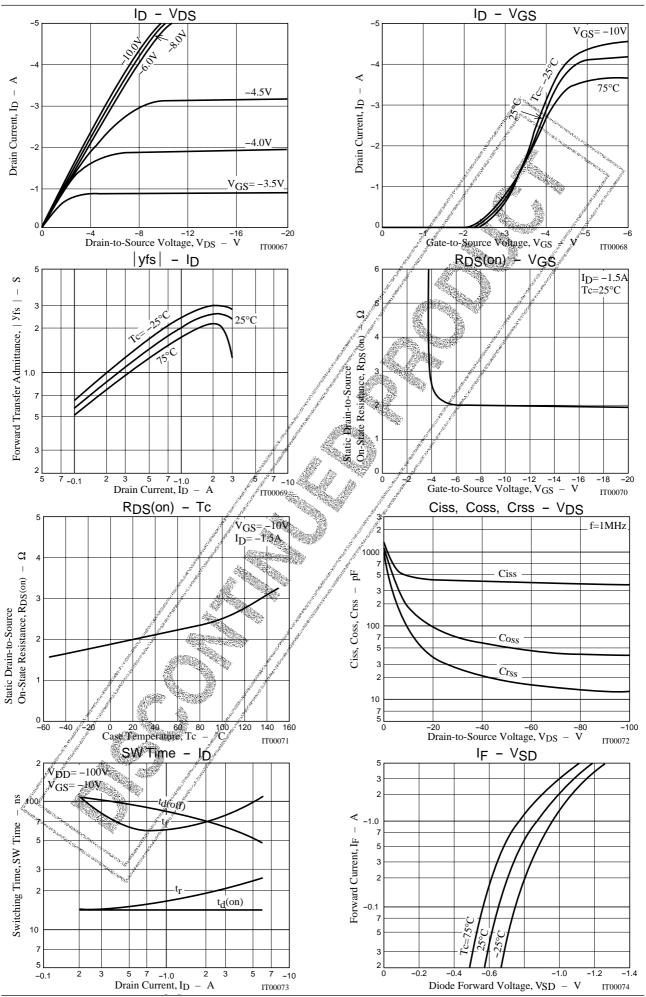
Parameter	Symbol	Conditions		Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>			-250	V
Gate-to-Source Voltage	V <sub>GSS</sub>		, s	±30	V
Drain Current (DC)	I <sub>D</sub>		1	-3	Α
Drain Current (Pulse)	I <sub>DP</sub>	PW≤10μs, duty cycle≤1%	11	-12	Α
Allowable Power Dissipation	PD			<i>₽</i>	W
	טי	Tc=25°C	11	30	W
Channel Temperature	Tch		J. J. 33	150	°C
Storage Temperature	Tstg		11 67	_55 to +150	°C

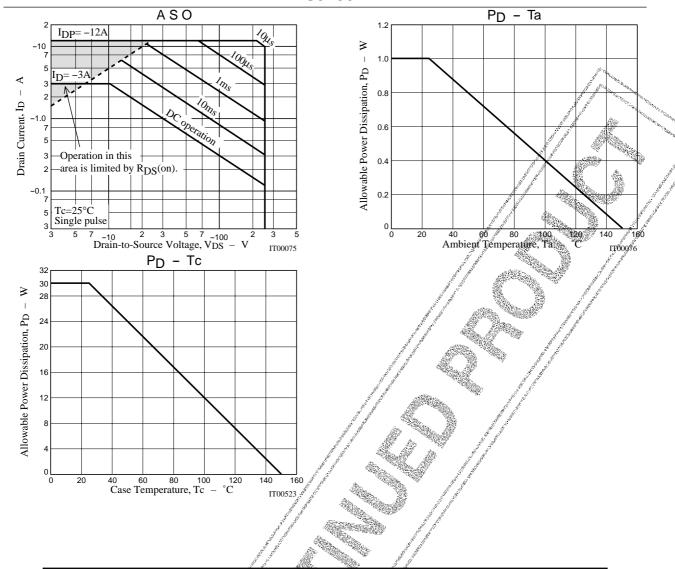
#### **Electrical Characteristics** at Ta = 25°C

		· · · · · · · · · · · · · · · · · · ·	20 NOTES			
Parameter	Symbol	Conditions	min	Ratings typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =-1mA, V <sub>GS</sub> =0	-250	and the second		V
Gate-to-Source Breakdown Voltage	V <sub>(BR)</sub> GSS	I <sub>G</sub> =±100μA, V <sub>DS</sub> =0	±30,			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-250V, V <sub>GS</sub> =0	ال المراجع	e <sup>der</sup>	-100	μA
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±25V, V <sub>DS</sub> =0	gagett gerill		±10	μA
Cutoff Voltage	V <sub>GS</sub> (off)	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA	<i>f</i> 2.0		-3.0	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1.5A	17	2.4		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)	I <sub>D</sub> =-1.5A, V <sub>G</sub> \$=-10V	e e	2.0	2.6	Ω
Input Capacitance	Ciss	V <sub>DS</sub> =-20V, <b>f</b> ≟1MHz		420		pF
Output Capacitance	Coss	V <sub>DS</sub> =−20V, f≠1MHz		100		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =-20V, f=1MHz		40		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit		14		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit		18		ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit		75		ns
Fall Time	t <sub>f</sub>	See specified Test Circuit		65		ns
Diode Forward Voltage	V <sub>SD</sub> /	I <sub>S</sub> =-3 <b>A</b> , V <sub>GS</sub> =0		-1.1	-1.5	V
Diode Reverse Recovery Time	t <sub>ef</sub> /	I <sub>S</sub> =-3A, di/dt=100A/µs		100		ns

## **Switching Time Test Circuit**







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