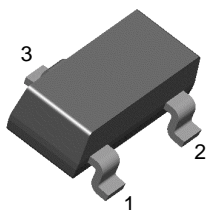
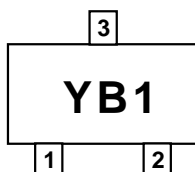


## FYV0203S/DN/DP/DS

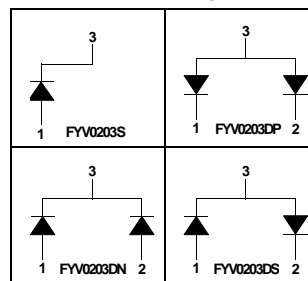


SOT-23



**Marking**  
FYV0203S = YB1    FYV0203DP = YB3  
FYV0203DN = YB2    FYV0203DS = YB4

Connection Diagram



### Schottky Diode

#### Absolute Maximum Ratings $T_A=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Value	Units
$V_{RRM}$	Maximum Repetitive Reverse Voltage	30	V
$I_{F(AV)}$	Average Rectified Forward Current	0.2	A
$I_{FSM}$	Non-repetitive Peak Surge Current Pulse Width = 1.0s	0.6	A
$T_{STG}$	Storage Temperature Range	-65 to +150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature	150	$^\circ\text{C}$

#### Thermal Characteristics

Symbol	Parameter	Value	Units
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	430	$^\circ\text{C}/\text{W}$

#### Electrical Characteristics $T_A=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Min.	Typ.	Max.	Units				
$V_F^*$	Forward Voltage Drop				mV				
						$I_F = 0.1\text{mA}$	-	210	240
						$I_F = 1\text{mA}$	-	270	320
						$I_F = 10\text{mA}$	-	340	400
						$I_F = 30\text{mA}$	-	390	500
						$I_F = 100\text{mA}$	-	485	800
$I_R^*$	Reverse Current @ Rated $V_R$	$T_A = 25^\circ\text{C}$ $T_A = 125^\circ\text{C}$	-	0.2	2	$\mu\text{A}$			
			-	130	-				
$C_T$	Total Capacitance $V_R = 1\text{V}$ , $f = 1.0\text{MHz}$	-	-	10	pF				
$t_{rr}$	Reverse Recovery Time $I_F = I_R = 10\text{mA}$ , $I_{RR} = 1\text{mA}$ , $R_L = 100\Omega$	-	-	5	ns				

\* Pulse Test: Pulse Width=300 $\mu\text{s}$ , Duty Cycle=2%

# Typical Characteristics

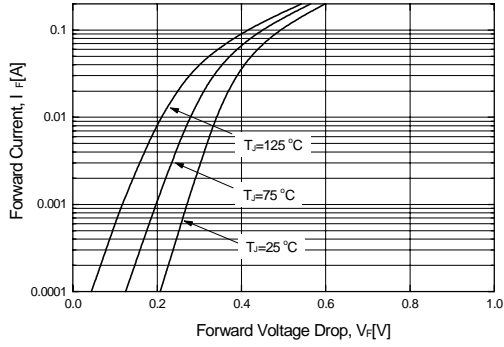


Figure 1. Typical Forward Voltage Characteristics

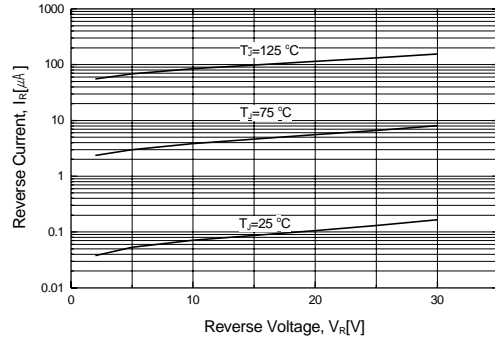


Figure 2. Typical Reverse Current vs. Reverse Voltage

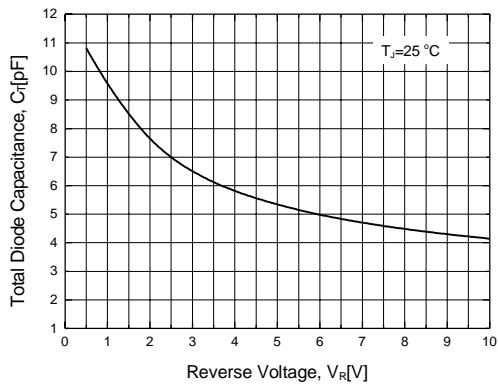
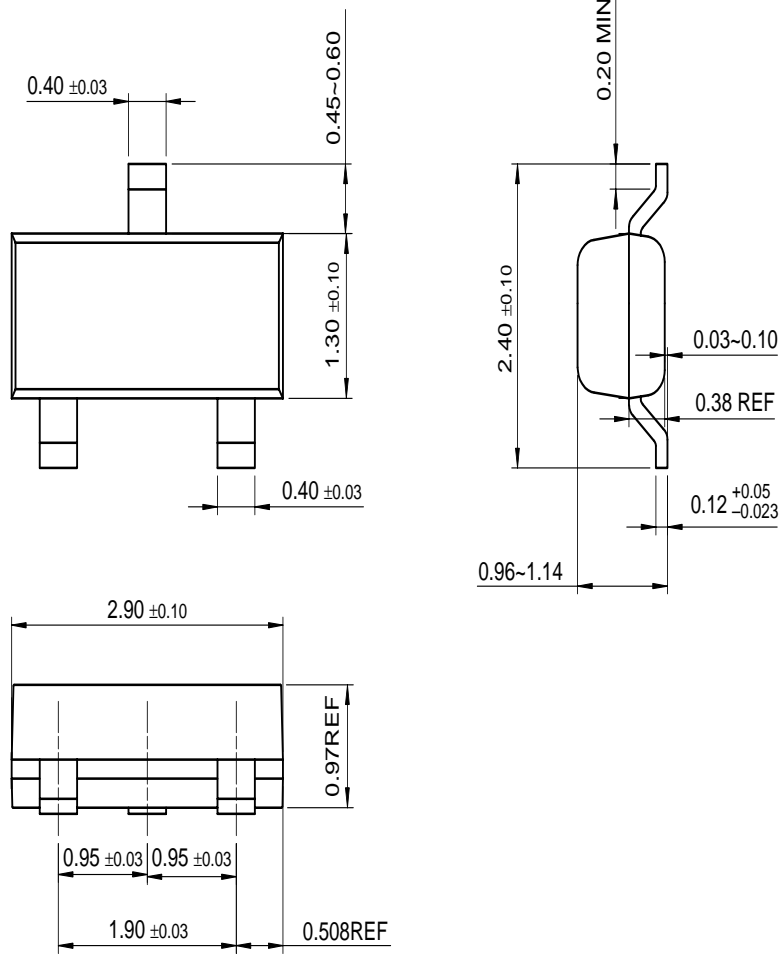


Figure 3. Total Diode Capacitance

# Package Dimensions

## SOT-23

FYV0203S/DN/DP/DS



Dimensions in Millimeters

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Bottomless™	FAST <sup>r</sup> ™	OPTOPLANAR™	STAR*POWER™	
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CROSSVOLT™	GlobalOptoisolator™	POP™	SuperSOT™-3	
DenseTrench™	GTO™	Power247™	SuperSOT™-6	
DOMET™	HiSeC™	PowerTrench <sup>®</sup>	SuperSOT™-8	
EcoSPARK™	ISOPLANAR™	QFET™	SyncFET™	
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EnSigna™	MicroFET™	QT Optoelectronics™	TinyLogic™	
FACT™	MicroPak™	Quiet Series™	UHC™	
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