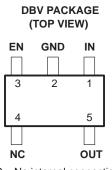
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- 100-mA Low-Dropout Regulator
- Fixed Output Voltage Options: 5 V, 3.8 V, 3.3 V, 3.2 V, and 3 V
- Dropout Typically 170 mV at 100-mA
- Thermal Protection
- Less Than 1 μA Quiescent Current in Shutdown
- -40°C to 125°C Operating Junction Temperature Range
- 5-Pin SOT-23 (DBV) Package
- ESD Protection Verified to 1.5 KV Human Body Model (HBM) per MIL-STD-883C



NC – No internal connection

#### description

The TPS761xx is a 100 mA, low dropout (LDO) voltage regulator designed specifically for battery-powered applications. A proprietary BiCMOS fabrication process allows the TPS761xx to provide outstanding performance in all specifications critical to battery-powered operation.

The TPS761xx is available in a space-saving SOT-23 (DBV) package and operates over a junction temperature range of –40°C to 125°C.

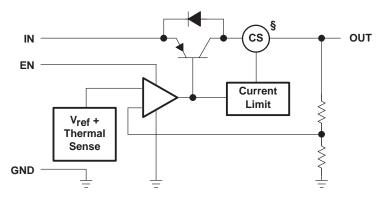
ТJ	VOLTAGE	PACKAGE	PART NUMBER		PART NUMBER S		SYMBOL
	3 V		TPS76130DBVR <sup>†</sup>	TPS76130DBVT <sup>‡</sup>	PAEI		
	3.2 V		TPS76132DBVR <sup>†</sup>	TPS76132DBVT <sup>‡</sup>	PAFI		
-40°C to 125°C	3.3 V	SOT-23 (DBV)	TPS76133DBVR <sup>†</sup>	TPS76133DBVT <sup>‡</sup>	PAII		
	3.8 V	(001)	TPS76138DBVR <sup>†</sup>	TPS76138DBVT <sup>‡</sup>	PAKI		
	5 V		TPS76150DBVR <sup>†</sup>	TPS76150DBVT <sup>‡</sup>	PALI		

#### **AVAILABLE OPTIONS**

<sup>†</sup> The DBVR passive indicates tape and reel of 3000 parts.

<sup>‡</sup> The DBVT passive indicates tape and reel of 250 parts.

### functional block diagram



§ Current sense



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### **Terminal Functions**

TERM	INAL	1/0	DESCRIPTION			
NAME	NO.	I/O	DESCRIPTION			
EN	3	I	Enable input			
GND	2		Ground			
IN	1	I	Input voltage			
NC	4		No connection			
OUT	5	0	Regulated output voltage			

## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

Input voltage range, V <sub>I</sub> (see Note 1)	
Voltage range at EN	$-0.3 \text{ V to V}_{\text{I}} + 0.3 \text{ V}$
Peak output current	internally limited
Continuous total dissipation	. See Dissipation Rating Table
Operating junction temperature range, T <sub>J</sub>	–40°C to 150°C
Storage temperature range, T <sub>stg</sub>	–65°C to 150°C
ESD rating, HBM	1.5 kV

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
NOTE 1: All voltages are with respect to device GND pin.

DISSIPATION RATING TABLE

	PACKAGE	T <sub>A</sub> ≤ 25°C POWER RATING	DERATING FACTOR ABOVE T <sub>A</sub> = 25°C	T <sub>A</sub> = 70°C POWER RATING	T <sub>A</sub> = 85°C POWER RATING
Recommended	DBV	350 mW	3.5 mW/°C	192 mW	140 mW
Maximum	DBV	437 mW	3.5 mW/°C	280 mW	227 mW

### recommended operating conditions

		MIN	NOM	MAX	UNIT	
	TPS76130	3.35		16		
	TPS76132	3.58		16		
Input voltage, VI	TPS76133	3.68		16	6 V	
	TPS76138	4.18		16		
	TPS76150	5.38		16		
Continuous output current, IO		0		100	mA	
Operating junction temperature	э, Т <u>ј</u>	-40		125	°C	



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PARAMETER		TEST CO	TEST CONDITIONS		TYP	MAX	υνιτ		
			T <sub>J</sub> = 25°C		2.96	3	3.04		
		TPS76130	TJ = 25°C,	1 mA < IO < 100 mA	2.9		3.04	V	
			1 mA < I <sub>O</sub> < 100 mA		2.89		3.07		
			TJ = 25°C		3.16	3.2	3.24		
		TPS76132	TJ = 25°C,	1 mA < IO < 100 mA	3.11		3.24	V	
			1 mA < I <sub>O</sub> < 100 mA		3.08		3.3	1	
			T <sub>J</sub> = 25°C		3.26	3.3	3.34		
VO	Output voltage	TPS76133	T <sub>J</sub> = 25°C,	1 mA < I <sub>O</sub> < 100 mA	3.21		3.34	V	
			1 mA < I <sub>O</sub> < 100 mA		3.18		3.4		
			TJ = 25°C		3.76	3.8	3.84		
		TPS76138	TJ = 25°C,	1 mA < IO < 100 mA	3.71		3.84	V	
			1 mA < IO < 100 mA		3.68		3.9		
		TPS76150	TJ = 25°C		4.95	5	5.05		
			TJ = 25°C,	1 mA < IO < 100 mA	4.88		5.05	V	
			1 mA < I <sub>O</sub> < 100 mA		4.86		5.1	-	
II(standby)	Standby current		EN = 0 V				1	μA	
(otariaby)	Quiescent current (GND current)		I <sub>O</sub> = 0 mA,	T <sub>J</sub> = 25°C		90	115		
			$I_{O} = 0 \text{ mA}$				130		
			I <sub>O</sub> = 1 mA,	TJ = 25°C		100	130		
			I <sub>O</sub> = 1 mA				170		
			I <sub>O</sub> = 10 mA,	TJ = 25°C		190	220		
			I <sub>O</sub> = 10 mA				260	μΑ	
			I <sub>O</sub> = 50 mA,	TJ = 25°C		850	1100	1	
			I <sub>O</sub> = 50 mA				1200	-	
			I <sub>O</sub> = 100 mA,	T <sub>J</sub> = 25°C		2600	3600		
			I <sub>O</sub> = 100 mA				4000	1	
TPS		TPS76130	4 V < V <sub>I</sub> < 16,	I <sub>O</sub> = 1 mA		3	10		
		TPS76132	4.2 V < V <sub>I</sub> < 16,	$I_{O} = 1 \text{ mA}$		3	10	1	
	Input regulation	TPS76133	4.3 V < V <sub>I</sub> < 16,	I <sub>O</sub> = 1 mA		3	10	mV	
		TPS76138	4.8 V < V <sub>I</sub> < 16,	I <sub>O</sub> = 1 mA		3	10	1	
		TPS76150	6 V < V <sub>I</sub> < 16	I <sub>O</sub> = 1 mA		3	10	1	
Vn	Output noise voltage	-	BW = 300 Hz to 50 kHz	C <sub>0</sub> = 10 μF, T <sub>J</sub> = 25°C		190		μVrm	
	Ripple rejection		$f = 1 \text{ kHz},  C_0 = 10 \ \mu\text{F},$	TJ = 25°C		63		dB	

### electrical characteristics over recommended operating free-air temperature range, $V_I = V_{O(typ)} + 1 V$ , $I_O = 1 mA$ , EN = $V_I$ , $C_o = 4.7 \mu F$ (unless otherwise noted)



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## electrical characteristics over recommended operating free-air temperature range, V<sub>I</sub> = V<sub>O(tvp)</sub> + 1 V, I<sub>O</sub> = 1 mA, EN = V<sub>I</sub>, C<sub>o</sub> = 4.7 $\mu$ F (unless otherwise noted) (continued)

PARA	AMETER	TES	ST CONDITIONS	MIN	TYP	MAX	UNIT			
		I <sub>O</sub> = 0 mA,	T <sub>J</sub> = 25°C		1	3				
		$I_{O} = 0 \text{ mA}$				5				
	I <sub>O</sub> = 1 mA,	T <sub>J</sub> = 25°C		7	10					
		I <sub>O</sub> = 1 mA				15				
Dropouty	altara	I <sub>O</sub> = 10 mA,	T <sub>J</sub> = 25°C		40	60				
Dropout	ropout voltage	I <sub>O</sub> = 10 mA				90	mV			
		I <sub>O</sub> = 50 mA,	$T_J = 25^{\circ}C$		120	150				
		I <sub>O</sub> = 50 mA				180				
		I <sub>O</sub> = 100 mA,	$T_J = 25^{\circ}C$		170	240				
		I <sub>O</sub> = 100 mA				280				
Peak outp	out current/current limit	T <sub>J</sub> = 25°C		100	150		mA			
High level	enable input			2			V			
Low level	enable input					0.8	V			
		EN = 0 V		-1	0	1	A			
II Input curre		EN = VI			2.5	5	μA			

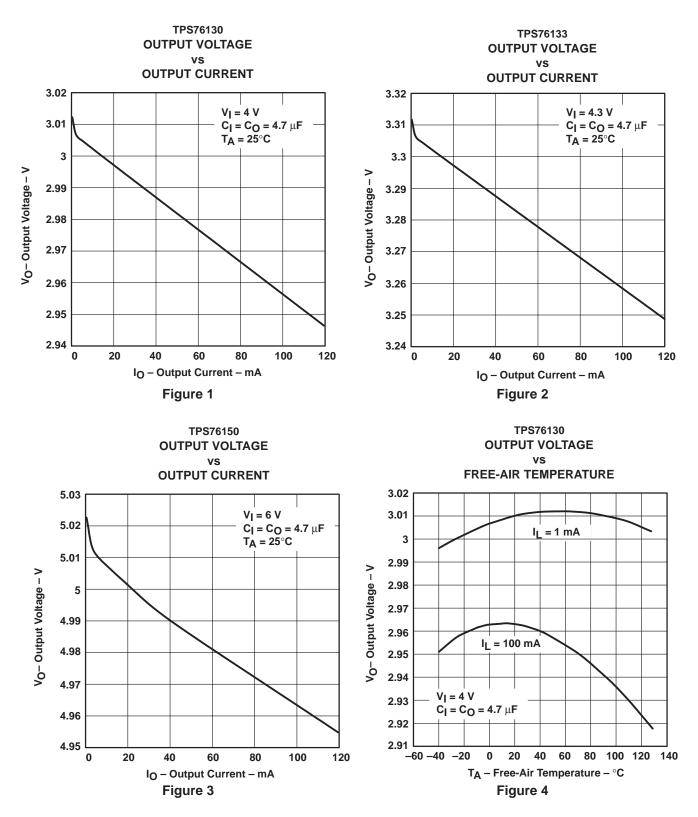
## TYPICAL CHARACTERISTICS

## **Table of Graphs**

			FIGURE
N -	Output voltage vs Output current		1, 2, 3
۷Ö	Oulput voltage	vs Free-air temperature	4, 5, 6
	Ground current	vs Free-air temperature	7, 8, 9
	Output noise	vs Frequency	10
Zo	Output impedance	vs Frequency	11
VDO	Dropout voltage	vs Free-air temperature	12
	Line transient response		13, 15
	Load transient response		14, 16

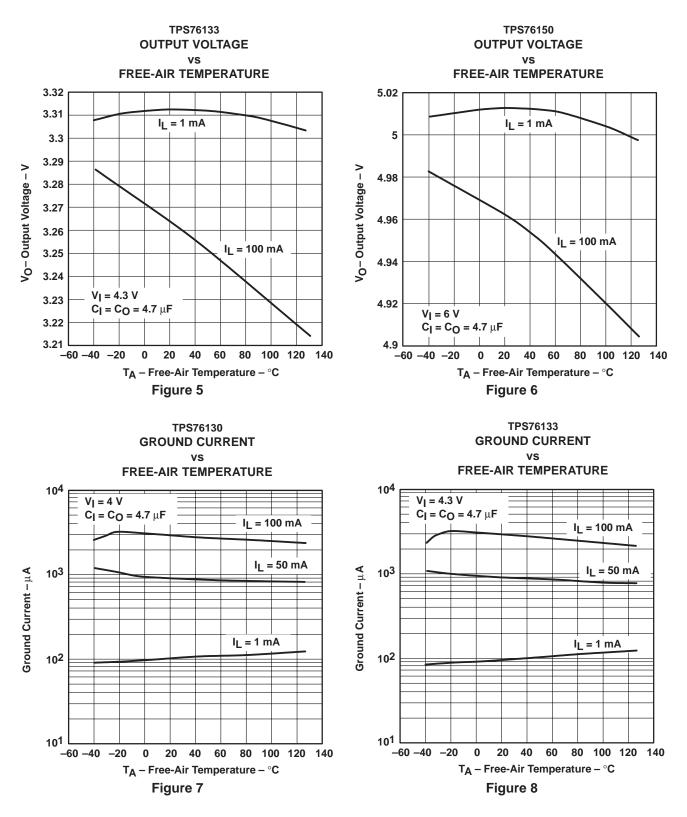


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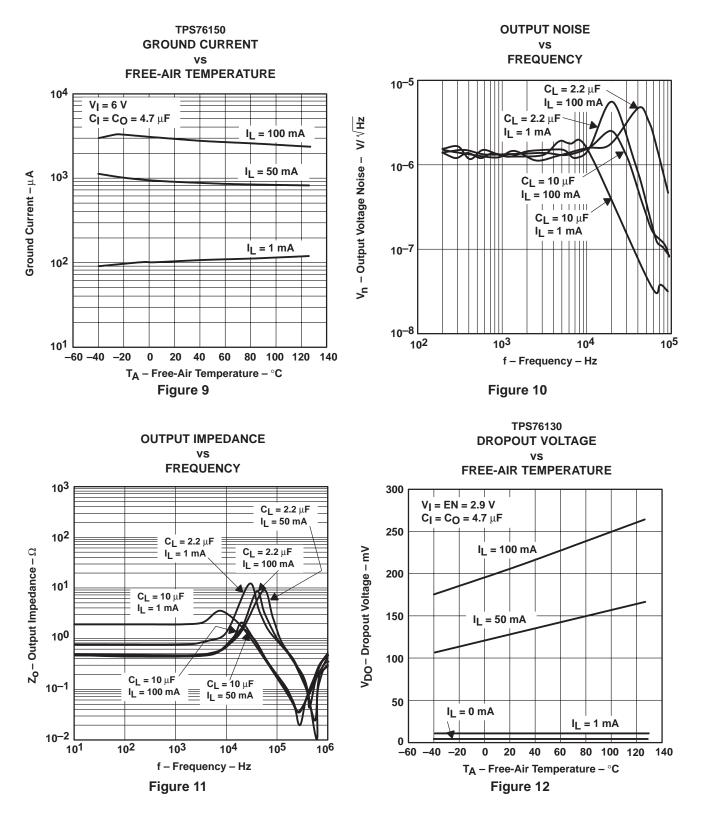


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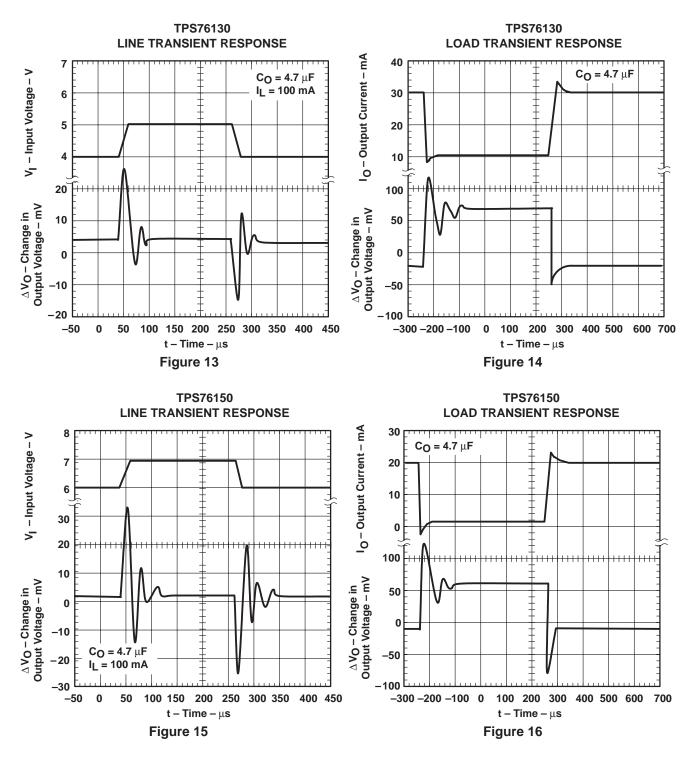


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### **APPLICATION INFORMATION**

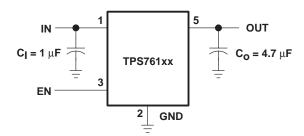


Figure 17. TPS761xx Typical Application

#### over current protection

The over current protection circuit forces the TPS761xx into a constant current output mode when the load is excessive or the output is shorted to ground. Normal operation resumes when the fault condition is removed.

NOTE:

An overload or short circuit may also activate the over temperature protection if the fault condition persists.

#### over temperature protection

The thermal protection system shuts the TPS761xx down when the junction temperature exceeds 160°C. The device recovers and operates normally when the temperature drops below 150°C.

#### input capacitor

A 1- $\mu$ F or larger ceramic decoupling capacitor with short leads connected between IN and GND is recommended. The decoupling capacitor may be omitted if there is a 1  $\mu$ F or larger electrolytic capacitor connected between IN and GND and located reasonably close to the TPS761xx. However, the small ceramic device is desirable even when the larger capacitor is present, if there is a lot of high frequency noise present in the system.

#### output capacitor

Like all low dropout regulators, the TPS761xx requires an output capacitor connected between OUT and GND to stabilize the internal control loop. The minimum recommended capacitance value is 4.7  $\mu$ F and the ESR (equivalent series resistance) must be between 0.1  $\Omega$  and 10  $\Omega$ . Solid tantalum electrolytic, aluminum electrolytic, and multilayer ceramic capacitors are all suitable, provided they meet the requirements described above. Most of the commercially available 4.7- $\mu$ F surface-mount solid-tantalum capacitors, including devices from Sprague, Kemet, and Nichicon, meet the ESR requirements stated above. Multilayer ceramic capacitors should have minimum values of 4.7  $\mu$ F over the full operating temperature range of the equipment.

#### enable (EN)

A logic zero on the enable input shuts the TPS761xx off and reduces the supply current to less than 1  $\mu$ A. Pulling the enable input high causes normal operation to resume. If the enable feature is not used, EN should be connected to IN to keep the regulator on all of the time. The EN input must not be left floating.

#### reverse current path

The power transistor used in the TPS761xx has an inherent diode connected between IN and OUT as shown in the functional block diagram. This diode conducts current from the OUT terminal to the IN terminal whenever IN is lower than OUT by a diode drop. This condition does not damage the TPS761xx provided the current is limited to 150 mA.

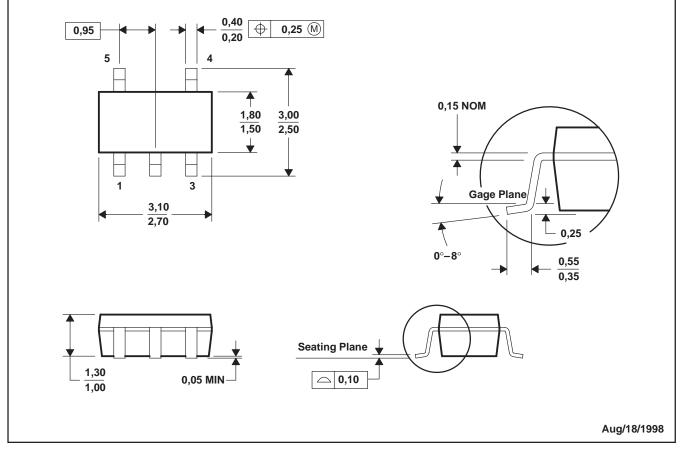


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**MECHANICAL DATA** 

#### DBV (R-PDSO-G5)

#### PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Body dimensions include mold flash or protrusion.



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