

# **HRW0702A**

# Silicon Schottky Barrier Diode for Rectifying

REJ03G0159-0600Z

(Previous: ADE-208-109E)

Rev.6.00 Jan.06.2004

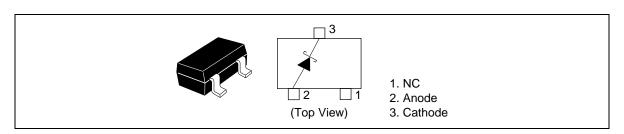
#### **Features**

- Low forward voltage drop and suitable for high efficiency rectifying.
- MPAK Package is suitable for high density surface mounting and high speed assembly.

# **Ordering Information**

Type No.	Laser Mark	Package Code	
HRW0702A	S15	MPAK	

#### **Pin Arrangement**



# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V <sub>RRM</sub> *1	20	V
Forward current	l <sub>F</sub> * <sup>1</sup>	700	mA
Non-Repetitive peak forward current	I <sub>FM</sub>	1.4	A
Non-Repetitive peak forward surge current	I <sub>FSM</sub> * <sup>2</sup>	5	Α
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Notes: 1. See from Fig.4 to Fig.6

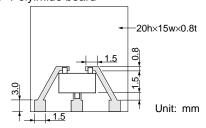
2. 10 ms sine wave 1 pulse

### **Electrical Characteristics**

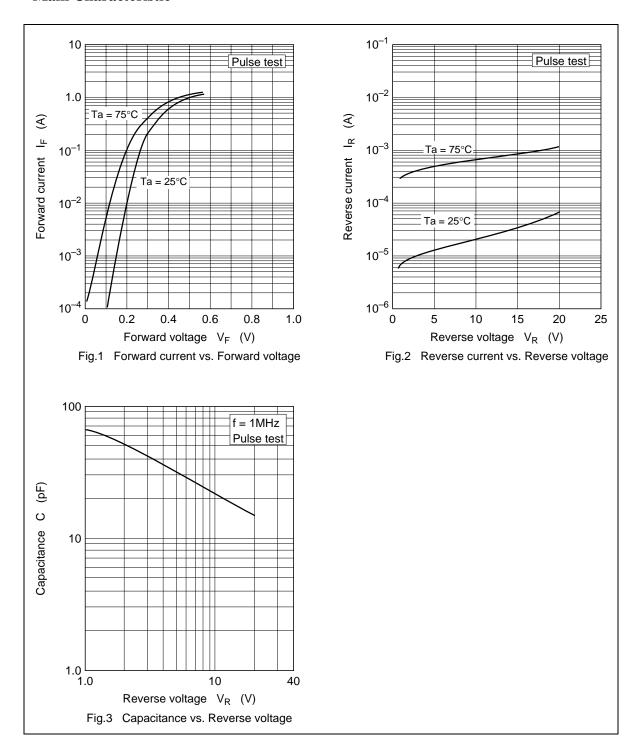
 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V <sub>F</sub>	_	_	0.43	V	I <sub>F</sub> = 700 mA
Reverse current	I <sub>R</sub>	_	_	200	μΑ	V <sub>R</sub> = 20 V
Capacitance	С	_	120	_	pF	V <sub>R</sub> = 0 V, f = 1 MHz
Thermal resistance	Rth(j-a)	_	340	_	°C/W	Polyimide board *1

Note: 1. Polyimide board



#### **Main Characteristic**



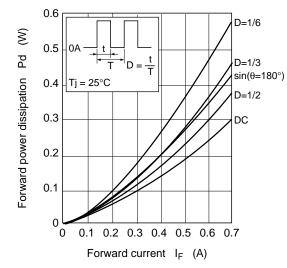


Fig.4 Forward power dissipation vs. Forward current

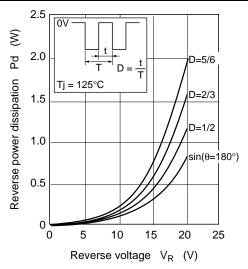


Fig.5 Reverse power dissipation vs. Reverse voltage

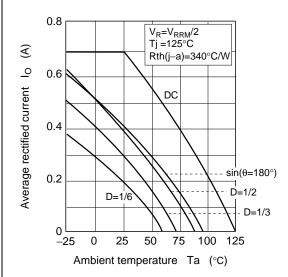
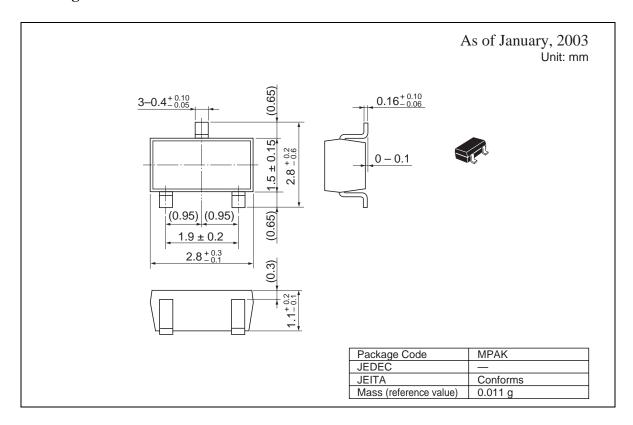


Fig.6 Average rectified current vs. Ambient temperature

# **Package Dimensions**



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Renesas Technology Singapore Pte. Ltd.
1, Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001