

## N-Channel JFETs

<b>J201</b>	<b>SST201</b>
<b>J202</b>	<b>SST202</b>
<b>J204</b>	<b>SST204</b>

<b>PRODUCT SUMMARY</b>				
<b>Part Number</b>	<b><math>V_{GS(off)}</math> (V)</b>	<b><math>V_{(BR)GSS}</math> Min (V)</b>	<b><math>g_{fs}</math> Min (mS)</b>	<b><math>I_{DSS}</math> Min (mA)</b>
J/SST201	-0.3 to -1.5	-40	0.5	0.2
J/SST202	-0.8 to -4	-40	1	0.9
J/SST204	-0.3 to -2	-25	0.5	0.2

### FEATURES

- Low Cutoff Voltage: J201 <1.5 V
- High Input Impedance
- Very Low Noise
- High Gain:  $A_V = 80$  @ 20  $\mu$ A

### BENEFITS

- Full Performance from Low Voltage Power Supply: Down to 1.5 V
- Low Signal Loss/System Error
- High System Sensitivity
- High Quality Low-Level Signal Amplification

### APPLICATIONS

- High-Gain, Low-Noise Amplifiers
- Low-Current, Low-Voltage Battery-Powered Amplifiers
- Infrared Detector Amplifiers
- Ultra High Input Impedance Pre-Amplifiers

### DESCRIPTION

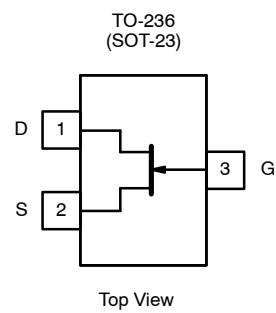
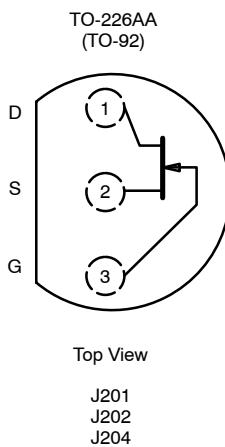
The J/SST201 series features low leakage, very low noise, and low cutoff voltage for use with low-level power supplies. The J/SST201 is excellent for battery powered equipment and low current amplifiers.

The J series, TO-226 (TO-92) plastic package, provides low cost, while the SST series, TO-236 (SOT-23) package, provides surface-mount capability. Both the J and SST series

are available in tape-and-reel for automated assembly (see Packaging Information).

For similar products in TO-206AA (TO-18) packaging, see the 2N4338/4339/4340/4341 data sheet.

For applications information see AN102 and AN106.



\*Marking Code for TO-236

**ABSOLUTE MAXIMUM RATINGS**

Gate-Drain, Gate-Source Voltage .....	-40 V
Gate Current .....	50 mA
Lead Temperature ( $1/16$ " from case for 10 sec.) .....	300°C
Storage Temperature .....	-55 to 150°C

Operating Junction Temperature .....	-55 to 150°C
Power Dissipation <sup>a</sup> .....	350 mW

## Notes

a. Derate 2.8 mW/°C above 25°C

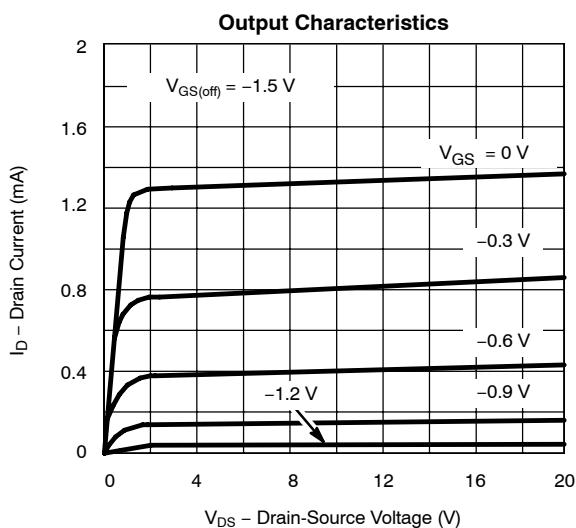
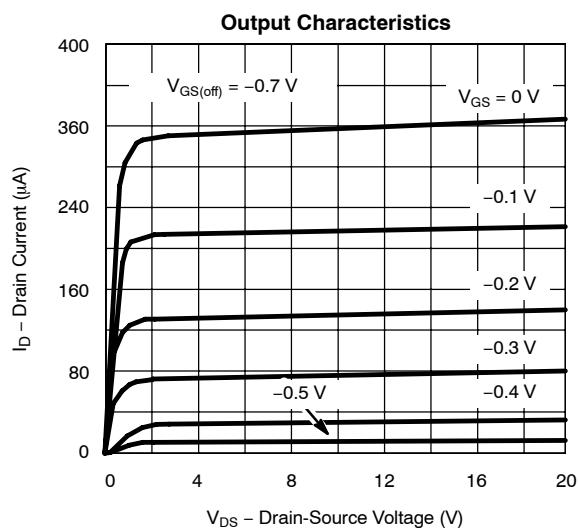
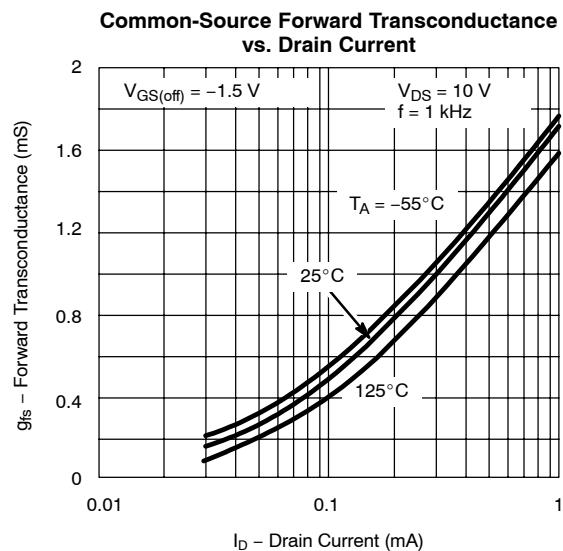
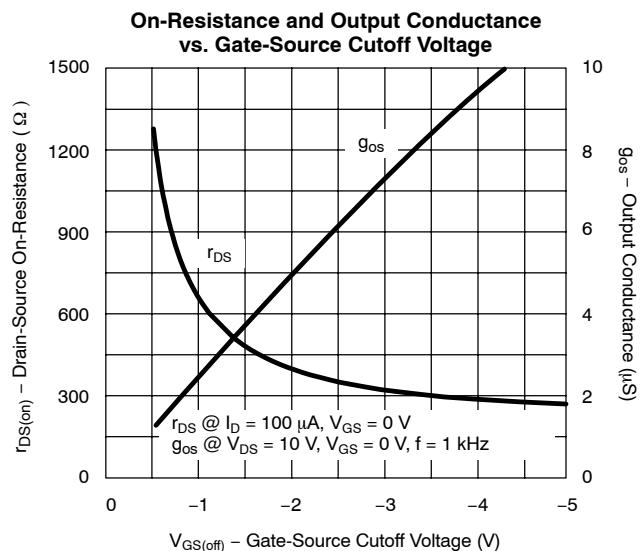
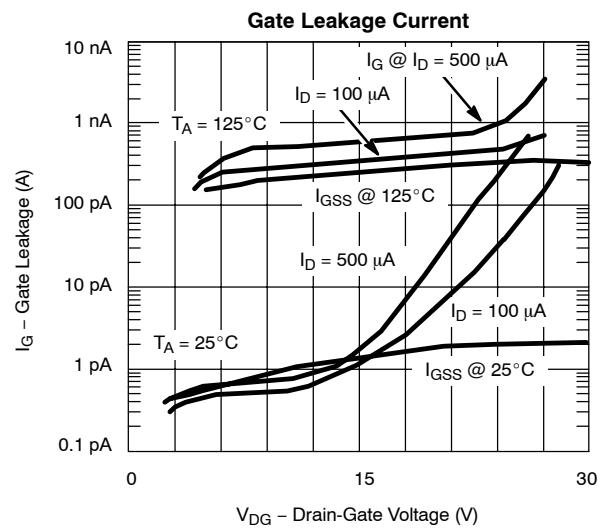
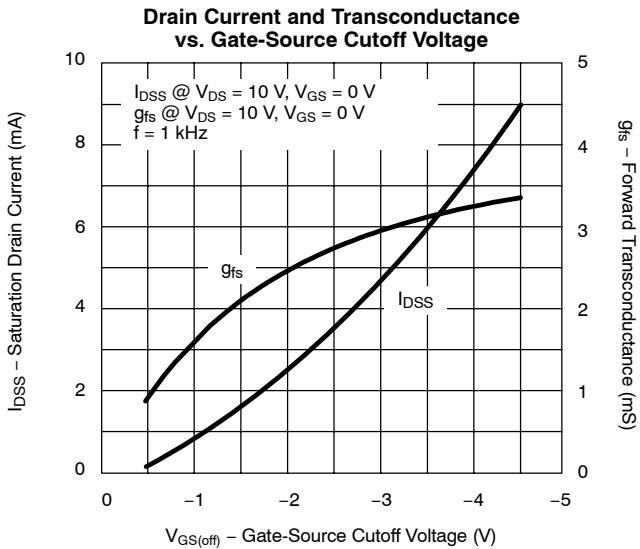
**SPECIFICATIONS ( $T_A = 25^\circ\text{C}$  UNLESS OTHERWISE NOTED)**

Parameter	Symbol	Test Conditions	Typ <sup>a</sup>	Limits						Unit
				J/SST201		J/SST202		J/SST204 <sup>c</sup>		
Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	
<b>Static</b>										
Gate-Source Breakdown Voltage	$V_{(\text{BR})\text{GSS}}$	$I_G = -1 \mu\text{A}, V_{DS} = 0 \text{ V}$		-40		-40		-25		V
Gate-Source Cutoff Voltage	$V_{GS(\text{off})}$	$V_{DS} = 15 \text{ V}, I_D = 10 \text{ nA}$		-0.3	-1.5	-0.8	-4	-0.3	-2	
Saturation Drain Current <sup>b</sup>	$I_{DSS}$	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}$		0.2	1	0.9	4.5	0.2	3	mA
Gate Reverse Current	$I_{GSS}$	$V_{GS} = -20 \text{ V}, V_{DS} = 0 \text{ V}$	-2		-100		-100		-100	pA
		$T_A = 125^\circ\text{C}$	-1							nA
Gate Operating Current	$I_G$	$V_{DG} = 10 \text{ V}, I_D = 0.1 \text{ mA}$	-2							
Drain Cutoff Current	$I_{D(\text{off})}$	$V_{DS} = 15 \text{ V}, V_{GS} = -5 \text{ V}$	2							pA
Gate-Source Forward Voltage	$V_{GS(F)}$	$I_G = 1 \text{ mA}, V_{DS} = 0 \text{ V}$	0.7							V
<b>Dynamic</b>										
Common-Source Forward Transconductance	$g_{fs}$	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}$ $f = 1 \text{ kHz}$		0.5		1		0.5		ms
Common-Source Input Capacitance	$C_{iss}$	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}$ $f = 1 \text{ MHz}$	4.5							pF
Common-Source Reverse Transfer Capacitance	$C_{rss}$		1.3							
Equivalent Input Noise Voltage	$\bar{e}_n$	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}$ $f = 1 \text{ kHz}$	6							$\text{nV}/\sqrt{\text{Hz}}$

## Notes

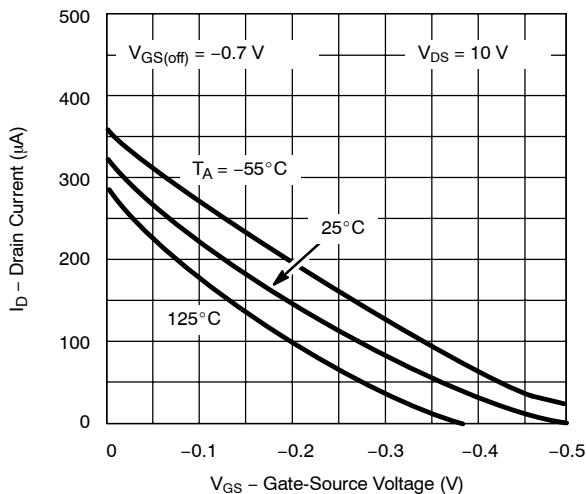
- a. Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.  
b. Pulse test: PW  $\leq 300 \mu\text{s}$  duty cycle  $\leq 3\%$ .  
c. See 2N/SST5484 Series for J204 and SST204 typical characteristic curves.

NPA, NH

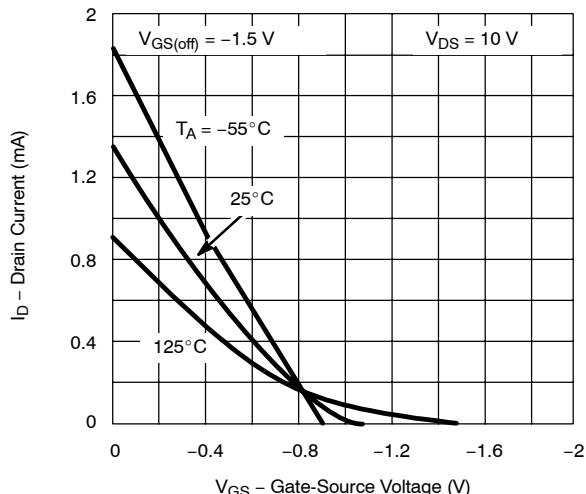
**TYPICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$  UNLESS OTHERWISE NOTED)**


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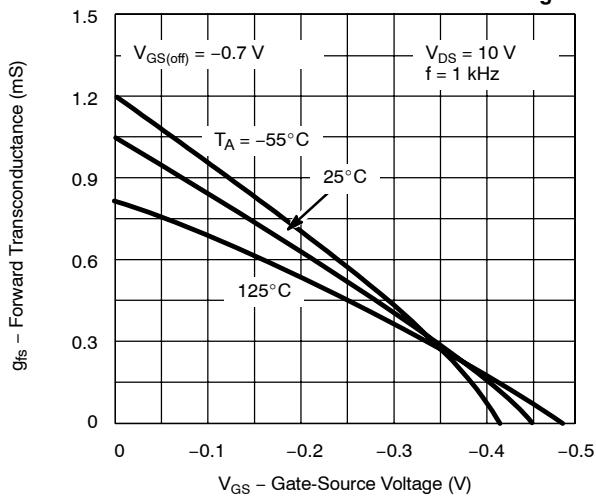
Transfer Characteristics



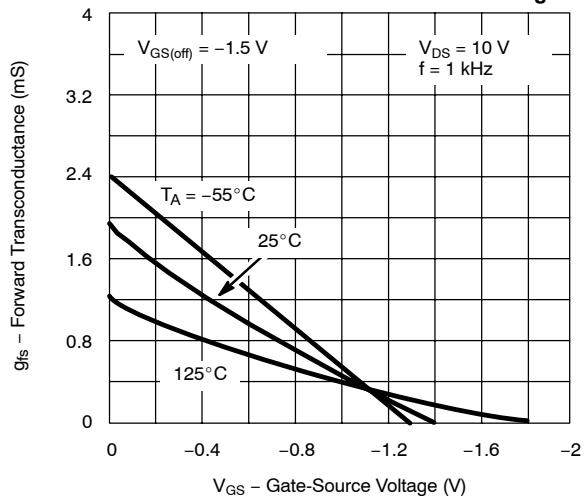
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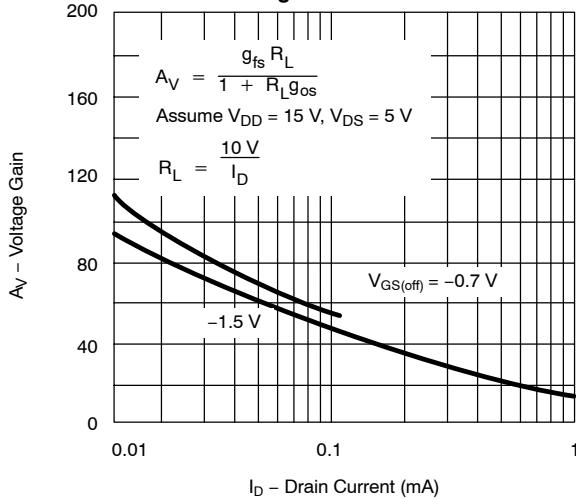
Transconductance vs. Gate-Source Voltage



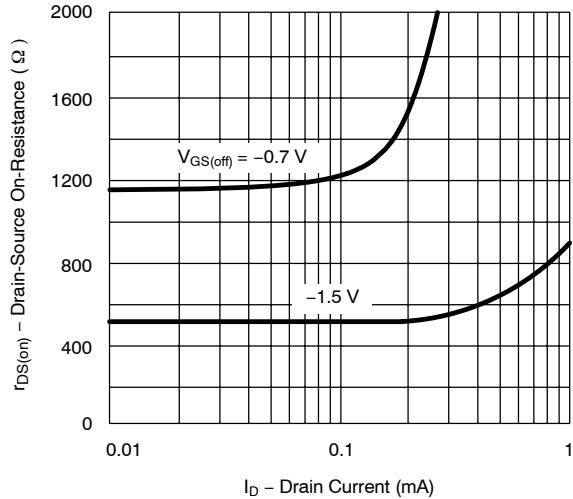
Transconductance vs. Gate-Source Voltage



Circuit Voltage Gain vs. Drain Current



On-Resistance vs. Drain Current



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