



**2SK3486**

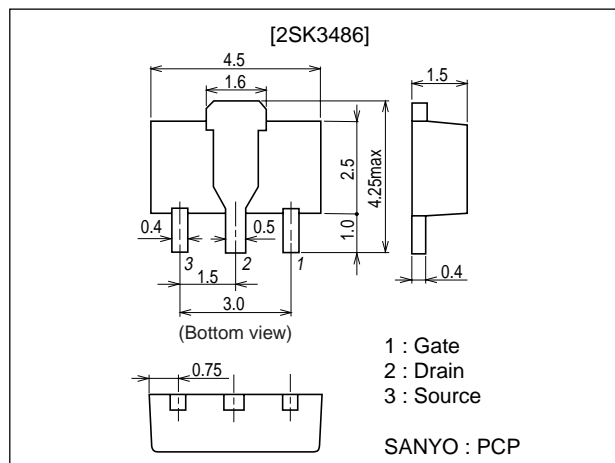
**Ultrahigh-Speed Switching Applications**

**Features**

- Low ON-resistance.
- Ultrahigh-speed switching.
- 2.5V drive.

**Package Dimensions**

unit : mm  
2062A



**Specifications**

**Absolute Maximum Ratings** at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		20	V
Gate-to-Source Voltage	V <sub>GSS</sub>		±10	V
Drain Current (DC)	I <sub>D</sub>		8	A
Drain Current (Pulse)	I <sub>DP</sub>	PW≤10μs, duty cycle≤1%	32	A
Allowable Power Dissipation	P <sub>D</sub>	Mounted on a ceramic board (250mm²×0.8mm)	1.5	W
		T <sub>c</sub> =25°C	3.5	W
Channel Temperature	T <sub>ch</sub>		150	°C
Storage Temperature	T <sub>stg</sub>		-55 to +150	°C

**Electrical Characteristics** at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =1mA, V <sub>GS</sub> =0	20			V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =0			1	μA
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0			±10	μA
Cutoff Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	0.4		1.3	V
Forward Transfer Admittance	y <sub>fs</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =4A	7	10		S
Static Drain-to-Source On-State Resistance	R <sub>DS(on)1</sub>	I <sub>D</sub> =4A, V <sub>GS</sub> =4V		33	43	mΩ
	R <sub>DS(on)2</sub>	I <sub>D</sub> =2A, V <sub>GS</sub> =2.5V		40	56	mΩ

Marking : LC

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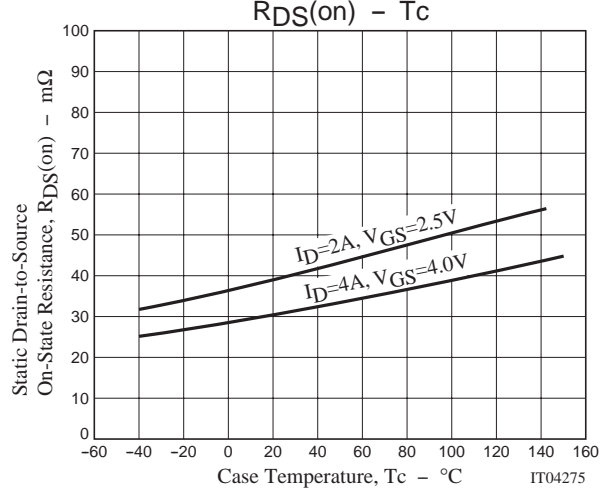
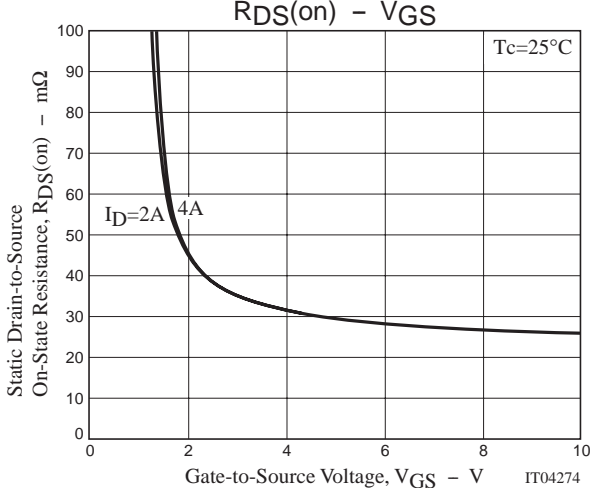
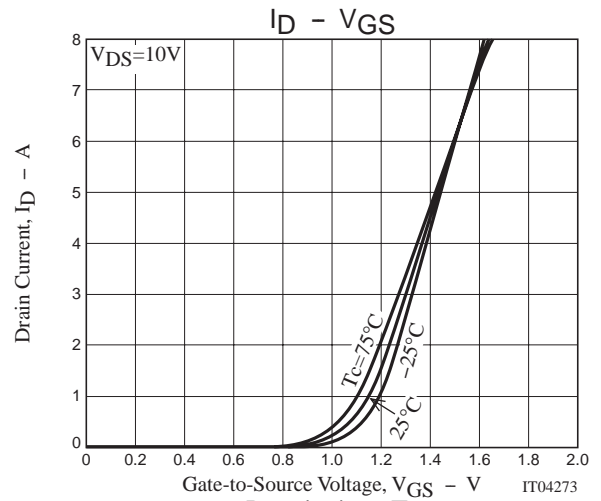
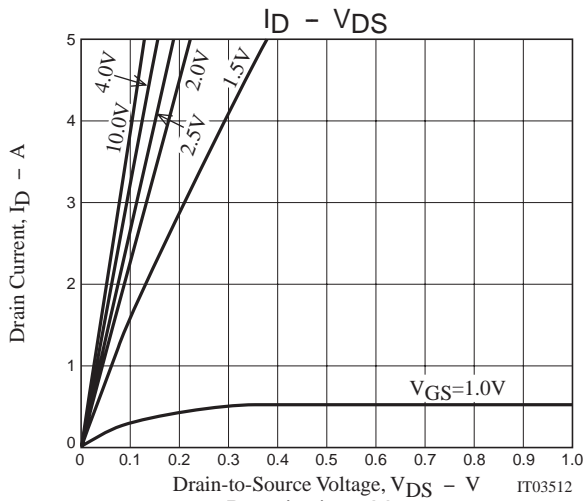
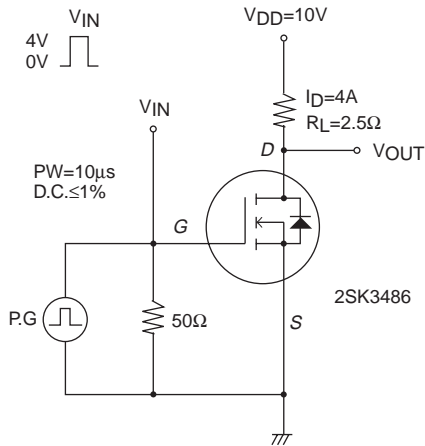
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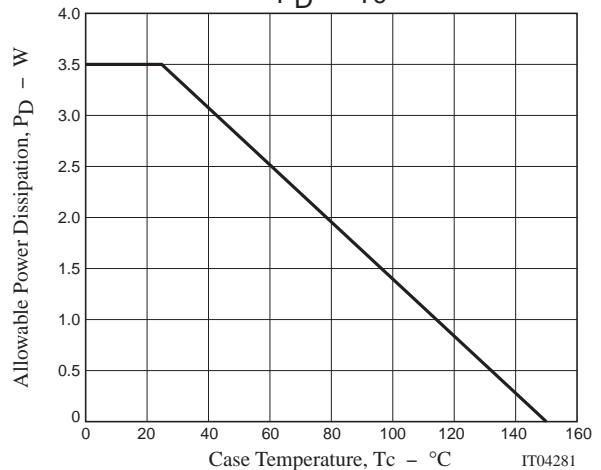
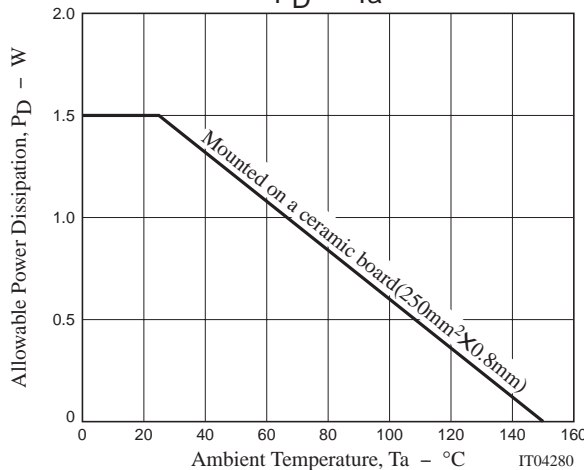
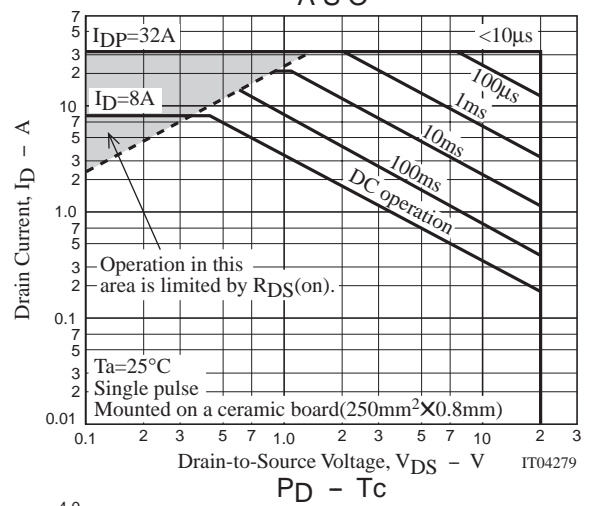
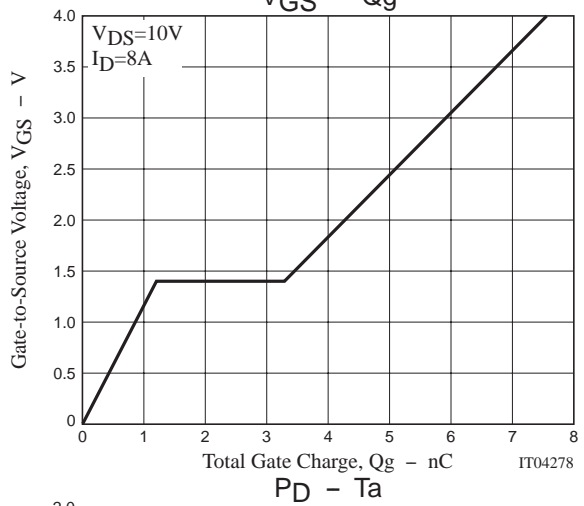
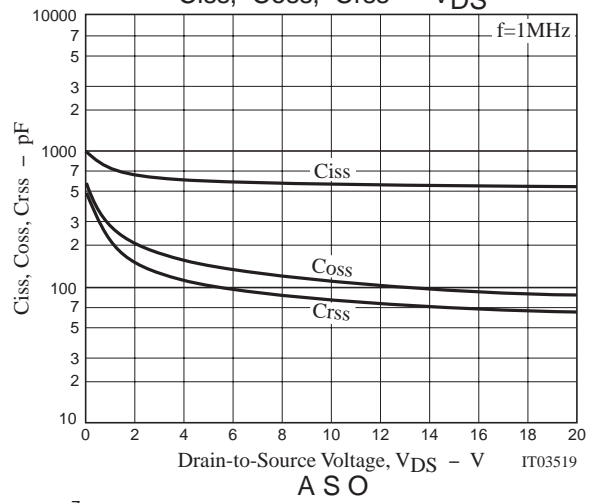
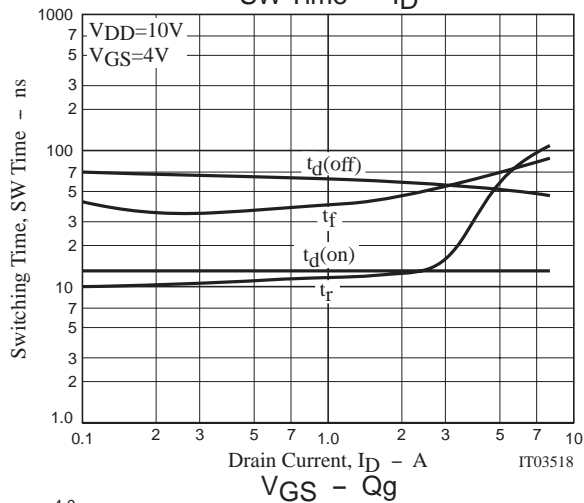
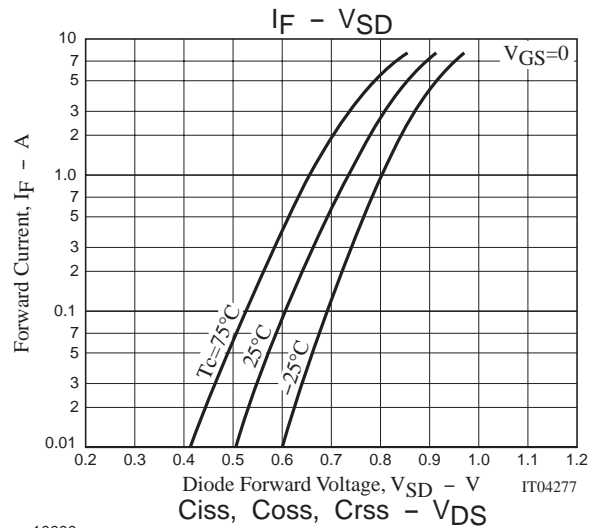
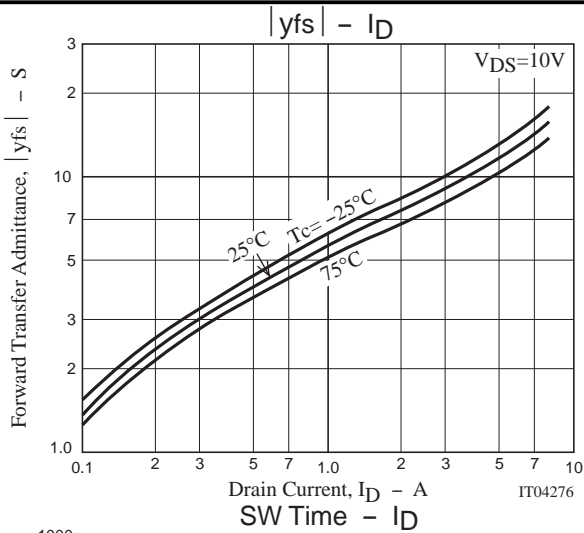
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	$C_{iss}$	$V_{DS}=10V, f=1MHz$		570		pF
Output Capacitance	$C_{oss}$	$V_{DS}=10V, f=1MHz$		110		pF
Reverse Transfer Capacitance	$C_{rss}$	$V_{DS}=10V, f=1MHz$		80		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit.		13		ns
Rise Time	$t_r$	See specified Test Circuit.		28		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit.		53		ns
Fall Time	$t_f$	See specified Test Circuit.		65		ns
Total Gate Charge	$Q_g$	$V_{DS}=10V, V_{GS}=4V, I_D=8A$		7.6		nC
Gate-to-Source Charge	$Q_{gs}$	$V_{DS}=10V, V_{GS}=4V, I_D=8A$		1.2		nC
Gate-to-Drain "Miller" Charge	$Q_{gd}$	$V_{DS}=10V, V_{GS}=4V, I_D=8A$		2.1		nC
Diode Forward Voltage	$V_{SD}$	$I_S=8A, V_{GS}=0$		0.9	1.2	V

## Switching Time Test Circuit





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