

MOS FIELD EFFECT TRANSISTOR 2SK3664

N-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

DESCRIPTION

The 2SK3664 is a switching device, which can be driven directly by a 2.5 V power source.

The device features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

FEATURES

- 2.5 V drive available
- · Low on-state resistance

 $R_{DS(on)1} = 0.57 \Omega MAX. (V_{GS} = 4.5 V, I_{D} = 0.30 A)$

RDS(on)2 = 0.60Ω MAX. (VGS = 4.0 V, ID = 0.30 A)

RDS(on)3 = 0.88Ω MAX. (VGS = 2.5 V, ID = 0.15 A)

ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK3664	SC-75 (USM)

Marking: G1

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vss = 0 V)	Voss	20	V
Gate to Source Voltage (VDS = 0 V)	Vgss	±12	V
Drain Current (DC)	ID(DC)	±0.5	Α
Drain Current (pulse) Note1	D(pulse)	±2.0	Α
Total Power Dissipation Note2	PT	0.2	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Mounted on ceramic substrate of 300 mm² x 0.64 mm

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

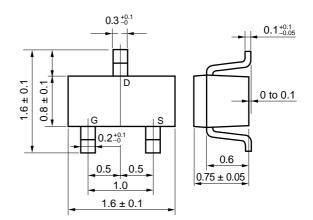
Caution This product is electrostatic-sensitive device due to low ESD capability and should be handled with caution for electrostatic discharge.

V_{ESD} = ± 200 V TYP. (C = 200 pF, R = 0 Ω , Single pulse)

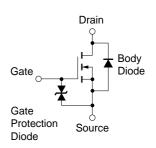
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PACKAGE DRAWING (Unit: mm)



EQUIVALENT CIRCUIT



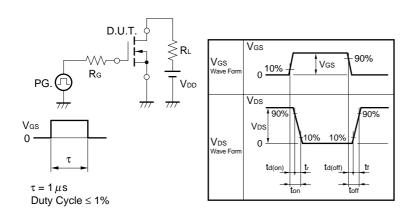


ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 20 V, V _{GS} = 0 V			1.0	μΑ
Gate Leakage Current	Igss	Vgs = ±12 V, Vps = 0 V			±10	μΑ
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1.0 mA	0.5	1.0	1.5	V
Forward Transfer Admittance Note	yfs	V _{DS} = 10 V, I _D = 0.30 A	0.25	0.75		S
Drain to Source On-state Resistance Note	R _{DS(on)1}	Vss = 4.5 V, ID = 0.30 A		0.38	0.57	Ω
	RDS(on)2	Vgs = 4.0 V, ID = 0.30 A		0.41	0.60	Ω
	RDS(on)3	V _{GS} = 2.5 V, I _D = 0.15 A		0.60	0.88	Ω
Input Capacitance	Ciss	Vps = 10 V		28		pF
Output Capacitance	Coss	V _G S = 0 V		11		pF
Reverse Transfer Capacitance	Crss	f = 1.0 MHz		7		pF
Turn-on Delay Time	td(on)	V _{DD} = 10 V, I _D = 0.30 A		20		ns
Rise Time	tr	V _{GS} = 4.0 V		51		ns
Turn-off Delay Time	td(off)	R _G = 10 Ω		94		ns
Fall Time	t f			87		ns
Body Diode Forward Voltage Note	V _F (S-D)	IF = 0.50 A, VGS = 0 V		0.87		V

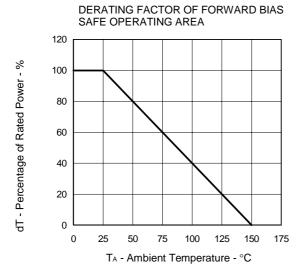
Note Pulsed: PW \leq 350 μ s, Duty Cycle \leq 2%

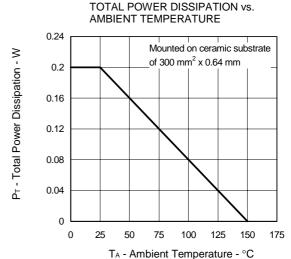
TEST CIRCUIT SWITCHING TIME

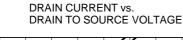


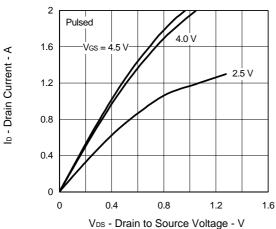
NEC

TYPICAL CHARACTERISTICS (TA = 25°C)

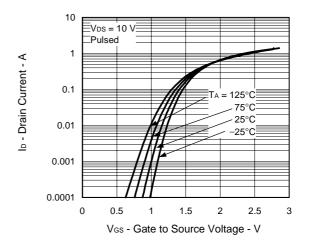




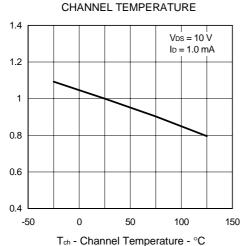




FORWARD TRANSFER CHARACTERISTICS

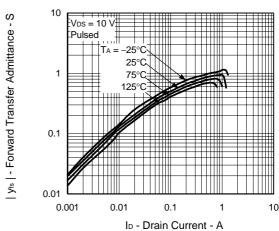


GATE CUT-OFF VOLTAGE vs.

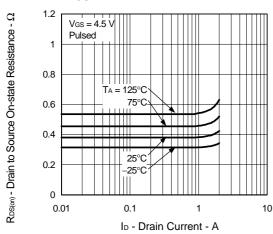


Ves(off) - Gate Cut-off Voltage - V

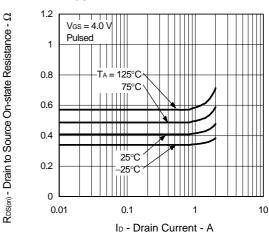
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



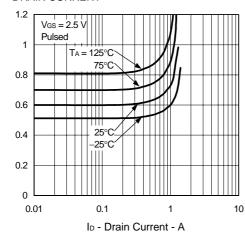
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



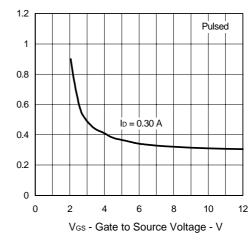
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



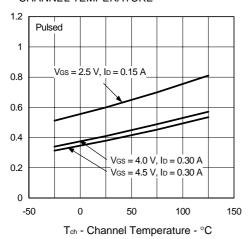
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



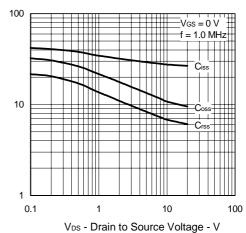
R_{DS(on)} - Drain to Source On-state Resistance - Ω

Ciss, Coss, Crss - Capacitance - pF

DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



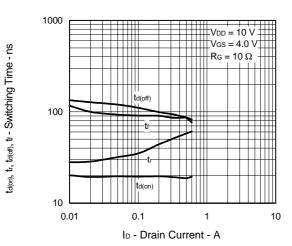
CAPACITANCE vs.
DRAIN TO SOURCE VOLTAGE



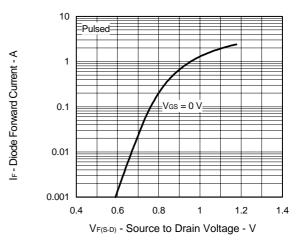
 $\mathsf{R}_{\mathsf{DS}(\varpi)}$ - Drain to Source On-state Resistance - Ω

 $\mathsf{R}_{\mathsf{DS}(\mathsf{on})}$ - Drain to Source On-state Resistance - Ω

SWITCHING CHARACTERISTICS



SOURCE TO DRAIN DIODE FORWARD VOLTAGE



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