TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7WZU04FU,TC7WZU04FK

Triple Inverter (Un-Buffer)

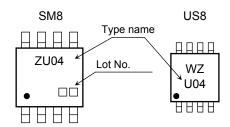
#### **Features**

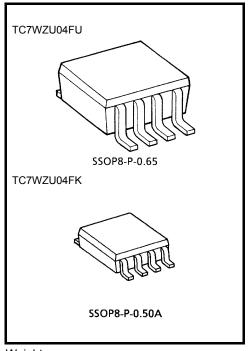
- High output drive: ±16 mA (min) at V<sub>CC</sub> = 4.5 V
- Low quiescent power: I<sub>CC</sub> < 1μA (max.)</li>

at  $V_{CC} = 5.5 \text{ V}$ , Ta=25°C

- Operation voltage range: V<sub>CC (opr)</sub> = 1.65~5.5 V
- 5.5-V tolerant inputs

#### Marking





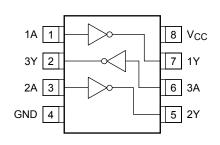
Weight

SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol	Rating	Unit
Power supply voltage	Vcc	-0.5~6	V
DC input voltage	V <sub>IN</sub>	-0.5~6	V
DC output voltage	V <sub>OUT</sub>	-0.5~V <sub>CC</sub> + 0.5V	V
Input diode current	lık	-20	mA
Output diode current	lok	±20	mA
DC output current	lout	±50	mA
DC V <sub>CC</sub> /ground current	Icc	±50	mA
Power dissipation	PD	300 (SM8) 200 (US8)	mW
Storage temperature	T <sub>stg</sub>	-65~150	°C
Lead temperature (10s)	TL	260	°C

### Pin Assignment (top view)



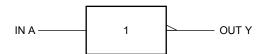
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

# **Truth Table**

А	Y
L	Н
Н	L

# Logic Diagram



# **Operating Ranges**

Characteristics	Symbol	Rating	Unit
Supply voltage	Vac	1.65~5.5	V
	V <sub>CC</sub>	1.5~5.5 (Note 1)	
Input voltage	V <sub>IN</sub>	0~5.5	V
Output voltage	V <sub>OUT</sub>	0~V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	-40~85	°C

Note 1: Data retention only



### **Electrical Characteristics**

### **DC Characteristics**

Characteristics		Symbol	Symbol Test Condition		Condition		Ta = 25°C			Ta = -40~85°C	
		Syllibol			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
Input voltage	High lovel	V	-		1.65~ 2.7	0.85 × V <sub>CC</sub>	_	_	0.85 × V <sub>CC</sub>	_	V
	High level	V <sub>IH</sub>			3.0~5.5	0.8 × V <sub>CC</sub>	_	_	0.8 × V <sub>CC</sub>	_	
	Low level	V <sub>IL</sub>	_		1.65~ 2.7			0.15 × V <sub>CC</sub>	_	0.15 × V <sub>CC</sub>	
	Low level	VIL			3.0~5.5			0.2 × V <sub>CC</sub>		$\begin{array}{c} 0.2 \\ \times  V_{CC} \end{array}$	
					1.65	1.45	1.64	_	1.45	_	
			Vini – Vii	I <sub>OH</sub> = -100 μA	2.3	2.1	2.29	_	2.1	_	
			VIN — VIL	10Н = -100 ДА	3.0	2.7	2.99	_	2.7	_	V
					4.5	4.0	4.48	_	4.0	_	
	High level	Vон	V <sub>IN</sub> =GND	$I_{OH} = -2 \text{ mA}$	1.65	1.29	1.52	_	1.29	_	
				$I_{OH} = -4 \text{ mA}$	2.3	1.9	2.19	_	1.9	_	
				$I_{OH} = -8 \text{ mA}$	3.0	2.4	2.82	_	2.4	_	
				I <sub>OH</sub> = -12 mA	3.0	2.3	2.73	_	2.3	_	
Output				I <sub>OH</sub> = -16 mA	4.5	3.8	4.24	_	3.8	_	
voltage	Low level	V <sub>OL</sub>	$V_{IN} = V_{IH}$	Ι <sub>ΟL</sub> = 100 μΑ	1.65	_	0.01	0.2	_	0.2	
					2.3	_	0.01	0.2	_	0.2	
					3.0	_	0.01	0.3	_	0.3	
					4.5	_	0.01	0.5	_	0.5	
			V <sub>IN</sub> =VCC	I <sub>OL</sub> = 2 mA	1.65	_	0.08	0.24	_	0.24	
				I <sub>OL</sub> = 4 mA	2.3	_	0.12	0.3	_	0.3	
				I <sub>OL</sub> = 8 mA	3.0	_	0.19	0.4	_	0.4	
				I <sub>OL</sub> = 12 mA	3.0	_	0.29	0.55	_	0.55	
				I <sub>OL</sub> = 16 mA	4.5	_	0.29	0.55	_	0.55	
Input leakage	current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V	or GND	0~5.5	_	_	±1	_	±10	μΑ
Quiescent su	pply current	Icc	$V_{IN} = 5.5 V$	or GND	1.65~5.5	_	—	1		10	μΑ

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# AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Cumbal	Test Condition		Ta = 25°C		Ta = -40~85°C		Unit	
Characteristics	Symbol	rest Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	t <sub>pLH</sub>	$C_L$ = 15 pF, $R_L$ = 1 $M\Omega$	$1.8\pm0.15$	1.5	4.6	8.1	1.5	8.9	- ns
			2.5 ± 0.2	1.2	3.3	5.7	1.2	6.3	
			$3.3 \pm 0.3$	0.8	2.7	4.1	0.8	4.5	
			5.0 ± 0.5	0.5	2.2	3.3	0.5	3.6	
	t <sub>pHL</sub>	$C_L = 50 \text{ pF}, R_L = 500 \Omega$	$3.3 \pm 0.3$	1.2	4.0	6.4	1.2	7.0	
			$5.0\pm0.5$	0.8	3.4	5.6	0.8	6.2	
Input capacitance	C <sub>IN</sub>	_	0~5.5		5.4		_		pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 2)	3.3		9.8		_		- pF
			5.5	_	22	_	_	_	

Note 2: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

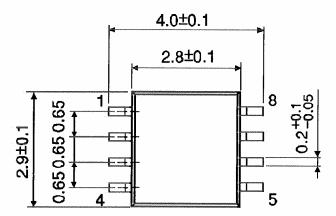
Average operating current can be obtained by the equation:

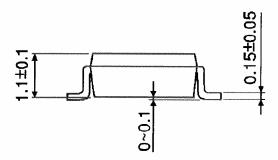
 $I_{CC \text{ (opr.)}} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$ 

Unit: mm

# **Package Dimensions**

SSOP8-P-0.65





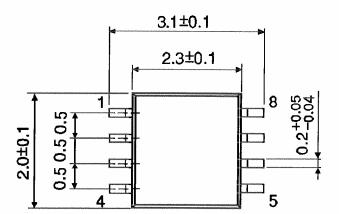
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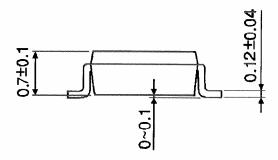
Weight: 0.02 g (typ.)

Unit: mm

# **Package Dimensions**

SSOP8-P-0.50A





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Weight: 0.01 g (typ.)

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20070701-EN GENERAL

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