

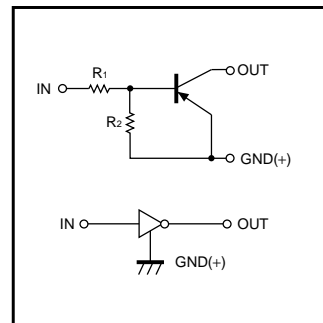
# Digital transistors (built-in resistors)

## DTA114EM / DTA114EE / DTA114EUA / DTA114ECA / DTA114EKA / DTA114ESA

### ●Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3) Only the on/off conditions need to be set for operation, making device design easy.

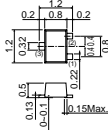
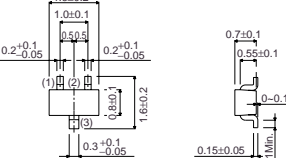
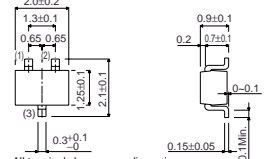
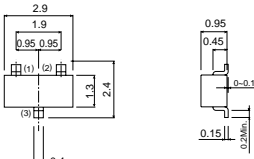
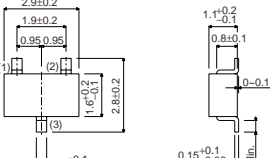
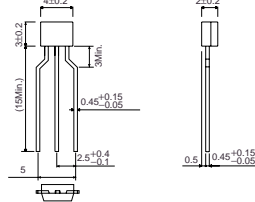
### ●Equivalent circuit



### ●Structure

PNP digital transistor (Built-in resistor type)

### ●External dimensions (Units : mm)

<p>DTA114EM</p>  <p>ROHM : VMT3 Abbreviated symbol : 14</p> <p>(1) IN (2) GND (3) OUT</p>	<p>DTA114EE</p>  <p>ROHM : EMT3 Abbreviated symbol : 14</p> <p>(1) GND (2) IN (3) OUT</p>
<p>DTA114EUA</p>  <p>ROHM : UMT3 EIAJ : SC-70 All terminals have same dimensions Abbreviated symbol : 14</p> <p>(1) GND (2) IN (3) OUT</p>	<p>DTA114ECA</p>  <p>ROHM : SST3 Each lead has same dimensions Abbreviated symbol : 14</p> <p>(1) GND (2) IN (3) OUT</p>
<p>DTA114EKA</p>  <p>ROHM : SMT3 EIAJ : SC-59 All terminals have same dimensions Abbreviated symbol : 14</p> <p>(1) GND (2) IN (3) OUT</p>	<p>DTA114ESA</p>  <p>ROHM : SPT EIAJ : SC-72</p> <p>(1) GND (2) OUT (3) IN</p>

# DTA114EM / DTA114EE / DTA114EUA DTA114ECA / DTA114EKA / DTA114ESA

## Transistors

### ●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits(DTA114E□)						Unit
		M	E	UA	CA	KA	SA	
Supply voltage	V <sub>CC</sub>	-50						V
Input voltage	V <sub>IN</sub>	-40~+10						V
Output current	I <sub>o</sub>	-50						mA
	I <sub>C(Max.)</sub>	-100						
Power dissipation	P <sub>d</sub>	150		200		300	mW	
Junction temperature	T <sub>j</sub>	150						°C
Storage temperature	T <sub>stg</sub>	-55~+150						°C

### ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	V <sub>I(off)</sub>	-	-	-0.5	V	V <sub>CC</sub> =-5V, I <sub>o</sub> =-100μA
	V <sub>I(on)</sub>	-3	-	-		V <sub>o</sub> =-0.3V, I <sub>o</sub> =-10mA
Output voltage	V <sub>O(on)</sub>	-	-	-0.3	V	I <sub>o</sub> /I <sub>E</sub> =-10mA/-0.5mA
Input current	I <sub>i</sub>	-	-	-0.88	mA	V <sub>I</sub> =-5V
Output current	I <sub>o(off)</sub>	-	-	-0.5	μA	V <sub>CC</sub> =-50V, V <sub>I</sub> =0V
DC current gain	G <sub>i</sub>	30	-	-	-	V <sub>o</sub> =-5V, I <sub>o</sub> =-5mA
Input resistance	R <sub>1</sub>	7	10	13	kΩ	-
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	0.8	1	1.2	-	-
Transition frequency	f <sub>t</sub>	-	250	-	MHz	V <sub>CE</sub> =-10V, I <sub>E</sub> =5mA, f=100MHz *

\* Transition frequency of the device

### ●Packaging specifications

Type	Package	VMT3	EMT3	UMT3	SST3	SMT3	SPT
	Packaging type	Taping	Taping	Taping	Taping	Taping	Taping
	Code	T2L	TL	T106	T116	T146	TP
	Basic ordering unit (pieces)	8000	3000	3000	3000	3000	5000
DTA114EM		○	-	-	-	-	-
DTA114EE		-	○	-	-	-	-
DTA114EUA		-	-	○	-	-	-
DTA114ECA		-	-	-	○	-	-
DTA114EKA		-	-	-	-	○	-
DTA114ESA		-	-	-	-	-	○

DTA114EM / DTA114EE / DTA114EUA  
DTA114ECA / DTA114EKA / DTA114ESA

Transistors

●Electrical characteristic curves

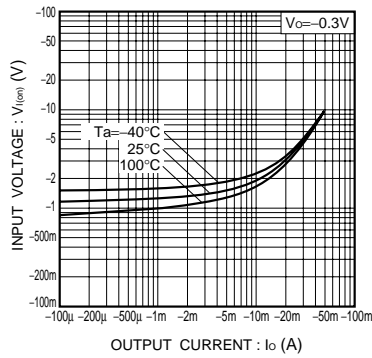


Fig.1 Input voltage vs. output current (ON characteristics)

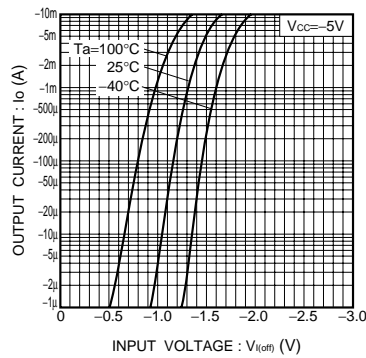


Fig.2 Output current vs. input voltage (OFF characteristics)

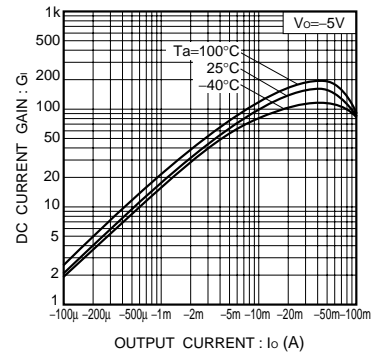


Fig.3 DC current gain vs. output current

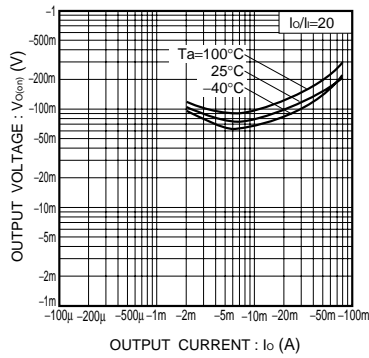


Fig.4 Output voltage vs. output current

### Notes

- No technical content pages of this document may be reproduced in any form or transmitted by any means without prior permission of ROHM CO.,LTD.
- The contents described herein are subject to change without notice. The specifications for the product described in this document are for reference only. Upon actual use, therefore, please request that specifications to be separately delivered.
- Application circuit diagrams and circuit constants contained herein are shown as examples of standard use and operation. Please pay careful attention to the peripheral conditions when designing circuits and deciding upon circuit constants in the set.
- Any data, including, but not limited to application circuit diagrams information, described herein are intended only as illustrations of such devices and not as the specifications for such devices. ROHM CO.,LTD. disclaims any warranty that any use of such devices shall be free from infringement of any third party's intellectual property rights or other proprietary rights, and further, assumes no liability of whatsoever nature in the event of any such infringement, or arising from or connected with or related to the use of such devices.
- Upon the sale of any such devices, other than for buyer's right to use such devices itself, resell or otherwise dispose of the same, no express or implied right or license to practice or commercially exploit any intellectual property rights or other proprietary rights owned or controlled by
- ROHM CO., LTD. is granted to any such buyer.
- Products listed in this document use silicon as a basic material.  
Products listed in this document are no antiradiation design.

The products listed in this document are designed to be used with ordinary electronic equipment or devices (such as audio visual equipment, office-automation equipment, communications devices, electrical appliances and electronic toys).

Should you intend to use these products with equipment or devices which require an extremely high level of reliability and the malfunction of which would directly endanger human life (such as medical instruments, transportation equipment, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), please be sure to consult with our sales representative in advance.

#### About Export Control Order in Japan

Products described herein are the objects of controlled goods in Annex 1 (Item 16) of Export Trade Control Order in Japan.

In case of export from Japan, please confirm if it applies to "objective" criteria or an "informed" (by MITI clause) on the basis of "catch all controls for Non-Proliferation of Weapons of Mass Destruction.