TOSHIBA Diode Silicon Epitaxial Schottky Barrier Type

1SS425

High-Speed Switching Applications

• Low forward voltage: V_{F (3)} = 0.50 V (typ.)

• Low reverse current: I_R = 0.5µA (max)

• Small total capacitance: C_T = 3.9 pF (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Maximum (peak) reverse Voltage	V_{RM}	25	(V)
Reverse voltage	V _R	20	
Maximum (peak) forward current	I _{FM}	100	mA
Average forward current	Io	50	mA
Surge current (10 ms)	I _{FSM}	1	\rightarrow A
Power dissipation	P*	100	mW
Junction temperature	Tj	125	°Ç
Storage temperature range	T _{stg}	-55 to 125	/ec

^{*:} Mounted on a glass epoxy circuit board of 20 mm × 20 mm, Cu pad dimension of 4 mm × 4 mm.

Unit: mm

0.52±0.03

0.2±0.05

0.2±0.05

0.6±0.05

JEDEC

JEITA

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1-1K1A

Weight: 0.0011 g (typ.)

†ØSHIBA

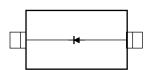
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure-rate, etc).

Electrical Characteristics (Ta = 25°C)

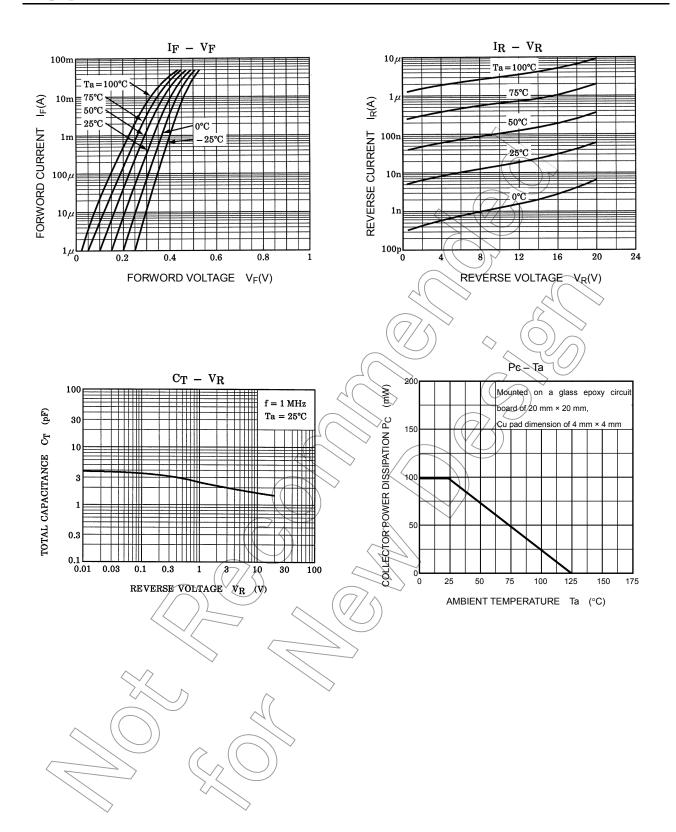
Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
	VF (1)	_	I _F = 1mA	_	0.33	_	
Forward voltage	V _F (2)	_	I _F = 5mA	-	0.38	-	V
	VF (3)	_	I _F = 50mA	_	0.50	0.55	
Reverse current	I _R	_	V _R = 20V	_	_	0.5	μΑ
Total capacitance	C _T	_	V _R = 0, f = 1MH _z	1	3.9	_	pF

Equivalent Circuit (top view)



Marking





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