高周波用円筒セラミックコンデンサ (セラチップ)

TUBULAR TYPE CERAMIC CAPACITORS (FOR HIGH FREQUENCY)

OPERATING TEMP. −25°C~+85°C



特長 FEATURES

- ・高周波特性に優れる
 - ・インピーダンス特性が良好
- ・等価直列抵抗 (ESR) が小さい
- ・高周波でのQ値が高い
- ・ハンダくわれ・ぬれ性に対する端子電極対応により、ハンダ付けの信頼 性に優れる
- ・耐熱衝撃性に優れる
- ・寸法安定性に優れ、高い実装性を誇る
- ・基板曲げ時の耐ベンディング性に優れる
- ・チューナ特性に優れる

- · Excellent high-frequency characteristics:
 - * Good impedance characteristics
 - * Low equivalent series resistance
 - * Large Q-value at high frequencies
- · Compatible with 0603 and 0805 component solder pad dimensions
- · Highly resistant to heat and impact
- Excellent solderability and ability to withstand PCB bending
- · Excellent tuner characteristics

用途 APPLICATIONS

- ・通信機器用 携帯電話、PHS、コードレス電話etc
- ・民生機器用 チューナ、ビデオ、テレビetc

- Communications Equipment: portable telephones, PHS, other wireless applications, etc.
- Consumer Electronic Appliances: tuners, video equipment, television sets, etc.

形名表記法 ORDERING CODE

Ø

定格電圧 [VDC] U 50

2

分類	
CN	円筒コンデンサ

3

形状寸法 [mm]			
	033	1.6×1.0	
	053	2.0×1.25	

温度特性〔ppm/°C〕

$\triangle A$	± 5%		
△B	±10%		
C	0:CK, CJ, CH		許容差
R□	-220 : RK,RJ,RH		
S□	-330 : SK,SJ,SH	Н	± 60
Τ□	-470 : TK,TJ,TH	J	±120
U	-750 : UK,UJ	K	±250
SL	+350~-1000	L	±500

△=スペース

	9	
	公称静	電容量 [pF]
例		
	0R5	0.5
	010	1
	472	4700

R=小数点

容量許容差				
	10	pF以	下	10pF超
C△	±	0.25	рF	
D△	±	0.5	рF	
J△				± 5%
Κ△				±10%

△=スペース

| 7 | ②装 | 単品 (袋づめ) | -2 | テービング | -7 | バルクカセット品 | △=スペース

$_{\perp}$ U $_{\perp}$	$C \setminus N$	0 3 3	$B_{L}C_{L}H_{L}$	1 , 0 , 0	\square D \square \square \square	_ 2
1	2	3	4	5	6	7



Rated	voltage(VDC)
U	50

2

lype	
CN	Tubular capacitor

3

External Dimensions (mm			
	033	1.6×1.0	
	053	2.0×1.25	

A

remperature characteristics (ppm/ C.				
△A				
△B	±10%			
C	0:CK, CJ, CH	<u> </u>	:Tolerance	
R□	-220 : RK,RJ,RH			
S	-330 : SK,SJ,SH	Н	± 60	
T	-470 : TK,TJ,TH	J	±120	
U	-750 : UK,UJ	K	±250	
SL	+350~-1000	L	±500	
△=Blank space				

5

Nominal Capacitance(pF		
0R5	0.5	
010	1	
472	4700	
	*R=decimal point	

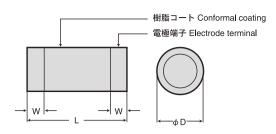
6

Capacitance Tolerances				
	10pF≦	10pF>		
C△	± 0.25 pl	F		
D△	± 0.5 pl	F		
J△	J△			
K△ ±10%				

7

Packa	ging			
$\triangle \triangle$	Bulk			
-2	Tape & reel			
-7	Bulk cassette			
	Diani, anges			

± 5% △=Blank space



Type	L	φD	W
033	$1.6 { + 0.2 \atop -0.1 \atop (0.063 { + 0.008 \atop -0.004})}$	1.0±0.1 (0.039±0.004)	$0.3 \begin{array}{c} +0.20 \\ 0.3 -0.15 \\ (0.012 \begin{array}{c} +0.008 \\ -0.006 \end{array}) \end{array}$
053	$2.0 {+0.3 \atop -0.1} \\ (0.079 {+0.012 \atop -0.004})$	1.25±0.2 (0.049±0.008)	$0.3 {+0.3 \atop -0.1} \\ (0.012 {+0.012 \atop -0.004})$

Unit: mm (inch)

概略バリエーション AVAILABLE CAPACITANCE RANGE

Class1 (Temperature compensating)

Classi	(remper	atur	- 00	mpe	riisa	ung							
V	V		50V (UCN)										
Temp	.char.	C		R		S		T[_	U		S	
Ту	ре	033	053	033	053	033	053	033	053	033	053	033	053
Cap													
[pF]	[pF 3digits]												
0.5	0R5												
0.75	R75												
1.5	010	_											
1.5	1R5												
2 2.5	020 2R5	_											
2.5	2R5												
3	030												
3.5	3R5												
4 4.5	040 4R5												
4.5													
5 6	050 060												
7	070												
8	080												
9	090												
10	100												
11	110												
12	120												
13	130												
15	130 150												
16	160												
18	180												
20	200												
22	220												
24	240												
27	270												
30	300												
33	330												
36	360												
39	390												
43	430												
47	470												
51 56	510 560												
68	680												
75	750												
82	820												
91	910												
100	101												
100 120	121												
150	151												

Class2 (High dielectric constant)

0.000_	(i ligit alc		.0 00	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
W	V	50	V (U	CN)
Temp.	char.	Α	E	3
	Туре	053	033	053
Ca	ιp			
[pF]	[pF 3digits]			
68	680			
82	820			
100	101			
120	121			
150	151			
180	181			
220	221			
270	271			
330	331			
390	391			
470	471			
560	561			
680	681			
820	821			
1000	102			
1500	152			
2200	222			
3300	332			
4700	472			
8200	822			
10000	103			
15000	153			
22000	223			

仕様 SPECIFICATIONS

033Type

• •			
温度特性	静電容量変化率	静電容量許容差	Q or $\tan \delta$
Temp.char.	Capacitance Change	CapacitanceTolerance	Q OI tall 0
CK	0±250ppm/℃		_
RK	-220±250		Q
SK		0.5∼5pF	400+20 · Cmin
TK	-470±250	C (±0.25pF)	(C≦27pF)
UK	-750±250		
CJ	0±120ppm/℃		
RJ			
SJ		6∼10pF	500min
TJ	-470±120	$D(\pm 0.5pF)$	(30≦C≦39pF)
UJ	-750±120		
CH	0±60ppm/℃		
RH	-220±60	11∼100pF	100min
SH		J (±5%)	(43≦C≦100pF)
TH	-470±60		
SL	+350~−1000ppm/°C		
В	±10%	K (±10%)	tan δ 2.5%max

053Type

温度特性	静電容量変化率	静電容量許容差	Q or tan δ		
Temp.char.	Capacitance Change	CapacitanceTolerance	Q OI tail o		
CK	0±250ppm/℃				
RK	-220±250				
SK	-330±250	0.5∼5pF	Q		
TK	-470±250	C (±0.25pF)	400+20 · Cmin		
UK	-750±250		(C≦27pF)		
CJ	0±120ppm/℃				
RJ	-220±120		1000min		
SJ	-330±120	6~10pF	(30≦C≦39pF)		
TJ	-470±120	D (± 0.5 pF)			
UJ	-750±120		500min		
CH	0±60ppm/℃_		(43≦C≦68pF)		
RH	-220 ± 60	11∼150pF			
SH	-330±60	J (±5%)	100min		
TH	-470±60		(75≦C≦150pF)		
SL	+350~−1000ppm/°C				
Α	± 5%	K (±10%)	tan δ 1.5%max		
В	±10%		tan δ 2.5%max		

セレクションガイド Selection Guide













アイテム一覧 PART NUMBERS

033Type —								
Elass1 定格 電圧 RatedVoltage (DC)	形 名 Ordering cod	е	EHS (Environmental Hazardous Substances)	温度特性 Temperature characteristics	公 称 静電容量 Capacitance 〔pF〕	静電容量 許 容 差 Capacitance tolerance	Q or tan δ	絶縁抵抗 Insulation resistance
	UCN033 △0F	5 🗆	RoHS		0.5	-		
	UCN033 △R7		RoHS	CK,RK	0.75			
	UCN033 △01	0 🗆	RoHS	SK,TK	1			
	UCN033 △1F	5 🗌	RoHS	UK,SL	1.5			
	UCN033 △02		RoHS		2			
	UCN033 △2F		RoHS	CJ,RJ,SJ	2.5	±0.25pF		
	UCN033 △03	0 🗆	RoHS	TJ,UJ,SL	3	-		
	UCN033 △3F		RoHS		3.5			
	UCN033 △04	0 🗆	RoHS]	4			
	UCN033 △4F		RoHS		4.5	-		
	UCN033 △05		RoHS	CH	5			
	UCN033 △06		RoHS	RH	6	-	Q≥400+20·C (C:静電容量)	
	UCN033 △07	0 🗌	RoHS	SH	7	-		
	UCN033 △08		RoHS	TH	8	±0.5pF	(C:capacitance)	
	UCN033 △09		RoHS	UJ	9			10000Μ Ω min.
	UCN033 △10		RoHS	SL	10		_	
	UCN033 △11		RoHS		11	-		
	UCN033 △12		RoHS		12	-		
	UCN033 △13	0 🗌	RoHS		13			
50V	UCN033 △15		RoHS	RH,SH,TH,UJ,SL	15			
	UCN033 △16	0 🗆	RoHS	SH,TH,UJ,SL	16			
	UCN033 △18		RoHS	011,111,00,02	18			
	UCN033 △20		RoHS	TH,UJ,SL	20			
	UCN033 △22	0 🗆	RoHS	111,00,02	22			
	UCN033 △24	0 🗆	RoHS	UJ,SL	24			
	UCN033 △27	0 🗆	RoHS	00,02	27			
	UCN033 SL30	_	RoHS		30	-		
	UCN033 SL33	0 🗆	RoHS		33	± 5%	Q≧500	
	UCN033 SL36	0 🗆	RoHS		36			
	UCN033 SL39	0 🗆	RoHS		39			
	UCN033 SL43		RoHS		43			
	UCN033 SL47	0 🗆	RoHS		47			
	UCN033 SL51	0 🗆	RoHS	SL	51			
	UCN033 SL56	0 🗆	RoHS		56			
	UCN033 SL62		RoHS		62			
	UCN033 SL68	0 🗆	RoHS		68	_	Q≧100	
	UCN033 SL75	0 🗆	RoHS		75			
	UCN033 SL82	0 🗆	RoHS]	82			
	UCN033 SL91	0 🗆	RoHS]	91			
	UCN033 SL10	1 🔲	RoHS		100			

形名の \triangle は温度特性記号、 \square は静電容量許容差記号が入ります。 \triangle Please specify the temperature characteristics and \square capacitance tolerance code.

Class2								
定格	形名		EHS	温度特性	公 称	静電容量		谷谷北土
電圧	形台		(Environmental		静電容量	許容差	0 1 5	絶縁抵抗 Insulation
RatedVoltage			Hazardous	Temperature	Capacitance	Capacitance	Q or tan δ	
(DC)	Ordering code		Substances)	characteristics	(pF)	tolerance		resistance
	UCN033 B121		RoHS		120			
	UCN033 B151		RoHS		150			
	UCN033 B181 🗆		RoHS	В	180	±10%	t 5 / 0 F	10000M Ω min.
50V	UCN033 B221		RoHS		220	±20%	tan δ ≦2.5	1000010122111111.
	UCN033 B271 🗌		RoHS		270			
	UCN033 B331 🗆		RoHS		330			

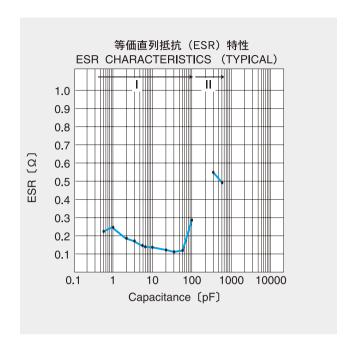
形名の \triangle は温度特性記号、 \square は静電容量許容差記号が入ります。 \triangle Please specify the temperature characteristics and \square capacitance tolerance code.

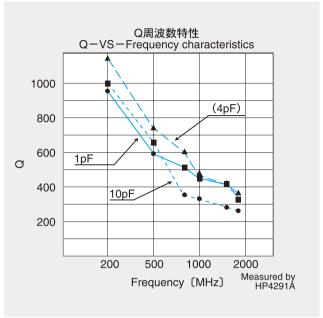
lass1 定格		EHS		公称	静電容量		
電圧	形名	(Environmental	温度特性	静電容量	許容差	Q or $\tan \delta$	絶縁抵抗
RatedVoltage		Hazardous	Temperature	Capacitance	Capacitance	Q 0. 100	Insulation
(DC)	Ordering code	Substances)	characteristics	(pF)	tolerance		resistance
	UCN053 △0R5□	RoHS		0.5			
	UCN053 △R75 ☐	RoHS	CK,RK	0.75			
	UCN053 △010 ☐	RoHS	SK,TK	1			
	UCN053 △1R5 □	RoHS	UK,SL	1.5			
	UCN053 △020 □	RoHS	1	2			
	UCN053 △2R5 □	RoHS	CJ,RJ,SJ	2.5	±0.25pF		
	UCN053 △030 □	RoHS	TJ,UJ,SL	3			
	UCN053 △3R5 □	RoHS		3.5			
	UCN053 △040 □	RoHS	4				
	UCN053 △4R5 □	RoHS	1	4.5			10000MΩmin.
	UCN053 △050 □	RoHS	1	5			
	UCN053 △060 □	RoHS	1	6		Q≧400+20·C (C:静電容量) (C:capacitance)	
	UCN053 △070 □	RoHS	CH	7			
	UCN053 △080 □	RoHS	RH	8	±0.5pF		
	UCN053 △090 □	RoHS	SH	9			
	UCN053 △100 □	RoHS	TH	10			
	UCN053 △110 □	RoHS	UJ	11			
	UCN053 △120 □	RoHS	SL	12			
	UCN053 △130 ☐	RoHS		13			
	UCN053 △150 □	RoHS		15			
50V	UCN053 △160 □	RoHS		16			
307	UCN053 △180 □	RoHS		18			
	UCN053 △200 □	RoHS		20			
	UCN053 △220 □	RoHS	SH,TH,UJ,SL	22			
	UCN053 △240 □	RoHS	TH,UJ,SL	24			
	UCN053 △270 □	RoHS	1H,00,3L	27			
	UCN053 △300 □	RoHS	UJ,SL	30			
	UCN053 SL330 🗆	RoHS		33		0>1000	
	UCN053 SL360	RoHS		36	± 5%	Q≧1000	
	UCN053 SL390	RoHS		39			
	UCN053 SL430	RoHS		43			
	UCN053 SL470	RoHS		47			
	UCN053 SL510	RoHS		51		0>500	
	UCN053 SL560 [RoHS		56		Q≧500	
	UCN053 SL620	RoHS	SL	62			
	UCN053 SL680 🗌	RoHS		68	1		
	UCN053 SL750 🗌	RoHS	 	75	1		
	UCN053 SL820 🗆	RoHS	1	82	1		
	UCN053 SL910 🗌	RoHS		91		0>100	
	UCN053 SL101 🗆	RoHS		100	1	Q≧100	
	UCN053 SL121 🗌	RoHS		120	1		
	UCN053 SL151 🗌	RoHS	-	150			

形名の△は温度特性記号、□は静電容量許容差記号が入ります。 △ Please specify the temperature characteristics and □ capacitance tolerance code. Class2

Classz									
定	格	形	名	EHS	温度特性	公 称	静電容量		絶縁抵抗 Insulation resistance
電	圧		占	(Environmental	Temperature	静電容量	許 容 差	0 . 5	
RatedVo	oltage			Hazardous	characteristics	Capacitance	Capacitance	Q or tan δ	
(DC	C)	Orderin	g code	Substances)	Characteristics	(pF)	tolerance		
		UCN053	A680 🗌	RoHS		68			
		UCN053	A820 🗌	RoHS		82	±10%	tan δ ≦1.5%	10000M Ω min.
		UCN053	A101 🗌	RoHS	A	100			
		UCN053	A121 🗌	RoHS		120			
		UCN053	A151 🗌	RoHS		150			
50\	V	UCN053	A181 🗌	RoHS		180	±20%		
		UCN053	B221 🗌	RoHS		220	±20 /6		
		UCN053	B271 🗌	RoHS		270			
		UCN053	B331 🗌	RoHS	В	330		tan δ ≦2.5%	
		UCN053	B391 🗌	RoHS		390			
	UCN053	B471 🗌	RoHS		470				

形名の \square は静電容量許容差記号が入ります。 \square Please specify the capacitance tolerance code.

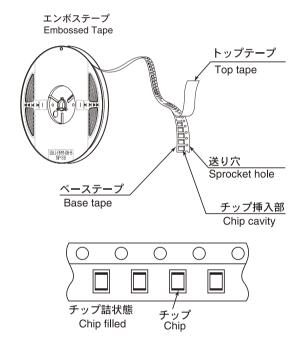




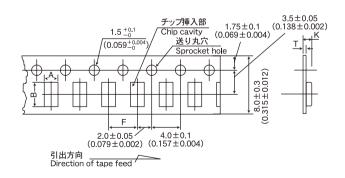
①最小受注単位数 Minimum Quantity

	最小受注単位数 (PCS)								
形式	Minimum Quantity								
Type	袋づめ	バルクカセット	テーピング						
	Bulk	Bulk cassette	Tape&Reel						
033	5000	10000	3000						
053	2000	6000	3000						

②テーピング材質 Tape Material

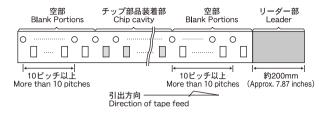


③テーピング寸法 Taping Dimensions

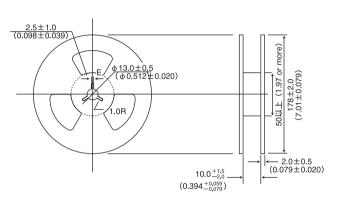


形式	チップ	挿入部	挿入ピッチ	テープ厚み				
Type	Chip	cavity	Insertion Pitch	Tape Thickness				
туре	Α	В	F	K	Т			
033	1.4±0.1	1.9±0.2	4.0±0.1	1.4max.	0.30max.			
000	(0.055±0.004)	(0.075±0.008)	(0.157±0.004)	(0.055max)	(0.012max)			
053	1.45±0.1	2.35±0.2	4.0±0.1	2.0max.	0.30max.			
000	(0.057±0.004)	(0.093±0.008)	(0.157±0.004)	(0.079max)	(0.012max)			
Unit: mm (inch)								

④リーダー部/空部 Leader and Blank Portion



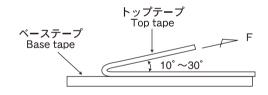
⑤リール寸法 Reel Size



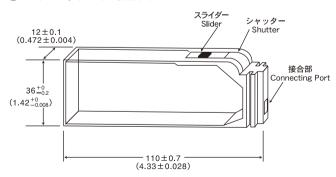
Unit = mm (inch)

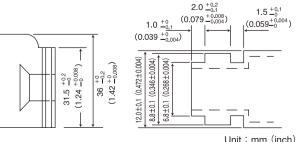
⑥トップテープ強度 Top Tape Strength

トップテープの剥離力は、下図矢印方向にて0.2~0.8Nとなります。 The top tape requires a peel-off force of 0.2 to 0.8N in the direction of the arrow as illutrated below.



⑦バルクカセット Bulk Cassette





ltem		Sp	ecified Value	
		Temperature Compensating (Class 1)	High Permittivity (Class 2)	Test Methods and Remarks
		Tubular (*CN)	Tubular (*CN)	
1.Operating Tem	perature Range	-25 to +85℃	-25 to +85°C	
2.Storage Tempe	erature Range	-25 to +85°C	-25 to +85°C	
3.Rated Voltage		50VDC	50VDC	
4.Withstanding	Between	No abnormality	No abnormality	Tubular (*CN):
Voltage	terminals			Applied voltage: Rated voltage × 3 (Class 1) Rated voltage × 2.5 (Class 2)
				Duration: 1 to 5 sec. Charge/discharge current: 50mA max. (Class 1,2)
5.Insulation Resistance		10000 MΩ min	10000 MΩ min	Applied voltage: Rated voltage Duration: 60±5 sec.
6. Capacitance		0.5 to 5 pF : ±0.25 pF	±10%	Tubular (*CN):
о. оприонинос		6 to 10 pF : ±0.5 pF	±20%	Measuring frequency: 1MHz ±20% (Class 1)
		11 pF or over: ± 5%	120%	1kHz ±20% (Class 2)
		Tipi di over. ± 370		Measuring voltage: 1 ±0.5Vrms (Class1,2)
				Bias application: None
7.Q or Tangent		053:	A: 1.5% max.	Tubular (*CN):
of Loss Angle	e (tan δ)	Under 30 pF : Q≧400 + 20C	B: 2.5% max.	Measuring frequency: 1MHz±20% (Class 1)
		30 pF or over : Q≧1000		1kHz±20% (Class 2)
		SL, 43 pF or over : Q≧500		Measuring voltage: 1 ±0.5Vrms (Class1,2)
		033:		Bias application: None
		Under 30 pF : Q≧400 + 20C		
		SL, 30 to 39 pF : Q≧500		
		SL, 43 to 100 pF : Q≧100		
		C= Nominal capacitance		

Item		Specifie	ed Value	
		Temperature Compensating (Class 1)	High Permittivity (Class 2)	Test Methods and Remarks
		Tubular (*CN)	Tubular (*CN)	
8.Capacitance	(When volt-	CK: 0±250	A: ±5%	According to JIS C 5102 clause 7.12.
Change	age is not	CJ: 0±120	B: ±10%	Temperature compensating:
due to	applied)	CH: 0±60		Measurement of capacitance at 20°C and 85°C shall be
Temperature		RK: -220±250		made to calculate temperature characteristic by the fo
or Rate of		RJ: -220±120		lowing equation.
Capacitance		RH: -220±60		(C85-C20) ×10 ⁶ (ppm/°C)
Change		SK: -330±250		C20×△T
		SJ: -330±120		(C-25-C20) ×10 ⁶ (ppm/°C)
		SH: -330±60		C20×△T × 10 (ppin/ C)
		TK: -470±250		High permittivity:
		TJ: -470±120		Change of maximum capacitance deviation in step 1 to 5
		TH: -470±60		Temperature at step 1: +20°C
		UK: -750±250		Temperature at step 2: -25°C
		UJ: -750±120		Temperature at step 3: +20°C (Reference temperature)
		SL: +350 to -1000		Temperature at step 4: +85°C
		(ppm/°C)		Temperature at step 5: +20°C
.Adhesion of	Electrode	No damage	No damage	Tubular (*CN):
				Applied force: 5N Push – Pull Apply force using a gauge. Tubular chip Alumina substrate (t = 1.0mm)
10.Solderabilit	у	At least 80% of terminal elec-	At least 80% of terminal elec-	Tubular (*CN):
		trodes is covered by new solder.	trodes is covered by new solder.	According to JIS C 5102 clause 8.13.
				Solder temperature: 230±5°C
				Duration: 4±1 sec.

	Specif	Specified Value		
Item	Temperature Compensating (Class 1)	High Permittivity (Class 2)	Test Methods and Remarks	
	Tubular (*CN)	Tubular (*CN)		
11.Resistance to Soldering	Appearance: No abnormality	Appearance: No abnormality	Tubular (*CN):	
Heat	Capacitance change:	Capacitance change:	According to JIS C 5102 clause 8.14.	
	Within $\pm 2.5\%$ or ± 0.25 pF,	A: Within ±3%	Solder temperature: 270±5°C	
	whichever is larger.	B: Within ±5%	Duration: 3±0.5 sec.	
	Q: Initial value	tan δ: Initial value	Preheating conditions: 80 to 120°C, 2 min.	
	Insulation resistance:	Insulation resistance:	150 to 200℃, 2 min.	
	Initial value	Initial value	Recovery: Recovery for the following period under the stan-	
	Withstanding voltage (between	Withstanding voltage (between	dard condition after the test.	
	terminals): No abnormality	terminals): No abnormality	24±2 hrs (Class 1)	
			48±4 hrs (Class 2)	
12.Thermal Shock	Appearance: No abnormality	Appearance: No abnormality	Tubular (*CN):	
	Capacitance change:	Capacitance change:	Conditions for 1 cycle:	
	Within ±2.5% or ±0.25pF,	A: Within ±3%	Step 1: Room temperature 10 min.	
	whichever is larger.	B: Within ±5%	Step 2: -25 +0°C 30 min.	
	Q: Initial value	tan δ: Initial value	Step 3: Room temperature 10 min.	
	0.5∼27pF	Insulation resistance:	Step 4: +85 +3 °C 30 min.	
	Q≧400+20C	Initial value	Number of cycles: 5	
	SL 30~39pF	Withstanding voltage (between	Recovery: Recovery for the following period under the stan-	
	Q≧500	terminals): No abnormality	dard condition after the test.	
	43~100pF		24±2 hrs (Class 1)	
	Q≧100		48±4 hrs (Class 2)	
	Insulation resistance:			
	Initial value			
	Withstanding voltage (between			
	terminals): No abnormality			

Thermal Shock is also referred to as "rapid change of temperature" under IEC specifications.

	Specified Value			
Item	Temperature Compensating (Class 1)	High Permittivity (Class 2)	Test Methods and Remarks	
	Tubular (*CN)	Tubular (*CN)		
13.Damp Heat (steady state)	Appearance : No abnormality	Appearance : No abnormality	Tubular (*CN033,053):	
	Capacitance change:	Capacitance change:	Temperature: 40±2°C	
	Within $\pm 5\%$ or ± 0.5 pF,	A: Within ±7.5%	Humidity: 90 to 95% RH	
	whichever is larger.	B: Within ±10%	Duration: 500+24 hrs	
	Q (033):	tan δ: A: 3% max.	Recovery: Recovery for the following period under the st	
	0.5 to 9 pF : Q≧200 + 10C	B : 5% max.	dard condition after the removal from test cha	
	10 to 27 pF : Q≧275 + 2.5C	Insulation resistance:	ber.	
	30 to 39 pF : Q≧250 (SL)	1000 MΩ min.	24±2 hrs (Class 1)	
	43 to 100 pF: Q≧50 (SL)		48±4 hrs (Class 2)	
	Q (053):			
	C≧30 pF : Q≧350			
	10≦C<30 pF : Q≧275 + 2.5C			
	C<10 pF : Q≧200 + 10C			
	053SL43 pF or over : Q≧250			
	C= Nominal capacitance			
	Insulation resistance :			
	1000 MΩ min.			
	A N I I		A	
4.Loading under Damp Heat	Appearance: No abnormality	Appearance: No abnormality	According to JIS C 5102 clause 9.9.	
	Capacitance change:	Capacitance change:	Tubular (*CN):	
	Within $\pm 7.5\%$ or ± 0.75 pF,	A: Within ±7.5%	Temperature: 40±2°C	
	whichever is larger.	B: Within ±10%	Humidity: 90 to 95% RH	
	Q (033):	tan δ: A: 5% max.	Duration: 500 ⁺²⁴ hrs (*CN033,053)	
	0.5 to 27 pF: Q≧100 + 10C/3	B : 5% max.	Applied voltage: Rated voltage	
	30 to 39 pF : Q≧250 (SL)	Insulation resistance:	Charge and discharge current: 50mA max. (Class 1,2	
	43 to 100 pF: Q≧30 (SL)	500 MΩ min.	Recovery: Recovery for the following period under the s	
	Q (053):		dard condition after the removal from test cha	
	C≧30 pF: Q≧200		ber.	
	C<30 pF : Q≥100 + 10C/3		24±2 hrs (Class 1)	
	053SL43 pF or over: Q≧150		48±4 hrs (Class 2)	
	C= Nominal capacitance			
	Insulation resistance:			
	500 MΩ min.			

	Speci	fied Value	
Item	Temperature Compensating (Class 1)	High Permittivity (Class 2) Test Methods and Remarks	
	Tubular (*CN)	Tubular (*CN)	
15. Load Test under High	Appearance: No abnormality	Appearance: No abnormality	According to JIS C 5102 clause 9.10.
Temp	Capacitance change:	Capacitance change:	Tubular (*CN):
	Within $\pm 3\%$ or ± 0.3 pF,	A: Within ±7.5%	Temperature: 85±2°C
	whichever is larger.	B: Within ±10%	Duration: 1000 +48 hrs (*CN033,053)
	Q (033):	tan δ: A: 3% max.	Applied voltage: Rated voltage ×2 (Class 1,2)
	0.5 to 9 pF : Q≧200 + 10C	B: 4% max.	Charge and discharge current: 50mA max. (Class 1,2)
	10 to 27 pF : Q≧275 + 2.5C	Insulation resistance:	Recovery: Recovery for the following period under the stan-
	30 to 39 pF : Q≧250 (SL)	1000 MΩ min.	dard condition after the removal from test cham-
	43 to 100 pF: Q≧50 (SL)		ber.
	Q (053):		24±2 hrs (Class 1)
	C≧30 pF : Q≧350		48±4 hrs (Class 2)
	10≦C<30 pF: Q≧275 + 2.5C		
	C<10 pF : Q≧200 + 10C		
	053SL43 pF or over: Q≧250		
	C= Nominal capacitance		
	Insulation resistance:		
	1000M Ω min.		

Note 1:

Note on standard condition: "standard condition" referred to herein is defined as follows:

5 to $35^\circ\!\text{C}$ of temperature, 45 to 85% relative humidity and 86 to 106kPa of air pressure.

When there are questions concerning measurement results:

In order to provide correlation data, the test shall be conducted under condition of 20±2°C of temperature, 60 to 70% relative humidity and 86 to 106kPa of air pressure.

Unless otherwise specified, all the tests are conducted under the "standard condition."

^{*}Please specify the rated voltage code.

Stages	Precautions	Technical considerations
Circuit Design	◆Verification of operating environment, electrical rating and	
	performance	
	1. A malfunction in medical equipment, spacecraft, nuclear	
	reactors, etc. may cause serious harm to human life or have	
	severe social ramifications. As such, any capacitors to be	
	used in such equipment may require higher safety and/or	
	reliability considerations and should be clearly differentiated	
	from components used in general purpose applications.	
	◆Operating Voltage (Verification of Rated voltage)	
	The operating voltage for capacitors must always be lower	
	than their rated values.	
	If an AC voltage is loaded on a DC voltage, the sum of the	
	two peak voltages should be lower than the rated value of	
	the capacitor chosen. For a circuit where both an AC and a	
	pulse voltage may be present, the sum of their peak voltages	
	should also be lower than the capacitor's rated voltage.	
	2. Even if the applied voltage is lower than the rated value, the	
	reliability of capacitors might be reduced if either a high fre-	
	quency AC voltage or a pulse voltage having rapid rise time	
	is present in the circuit.	
	◆Operating Current (Limitation in the current)	
	1. General purpose capacitors are usually designed in a DC	
	environment. Therefore, if capacitors are used in the circuits	
	where AC or Pulse voltages are loaded, a large current run-	
	ning through the capacitor may result in a short-circuit due	
	to self-generated heat.	
	◆*Operating Environment precautions	
	Capacitors should not be used in the following environments:	
	(1) Environmental conditions to avoid	
	a. exposure to water or salt water.	
	b. exposure to moisture or condensation.	
	c. exposure to corrosive gases (such as hydrogen sul-	
	fide, sulfurous acid, chlorine, and ammonia)	

Stages	Precautions	Technica	al considerations
PCB Design	◆Pattern configurations (Design of Land-patterns) 1. When capacitors are mounted on a PCB, the amount of solder used (size of fillet) can directly affect capacitor performance. Therefore, the following items must be carefully considered in the design of solder land patterns: (1) The amount of solder applied can affect the ability of chips to withstand mechanical stresses which may lead to breaking or cracking. Therefore, when designing land-patterns it is necessary to consider the appropriate size and configuration of the solder pads which in turn determines the amount of solder necessary to form the fillets. (2) When more than one part is jointly soldered onto the same land or pad, the pad must be designed so that each component's soldering point is separated by solder-resist.	to prevent excessive solder amounts	show some examples of recommended patterns is (larger fillets which extend above the compo improper pattern designs are also shown. The at the composition of the co
		Recommended land patterns for reflow soldering Type Location A 1.2 B 0.8 C 1.0 D 0.5 or more 0. Notes; 1. When designing land patterns, roun better solderability. 2. The size of the solder pad can vary of solder.	1.0 1.0 1.0 0.8~1.0 0.8

Item	Not recommended	Lead wire of component
Mixe-mounting of SMD and leaded compo- nents	Lead wire of component	Solder-resist
Component placement close to the chassis	Chassis Solder(for grounding)	Solder-resist
Hand-soldering of leaded components near mounted components	Soldering Of component iron	Solder-resist
Horizontal component placement		Solder-resist

Stages	Precautions	Technical considerations
Considerations for automatic placement	◆Adjustment of mounting machine-1 1. Excessive impact load should not be imposed on the capacitors when mounting onto the PC boards.	1. If the lower limit of the pick-up nozzle is low, too much force may be imposed on the capacitors, causing damage. To avoid this, the following points should be considered before lowering the pick-up nozzle: (1) The lower limit of the pick-up nozzle should be adjusted to the surface level of the PC board after correcting for deflection of the board. (2) The pick-up pressure should be adjusted between 1 and 3 N or less static loads. (3) In case of double-sided mounting, a supporting pin should be used under the P board to minimize the effect of pick-up nozzle impact on the board. (Fig. 1) (4) The following figures show typical results when the bottom dead center of the pick-up nozzle is too low. (Figs 2,3)
	◆Selection of Adhesives 1. Mounting capacitors with adhesives in preliminary assembly, before the soldering stage, may lead to degraded capacitor characteristics unless the following factors are appropriately checked; the size of land patterns, type of adhesive, amount applied, hardening temperature and hardening period. Therefore, it is imperative to consult the manufacturer of the adhesives on proper usage and amounts of adhesive to use.	With supporting pin (Fig.1) In case of single-sided mounting (Fig.2) double-sided mounting (Fig.2) Mounting head Supporting pin Excessive pressure Cracks Solder peeling Cracks 1. Some adhesives may cause reduced insulation resistance. The difference between the shrinkage percentage of the adhesive and that of the capacitors may result stresses on the capacitors and lead to cracking. Moreover, too little or too much adhesive applied to the board may adversely effect component placement, so the following precautions should be noted in the application of adhesives.
		(1) Required adhesive characteristics a. The adhesive should be strong enough to hold parts on the board during the mounting & solder process. b. The adhesive should have sufficient strength at high temperatures. c. The adhesive should have good coating and thickness consistency. d. The adhesive should be used during its prescribed shelf life. e. The adhesive should harden rapidly f. The adhesive must not be contaminated. g. The adhesive should have excellent insulation characteristics. h. The adhesive should not be toxic and have no emission of toxic gasses.
		(2) The recommended amount of adhesives is as follows; When using adhesives to mount capacitors on a PCB, inappropriate amoun of adhesive on the board may adversely affect component placement. Too litt adhesive may cause the capacitors to fall off the board during the solder proces Too much adhesive may cause defective soldering due excessive flow of adh sive on to the land or solder pad. [Recommended conditions]
		Figure 2125 case sizes as examples a 0.3mm min b 200~300 \(\mu\) m (when two points applied) c Adhesives should not contact the pad
		Amount of adhesives

Stages	Precautions	Technical considerations
4. Soldering		1. Flux is used to increase solderability in wave soldering, but if too much is applied, a large amount of flux gas may be emitted and may detrimentally affect solderability. To minimize the amount of flux applied, it is recommended to use a flux-bubbling system. 2. With too much halogenated substance (Chlorine, etc.) content is used to activate the flux, an excessive amount of residue after soldering may lead to corrosion of the terminal electrodes or degradation of insulation resistance on the surface of the capacitors. 3. Since the residue on the surface of capacitors in high humidity conditions may cause a degradation of insulation resistance and therefore affect the reliability of the components. The cleaning methods and the capability of the machines used should also be considered carefully when selecting water-soluble flux. (Recommended conditions for soldering) [62Sn Solder Flow]
		3. During the reflow process, when too much solder paste is applied excess solder mass can produce mechanical and heat stresses on the capacitors and may consequently result in the breakage or cracking of the components. On the other hand, too little solder paste will weaken the adhesion characteristics and may consequently cause separation of components and degrade the circuit reliability.
		(a) Too much solder (b) Appropriate amount (c) Too little solder
		With inappropriate solder materials, solder balls may form. These solder balls must be thoroughly removed, since the balls would cause a reduction in capacitor electrical characteristics or degradation of reliability.

Stages	Precautions		Technical considerations	
4. Soldering	 ◆Hand soldering with iron 1 Temperature, time, amount of solder, etc. are specified in accordance with the following recommended conditions. 2. When touch-up work is required for repair, preheating must be conducted with appropriate temperature control. 3. Special attention should be paid to the diameter of the soldering iron tip and wattage when additional part mounting or repair work takes place. 4. The solder iron should only directly touch the external electrodes. 5. Ammount of solder should be applied at the appropriate level. 	reliability of the capacitors. Above all, rapid heating/cooling or partial heat be the major causes of cracks. 2. Recommended conditions for solder iron touch-up [Example of soldering iron] Soldering iron's temperature		Iron tip dia. [mm] Below 3.0 in diameter perature. 130°C or less
		If the soldering iron tip to develop heat stresses, and During the solder iron proceical and heat stresses on the or cracking of the componing the solder.	uches the ceramic material cracks. ess, when too much solder is the capacitors and may conservents. On the other hand, too did may consequently cause solutions.	
5. Cleaning	 ◆Board cleaning 1. When using ultrasonic cleaning on PC boards with capacitors, avoid subjecting the PCB directly to vibration. Special attention should be paid to output frequency and duration of ultrasonic cleaning. 2. Cleaning conditions should be determined after verifying, through a test run, that the cleaning process does not affect the capacitor's characteristics. 	vibration of the PC board soldered portion, or decrea conditions should be careful Ultrasonic output Ultrasonic frequency Ultrasonic clearning peri 2. In case of insufficient clean (1) The halogenated controlled the	which may lead to the craces the terminal electrodes's ally checked; Below 20 W/ & Below 40 kHz od 5 min. or less ing ent in the flux residue may lead on of insulation resistance. uble flux, it may degrade insulation.	output can cause excessive cking of the capacitor or the strength. Thus the following ad to corrosion of the terminal plation resistance characteris-
6. Post cleaning processes	 Application of resin molding, etc. to the PCB and components. Please contact your local Taiyo Yuden sales office before performing resin coating or molding on mounted capacitors. 	or destruction. 1-2. With some type of resins main inside the resin durin conditions resulting in the classification or some types of coating or some types or some types or some types of coating or some types o	a decomposition gas or che g the hardening period or what deterioration of the capacitor	ay lead to capacitor damage mical reaction vapor may re- nile left under normal storage s performance. e humidity resistance. There-

Stages	Precautions	Technical considerations
7. Handling	◆Breakaway PC boards (splitting along perforations) 1. When splitting the PC board after mounting capacitors and other components, care is required so as not to give any stresses of deflection or twisting to the board. 2. Board separation should not be done manually, but by using the appropriate devices.	I. If the board is subjected to the stresses of deflection and twisting (as shown below) when splitting or breaking away the boards, it may cause cracks in the board. Deflection Twisting
	 Mechanical considerations Be careful not to subject the capacitors to excessive mechanical shocks. If ceramic capacitors are dropped on the floor or a hard surface they should not be used. 	Because the capacitor is made of ceramic, mechanical shocks applied to the board may damage or crack the capacitors. Ceramic capacitors which are dropped onto the floor or a hard surface may develop defects and have a higher risk of failure over time.
8. Storage conditions	◆Storage 1. To maintain the solderability of terminal electrodes and to keep the packaging material in good condition, care must be taken to control temperature and humidity in the storage area. Humidity should especially be kept as low as possible. Recommended conditions: Ambient temperature Below 40 deg °C Humidity Below 70% RH 2. Capacitors should not be kept in an environment filled with decomposition gases such as (sulfurous hydrogen, sulfurous acid, chlorine, ammonia, etc.) 3. Capacitors should not be kept in a location where they may be exposed to moisture, condensation or direct sunlight.	1. Under high temperature/high humidity conditions, the decrease in solderability due to the oxidation of terminal electrodes and deterioration of taping and packaging characteristics may be accelerated, so the products should be used within 6 months after delivery. After the above period, the solderability should be checked before using the capacitors. 2. Harmful gasses in the ambient air may also degrade the solderability of the terminal electrodes resulting in a deterioration of the capacitor's reliability. 3. Direct sunlight, the photochemical effect of resin coatings, or a rapid change in the humidity may cause condensation on or around the terminals. So special care must be taken to prevent reduced solderability or performance of the capacitors.