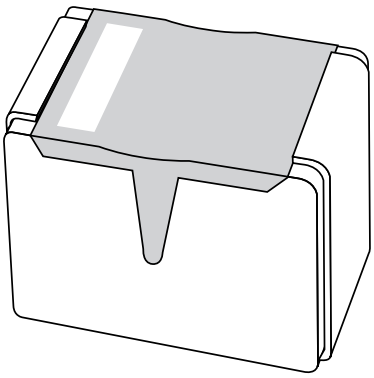
DATA SHEET



BAT254
Schottky barrier diode

Product specification
Supersedes data of 1999 Apr 22

2002 May 28

Philips
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Schottky barrier diode BAT254

FEATURES

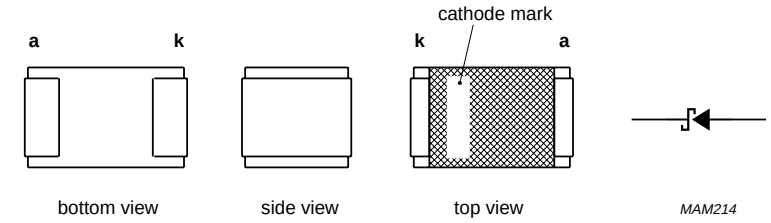
- Low forward voltage
- Guard ring protected
- Very small ceramic SMD package.

APPLICATIONS

- Ultra high-speed switching
- Voltage clamping
- Protection circuits
- Blocking diodes.

DESCRIPTION

Planar Schottky barrier diode encapsulated in a SOD110 very small ceramic SMD package.



Marking code: L4.

Fig.1 Simplified outline (SOD110) and symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_R	continuous reverse voltage		–	30	V
I_F	continuous forward current		–	200	mA
I_{FRM}	repetitive peak forward current	$t_p \leq 1$ s; $\delta \leq 0.5$	–	300	mA
I_{FSM}	non-repetitive peak forward current	$t_p < 10$ ms	–	600	mA
T_{stg}	storage temperature		–65	+150	°C
T_j	junction temperature		–	125	°C
T_{amb}	operating ambient temperature		–65	+125	°C

ELECTRICAL CHARACTERISTICS

T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MAX.	UNIT
V_F	forward voltage	see Fig.2		
		I_F = 0.1 mA	240	mV
		I_F = 1 mA	320	mV
		I_F = 10 mA	400	mV
		I_F = 30 mA	500	mV
		I_F = 100 mA	800	mV
I_R	reverse current	V_R = 25 V; note 1; see Fig.3	2	μA
t_{rr}	reverse recovery time	when switched from I_F = 10 mA to I_R = 10 mA; R_L = 100 Ω; measured at I_R = 1 mA; see Fig.5	5	ns
C_d	diode capacitance	f = 1 MHz; V_R = 1 V; see Fig.4	10	pF

Note

1. Pulse test: t_p = 300 μs; δ = 0.02.

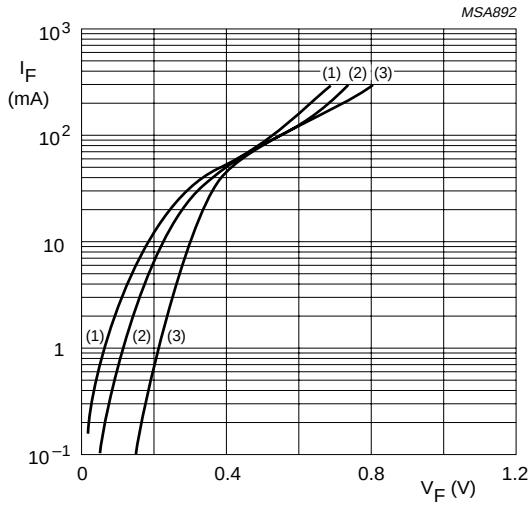
THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j\text{-}a}$	thermal resistance from junction to ambient	note 1	315	K/W

Note

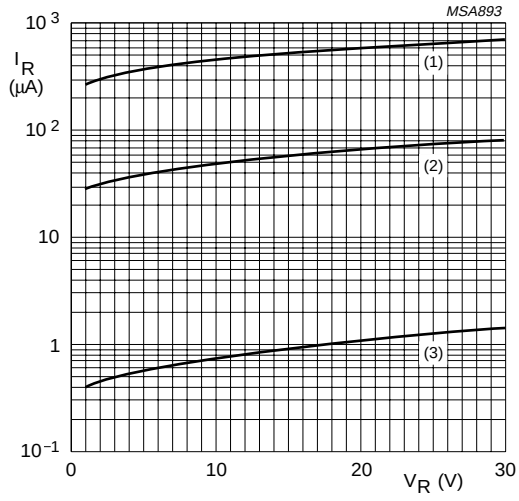
1. Refer to SOD110 standard mounting conditions.

GRAPHICAL DATA



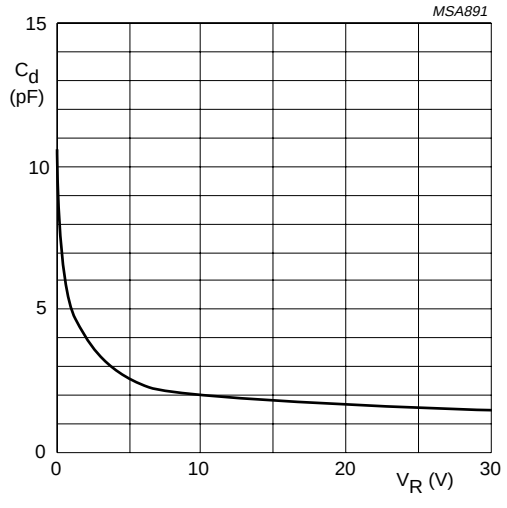
(1) T_{amb} = 125 °C.
(2) T_{amb} = 85 °C.
(3) T_{amb} = 25 °C.

Fig.2 Forward current as a function of forward voltage; typical values.



(1) T_{amb} = 125 °C.
(2) T_{amb} = 85 °C.
(3) T_{amb} = 25 °C.

Fig.3 Reverse current as a function of reverse voltage; typical values.



f = 1 MHz; T_{amb} = 25 °C.

Fig.4 Diode capacitance as a function of reverse voltage; typical values.

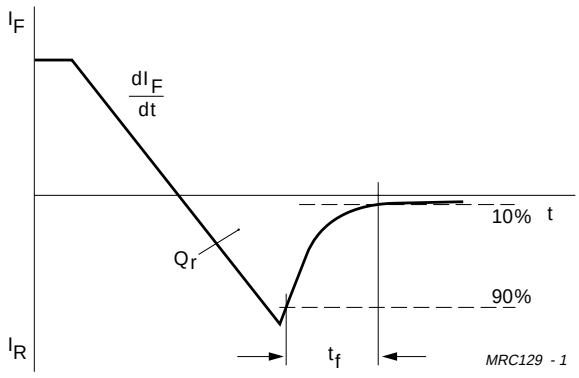
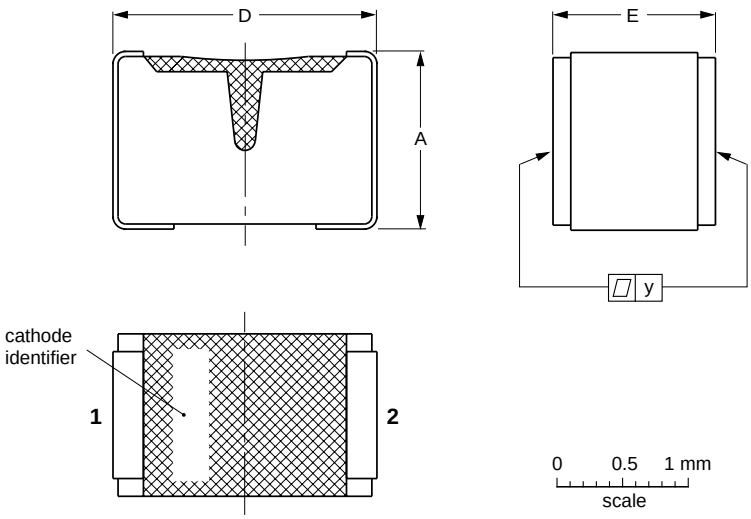


Fig.5 Reverse recovery definitions.

PACKAGE OUTLINE

Very small ceramic rectangular surface mounted package **SOD110**



DIMENSIONS (mm are the original dimensions)

UNIT	A max.	D	E	y
mm	1.6	2.10 1.90	1.40 1.10	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOD110						97-04-14

DATA SHEET STATUS

DATA SHEET STATUS[1]	PRODUCT STATUS[2]	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
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