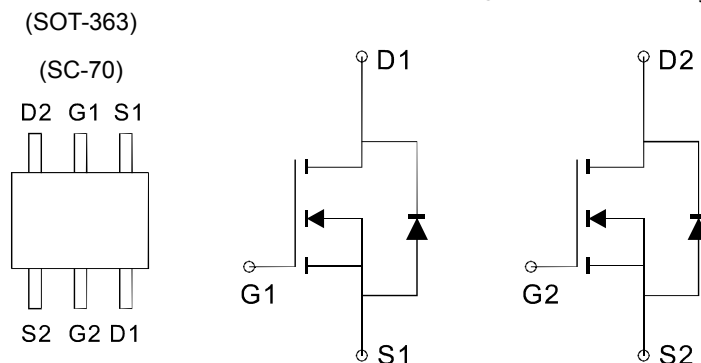


## Dual N-Channel MOSFET

### GENERAL DESCRIPTION

The ME2N7002KW is the Dual N-Channel enhancement mode field effect transistors are produced using high cell density DMOS technology. These products have been designed to minimize on-state resistance while provide rugged, reliable, and fast switching performance. They can be used in most applications requiring up to 300mA DC and can deliver pulsed currents up to 1.0A. These products are particularly suited for low voltage, low current applications such as small servo motor control, power MOSFET gate drivers, and other switching applications.

### PIN CONFIGURATION



### FEATURES

- 60V / 0.50A ,  $R_{DS(ON)} = 5.0\Omega @ V_{GS}=10V$
- 60V / 0.30A ,  $R_{DS(ON)} = 6.0\Omega @ V_{GS}=4.5V$
- Super high density cell design for extremely low  $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- SOT-363 package design

### APPLICATIONS

- Drivers: Relays, Solenoids, Lamps, Hammers, Display, Memories, Transistors, etc.
- High saturation current capability. Direct Logic-Level Interface: TTL/CM
- Battery Operated Systems
- Solid-State Relays

### Maximum ratings and electrical characteristic

Ratings at 25°C ambient temperature unless otherwise specified

Symbol	PARAMETER		Typical	Units
$V_{DSS}$	Drain-Source Voltage		60	V
$V_{GSS}$	Gate-Source Voltage - Continuous		$\pm 20$	V
$V_{GSS}$	Gate-Source Voltage - Non Repetitive (tp < 50 $\mu$ s)		$\pm 40$	V
$I_D$	Drain Current - Continuous ( $T_J=150^\circ C$ )	$T_A=25^\circ C$	300	mA
	- Pulsed (Note 1)		1000	
$I_S$	Continuous Source Current (Diode Conduction)		250	mA
$P_D$	Power Dissipation	$T_A=25^\circ C$	350	mW
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 ~ +150	$^\circ C$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		375	$^\circ C/W$

Note : 1. Pulse width limited by safe operating area

## Dual N-Channel MOSFET

### Maximum ratings and electrical characteristic

Ratings at 25°C ambient temperature unless otherwise specified

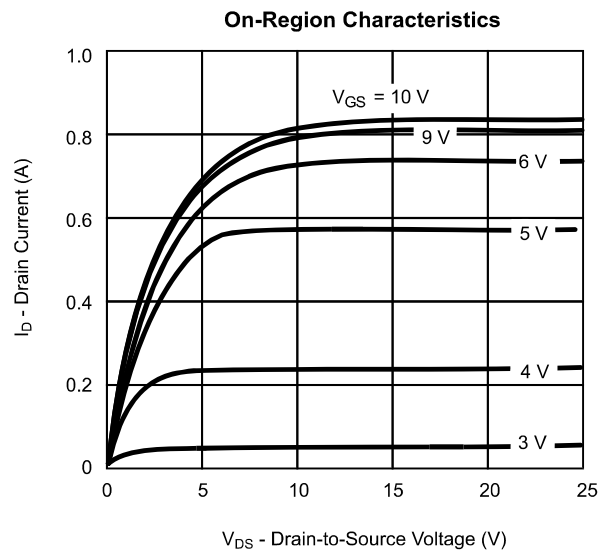
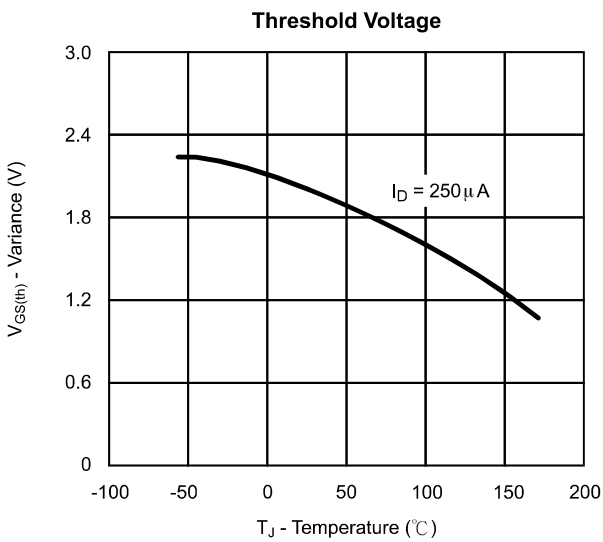
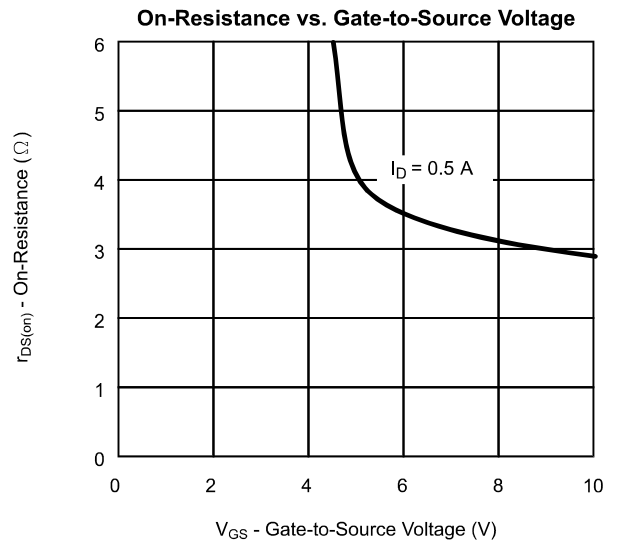
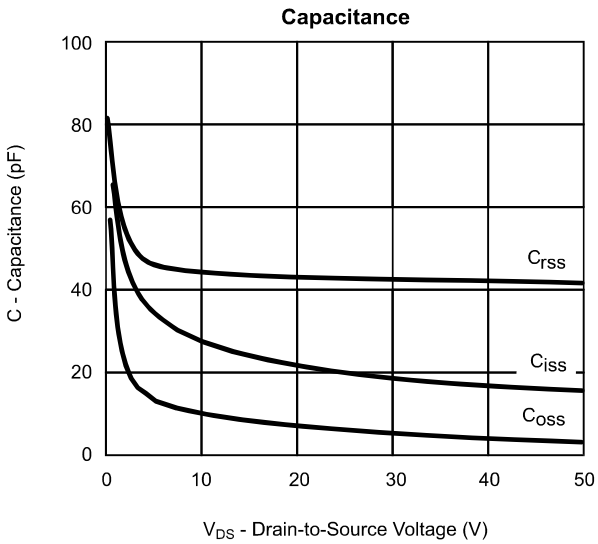
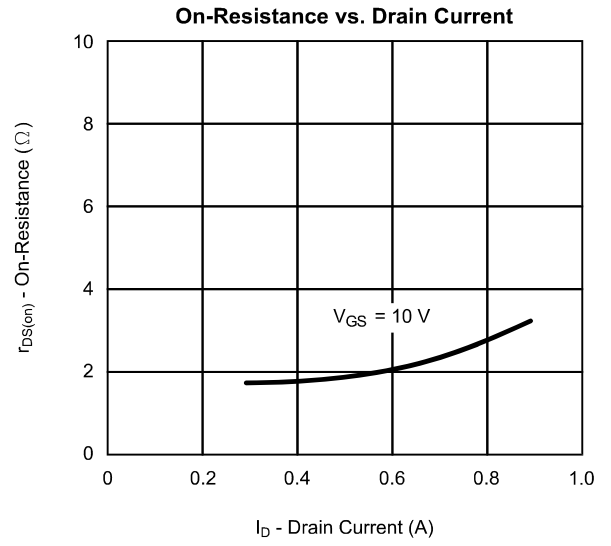
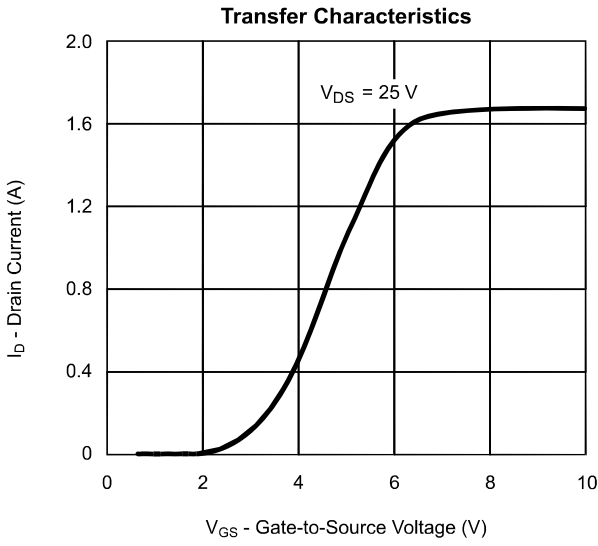
Symbol	Ratings	Test Conditions	Min	Typ	Max	Units
<b>OFF CHARACTERISTICS</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	60	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 60V, V_{GS} = 0V$ $V_{DS} = 60V, V_{GS} = 0V$ $T_J = 125^\circ C$	-	-	1 10	$\mu A$
$I_{GSSF}$	Gate-Body Leakage, Forward	$V_{DS} = 0V, V_{GS} = 20V$	-	-	100	nA
$I_{GSSR}$	Gate-Body Leakage, Reverse	$V_{DS} = 0V, V_{GS} = -20V$	-	-	-100	nA
<b>ON CHARACTERISTIC (Note1)</b>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250\mu A$	1	1.7	2.5	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS} = 10V, I_D = 500mA$ $V_{GS} = 4.5V, I_D = 300mA$	-	3.5 4.0	5 6	$\Omega$
$I_{SD}$	Source-drain Current		-	-	0.35	A
$I_{SDM(2)}$	Source-drain Current (pulsed)		-	-	1.4	A
$G_{FS(1)}$	Forward Trans-conductance	$V_{DS} = 10V, I_D = 500mA$	-	0.6	-	S
$V_{SD(1)}$	Diode Forward Voltage	$V_{GS} = 0V, I_S = 0.12mA$	-	0.85	1.5	V
<b>DYNAMIC CHARACTERISTICS</b>						
$C_{ISS}$	Input Capacitance	$V_{DS} = 25V, V_{GS} = 0V,$ $F = 1.0MHz$	-	43	-	pF
$C_{OSS}$	Output Capacitance		-	20	-	
$C_{RSS}$	Reverse Transfer Capacitance		-	6	-	
$Q_G$	Total Gate Charge	$V_{DD} = 30V, I_D = 1A, V_{GS} = 5V$	-	1.4	2.0	nC
$Q_{GS}$	Gate-Source Charge		-	0.8	-	
$Q_{GD}$	Gate-Drain Charge		-	0.5	-	
$TD_{(ON)}$	Turn-On Time	$V_{DD} = 30V, R_G = 4.7\Omega,$ $I_D = 500mA, V_{GS} = 4.5V$	-	6	-	nS
$T_R$			-	5	-	
$TD_{(OFF)}$	Turn-Off Time		-	15	-	
$T_R$			-	6	-	

Note : 1. Pulsed: Pulse duration = 300  $\mu s$ , duty cycle 1.5 %.

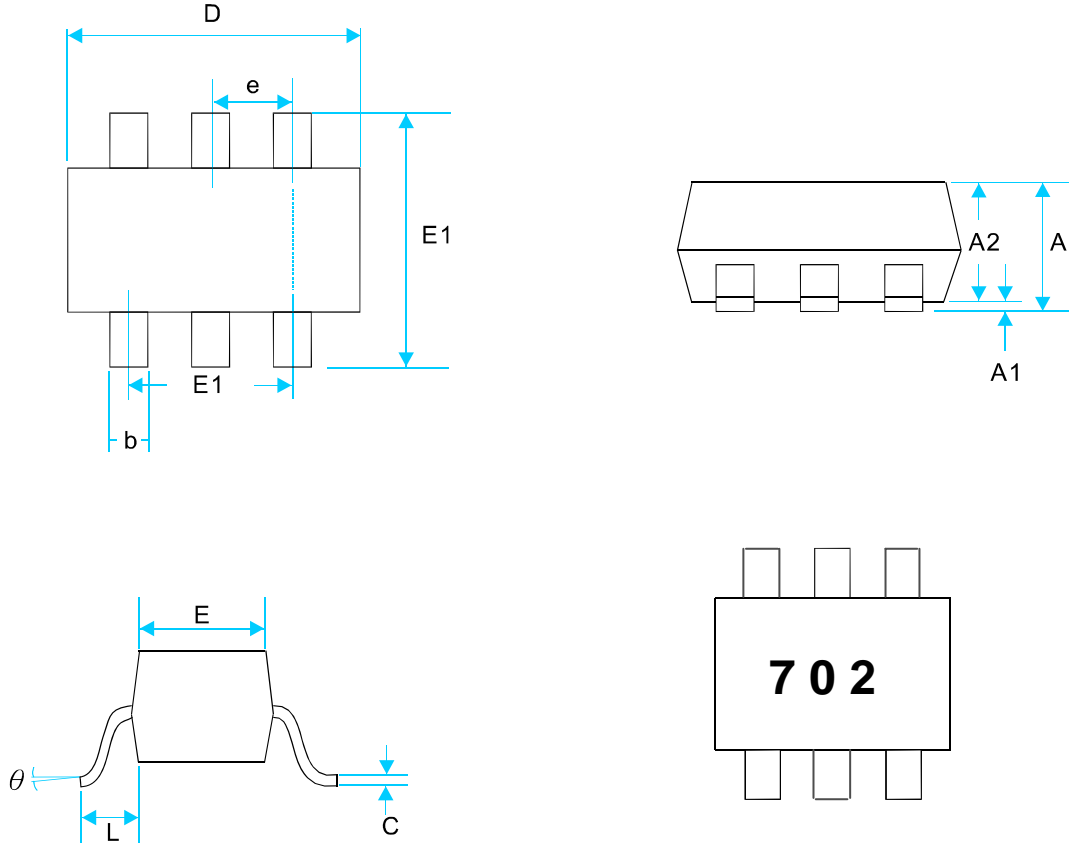
2. Pulse width limited by safe operating area.

## Dual N-Channel MOSFET

### Typical Characteristics (T<sub>J</sub> = 25°C Noted)



## SOT-363 Package Outline



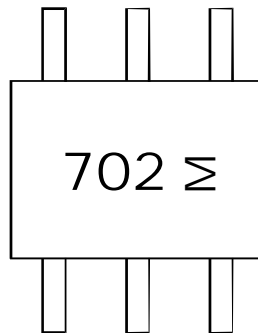
SYMBOL	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.900	1.000	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650TYP		0.026TYP	
e1	1.200	1.400	0.047	0.055
L	0.525REF		0.021REF	
L1	0.260	0.460	0.010	0.018
$\theta$	0°	8°	0°	8°



**Device name: ME2N7002KW**

**Package: SOT-363**

**Marking Code :**



702 : Device number

M : Date code

**MONTH CODE**

ODD YEARS(2007, 2009)

Jan	1
Feb	2
Mar	3
Apr	4
May	5
Jun	6
Jul	7
Aug	8
Sep	9
Oct	T
Nov	V
Dec	C

EVEN YEARS(2006,2008)

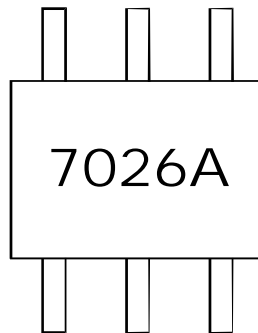
Jan	E
Feb	F
Mar	H
Apr	J
May	K
Jun	L
Jul	N
Aug	P
Sep	U
Oct	X
Nov	Y
Dec	Z



Device name: ME2N7002KW

Package: SOT-363

Marking Code :



702 : Device number

6 : Year code(2006)

A : Week code

“A”~”Z” = The first week ~ The twenty-six.

“a” ~ ”z” = The twenty-seven ~ The fifty-two

“.” = The fifty-three