TOSHIBA Diode Silicon Epitaxial Planar Type

1SS427

Ultra-High Speed Switching Applications

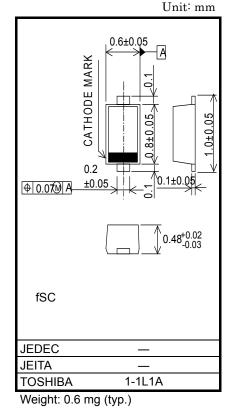
- 2-pin compact package: Ideal for high-density mounting
- Low forward voltage: $V_{F(3)} = 0.98 V$ (typ.)

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- Fast reverse recovery time: $t_{rr} = 1.6 \text{ ns} (typ.)$
- Small total capacitance: $C_T = 0.5 \text{ pF}$ (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics Symbol Rating Unit 85 V Maximum (peak) reverse voltage V_{RM} 80 V Reverse voltage VR Maximum (peak) forward current IFM 200 mΑ Average forward current 100 mΑ lo Surge current (10 ms) 1 А IFSM Р 150 * Power dissipation mW 150 °C Junction temperature Тj Storage temperature Tstg -55 to 150 °C



- * : Mounted on a glass epoxy circuit board of 20 mm × 20 mm, Cu pad dimension of 4 mm × 4 mm.
- Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

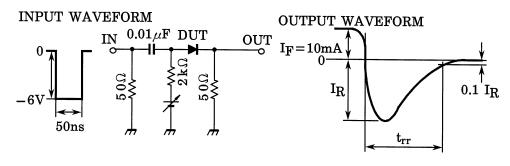
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V _{F (1)}	_	I _F = 1 mA		0.62	_	V
	V _{F (2)}	_	I _F = 10 mA		0.75	—	
	V _{F (3)}		I _F = 100 mA	-	0.98	1.20	
Reverse current	I _{R (1)}		V _R = 30 V	-	_	0.1	μA
	I _{R (2)}		V _R = 80 V	-	_	0.5	
Total capacitance	CT		V _R = 0, f = 1 MH _z	-	0.5	_	pF
Reverse recovery time	t _{rr}	_	I _F = 10 mA, Fig.1	_	1.6	_	ns

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Fig.1 Reverse Recovery Time (trr) Test Circuit



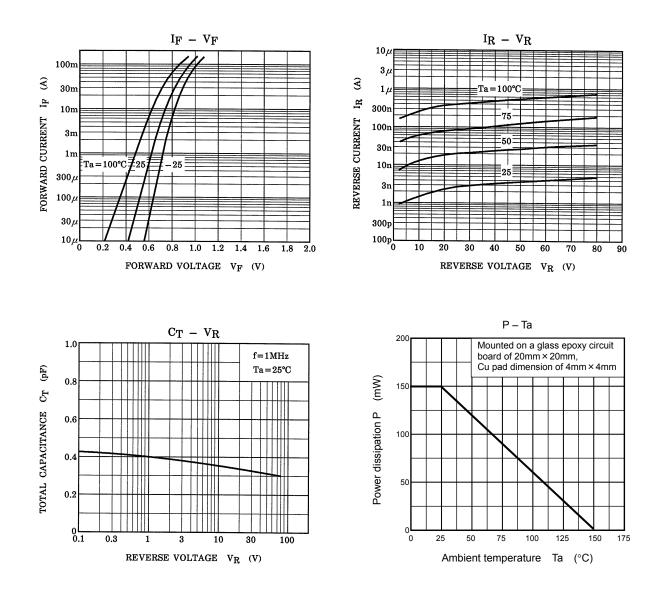
Pin Assignment (top view)



Marking



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