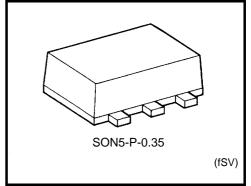
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SH02FS

2-INPUT NOR GATE

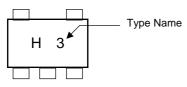
Features

High speed: t_{pd} = 3.6 ns (typ.) at V_{CC} = 5 V Low power dissipation: I_{CC} = 2 µA (max) at Ta = 25°C High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min) 5.5V tolerant input. Wide operating voltage range: V_{CC} (opr) = 2~5.5 V

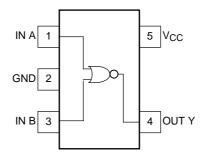


Weight : 0.001 g (Typ.)

Marking



Pin Assignment (top view)



Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5~7.0	V
DC input voltage	V _{IN}	-0.5~7.0	V
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5	V
Input diode current	I _{IK}	-20	mA
Output diode current	I _{OK}	±20	mA
DC output current	IOUT	±25	mA
DC V _{CC} /ground current	ICC	±50	mA
Power dissipation	PD	50	mW
Storage temperature	T _{stg}	-65~150	°C

Logic Diagram



Truth Table

А	В	Y
L	L	Н
L	Н	L
Н	L	L
Н	Н	L

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2.0~5.5	V
Input voltage	V _{IN}	0~5.5	V
Output voltage	V _{OUT}	0~V _{CC}	V
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~100 (V_{CC} = 3.3 \pm 0.3 V)	ns/V
	ui/uv	0~20 (V_{CC} = 5 \pm 0.5 V)	115/ V

Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Circuit		Test				Ta = 25°C			Ta = -40~85°C		
		Test Condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit	
High-level input	High lovel input				2.0	1.50		_	1.50	_	
voltage	VIH	—		_	3.0~ 5.5	V _{CC} × 0.7	_	_	V _{CC} × 0.7		V
Low-level input					2.0			0.50		0.50	
voltage	VIL	—		_	3.0~ 5.5	_	_	V _{CC} × 0.3	_	V _{CC} × 0.3	V
				I _{OH} = -50 μA	2.0	1.9	2.0		1.9	_	V
			V _{IN} = V _{IL}		3.0	2.9	3.0		2.9	_	
High-level output voltage	V _{OH}				4.5	4.4	4.5	_	4.4	_	
				I _{OH} = -4 mA	3.0	2.58	_		2.48	_	
				I _{OH} = -8 mA	4.5	3.94	_		3.80	_	
				I _{OL} = 50 μA	2.0	_	0.0	0.1	_	0.1	
					3.0	_	0.0	0.1	_	0.1	
Low-level output voltage V _{OL}	—	V _{IN} = V _{IH} or VIL		4.5	_	0.0	0.1	_	0.1	V	
			0	$I_{OL} = 4 \text{ mA}$	3.0	_		0.36		0.44	
				I _{OL} = 8 mA	4.5	_		0.36		0.44	
Input leakage current	I _{IN}	_	V _{IN} = 5.5 V or GND		0~ 5.5	—	_	±0.1	_	±1.0	μΑ
Quiescent supply current	Icc	—	V _{IN} = V _{CC} or GND		5.5	—	_	2.0	_	20.0	μΑ

AC Characteristics (Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics Symbol	Symbol	Test	٦	Test Condition		Ta = 25°C			Ta = -40~85°C		Unit
	Circuit		V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Onit	
Propagation delay ^t pLH time ^t pHL			_	3.3 ± 0.3	15		5.6	7.9	1.0	9.5	ns
	t _{pLH}				50		8.1	11.4	1.0	13.0	
	t _{pHL}			5.0 ± 0.5	15		3.6	5.5	1.0	6.5	
					50		5.1	7.5	1.0	8.5	
Input capacitance	C _{IN}			—			4	10		10	pF
Power dissipation capacitance	C _{PD}				(Note)		15			_	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

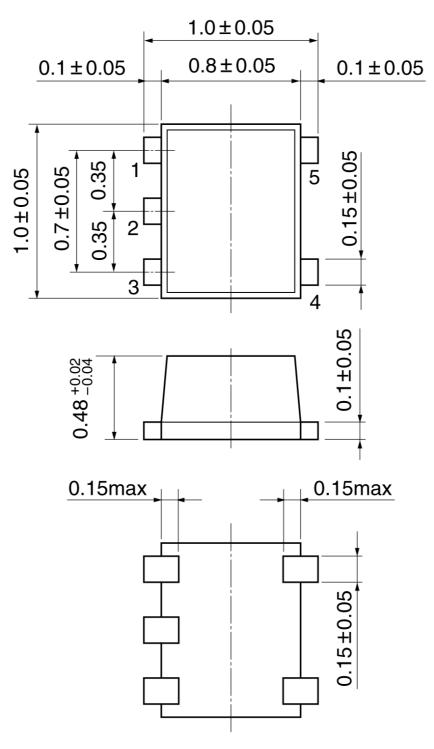
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

TOSHIBA

Package Dimensions

SON5-P-0.35

Unit:mm



Weight: 0.001 g (typ.)

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Handbook" etc..

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