Old Company Name in Catalogs and Other Documents

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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)
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DATA SHEET

MOS FIELD EFFECT POWER TRANSISTOR

Phase-out/Discontinued 2SJ132,132-Z

P-CHANNEL POWER MOSFET FOR SWITCHING

FEATURES

- Gate drive available at logic level (V_{GS} = -4 V)
- High current control available in small dimension due to low $R_{DS(on)}$ ($\cong 0.25 \Omega$)
- 2SJ132-Z is a lead process product and is ideal for mounting a hybrid IC.

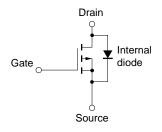
ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Parameter	Symbol	Conditions	Ratings	Unit
Drain to source voltage	VDSS	V _{GS} = 0 V	-30	V
Gate to source voltage	V _{GSS}	V _{DS} = 0 V	∓20	V
Drain current (DC)	I _{D(DC)}	Tc = 25°C	∓2.0	Α
Drain current (pulse)	I _{D(pulse)}	PW ≤ 300 <i>µ</i> s duty cycle ≤ 10%	∓8.0	А
Total power dissipation	P _{T1}	Tc = 25°C	20	W
Total power dissipation	P _{T2}	T _A = 25°C	1.0 Note 1, 2.0 Note 2	W
Channel temperature	Tch		150	°C
Storage temperature	Tstg		–55 to +150	°C

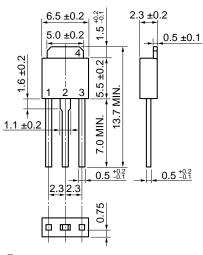
Note 1. Printing board mounted

2. $7.5 \text{ cm}^2 \times 0.7 \text{ mm}$ ceramic board mounted

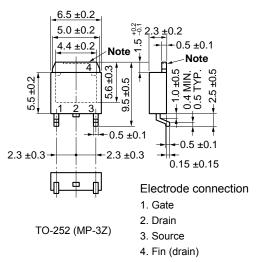
EQUIVALENT CIRCUIT



PACKAGE DRAWING (UNIT: mm)



<R> TO-251 (MP-3)



Note The depth of notch at the top of the fin is from 0 to 0.2 mm.

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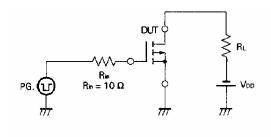


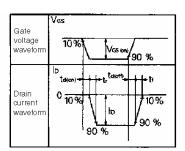


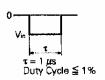
ELECTRICAL CHARACTERISTICS (TA = 25°C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Drain cutoff current	IDSS	V _{DS} = -30 V, V _{GS} = 0 V			-10	μΑ
Gate cutoff current	Igss	V _{GS} = ∓20 V, V _{DS} = 0 V			∓100	nA
Gate cutoff voltage	V _{GS(off)}	$V_{DS} = -10 \text{ V}, I_{D} = -1.0 \text{ mA}$	-1.0	-2.0	-3.0	V
Forward transfer admittance	y _{ts}	V _{DS} = -10 V, I _D = -1.0 A	1.0	1.8		S
Drain to source on-state resistance	RDS(on)1	V _G S = -10 V, I _D = -1.0 A		0.25	0.4	Ω
Drain to source on-state resistance	RDS(on)2	V _{GS} = -4 V, I _D = -0.8 A		0.4	0.6	Ω
Input capacitance	Ciss	V _{DS} = -10 V, V _{GS} = 0 V f = 1 MHz		730		pF
Output capacitance	Coss			180		pF
Reverse transfer capacitance	Crss			45		pF
Turn-on delay time	t _{d(on)}	$I_D = -1.0 \text{ A, } V_{\text{GS(on)}} = -10 \text{ V}$ $V_{DD} \cong -15 \text{ V, } R_L = 50 \Omega,$ $R_{\text{in}} = 10 \Omega$		30		ns
Rise time	t _t			30		ns
Turn-off delay time	t _{d(off)}			110		ns
Fall time	tf			40		ns

SWITCHING TIME TEST CIRCUIT, TEST CONDITION (RESISTANCE LOAD)

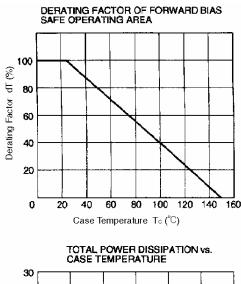


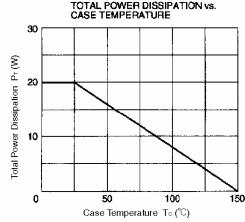


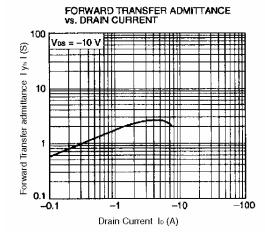


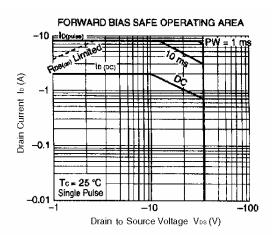


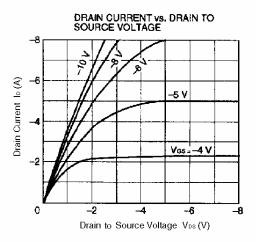
TYPICAL CHARACTERISTICS (TA = 25°C)

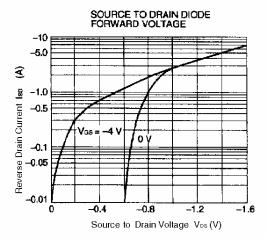






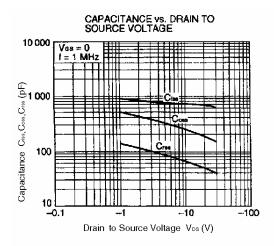


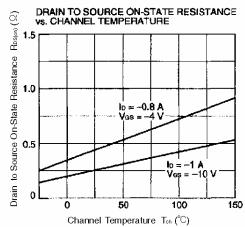


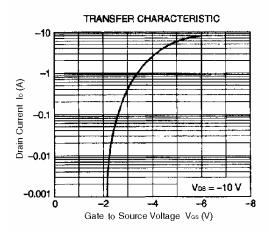


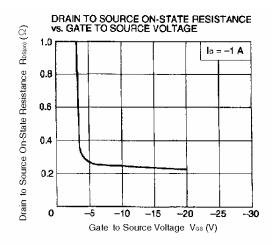


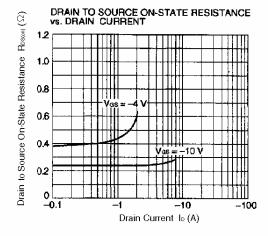
Phase-out/Discontinued

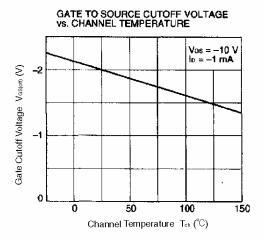




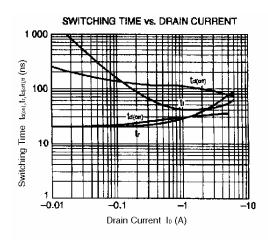


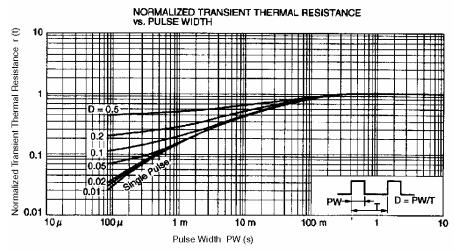
















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