



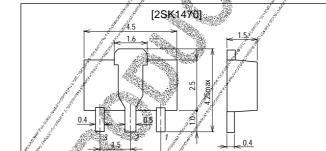
Ultrahigh-Speed Switching Applications

Features

- · Low ON resistance.
- · Ultrahigh-speed switching.
- \cdot Low-voltage drive.

Package Dimensions

unit:mm 2062A



1 : Gate 2 : Drain

3 : Source

SANYO: PCP

Specifications

Absolute Maximum Ratings at Ta = 25°C

| Parameter | Symbol | Ratings | Unit |
|-----------------------------|---|-------------|------|
| Drain-to-Source Voltage | V _{D\$\$} | 60 | V |
| Gate-to-Source Voltage | V _G SS // | ±15 | V |
| Drain Current (DC) | / /6 | 2 | Α |
| Drain Current (pulse) | / I _{DP} PW≤10us, duty cycle≤1% | 8 | Α |
| Allowable Power Dissipation | Tc=25°C | 3.5 | W |
| | Mounted on a ceramic board (250mm²x0.8mm) | 1.5 | W |
| Channel Temperature | / Tch | 150 | °C |
| Storage Temperature | Tstg // | -55 to +150 | °C |

Electrical Characteristics at Ta = 25°C

| Parameter | Symbol Conditions | Conditions | | Ratings | | Unit |
|--|-----------------------------------|--|-----|---------|------|------|
| 1 diameter | Oymboi S. | Conditions | min | typ | max | Onit |
| Drain-to-Source Breakdown Voltage | V(BR)DSS | lo=1mA, V _{GS} =0 | 60 | | | V |
| Zero-Gate Votlage Drain/Current | I _{DSS} / _/ / | V _{DS} =60V, V _{GS} =0 | | | 100 | μA |
| Gate-to-Source Leakage Current | I _{GSS} | $V_{GS}=\pm 12V$, $V_{DS}=0$ | | | ±10 | μA |
| Cutoff Voltage | VGS(off) | $V_{DS}=10V$, $I_{D}=1mA$ | 1.0 | | 2.0 | V |
| Forward Transfer Admittance | / l yfs | $V_{DS}=10V$, $I_{D}=1A$ | 1.2 | 2.0 | | S |
| Static Drain-to-Source On State Resistance | R _{DS(on)} 1 | I _D =1A, V _{GS} =10V | | 0.35 | 0.45 | Ω |
| Static Drain-to-Gource Orestate Resistance | R _{DS(on)} 2 | I_{D} =1A, V_{GS} =4V | | 0.45 | 0.6 | Ω |

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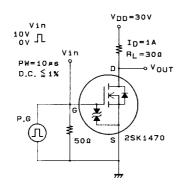
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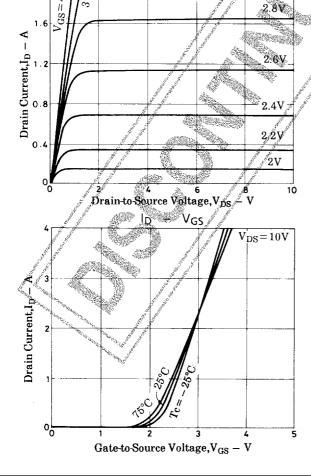
| Parameter | Symbol | Conditions | | Ratings | | Unit |
|------------------------------|----------------------|--|--|---------|-----------------------------|------|
| Input Capacitance | Ciss | V _{DS} =20V, f=1MHz | | 150 | | pF |
| Output Capacitance | Coss | V _{DS} =20V, f=1MHz | | 60 | | pF |
| Reverse Transfer Capacitance | Crss | V _{DS} =20V, f=1MHz | | 12 | | pF |
| Turn-ON Delay Time | t _d (on) | See specified Test Circuit | | 6 | | ns |
| Rise Time | t _r | See specified Test Circuit | A State of S | 10 | | ns |
| Turn-OFF Delay Time | t _d (off) | See specified Test Circuit | P F | .60 | eraja sa | ns |
| Fall Time | t _f | See specified Test Circuit | P A | 20 | Cathalline and Market State | ns |
| Diode Forward Voltage | V _{SD} | I _S =2A, V _{GS} =0 | 40 | 1.0 | All Marie Land | V |

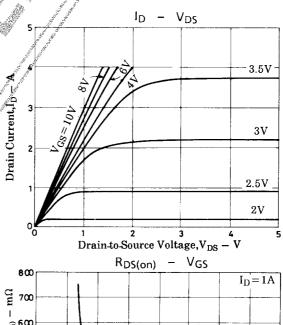
Switching Time Test Circuit

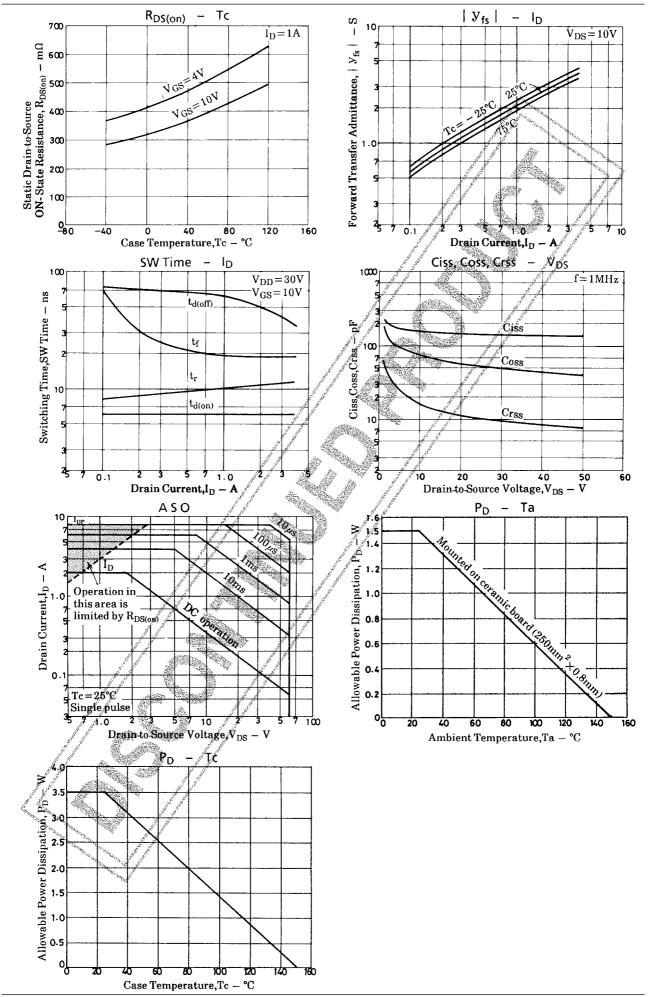
2.0

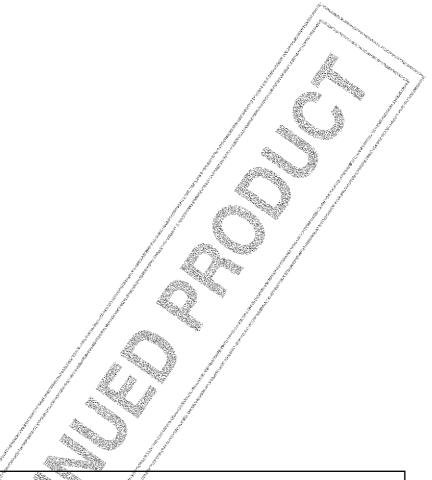


ID - VDS









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